# Latency-Aware Neural Architecture Search with Multi-Objective Bayesian Optimization

### Latency-Aware Neural Architecture Search (NAS)

**Problem**: Deploying models on end user devices such as mobile phones requires low-latency and accurate predictive models

**Goal**: Provide an automated framework for identifying neural architectures that are optimal with respect to accuracy and on-device prediction latency

## NAS Methodology

- Pre-defined search space (i.e., # of layers, kernel shape)
- Search engine with a user-specified search strategy (i.e., multiobjective Bayesian Optimization)
- Selected architecture is trained/evaluated and deployed to obtain actual inference latency
  - Inference latency is benchmarked on a tier-1 Android device with arm64 kernel through PyTorch lite interpreter
  - Inference latency is defined as the time when the input data becomes available to the time the model generates the final outputs
- Accuracy/latency results are fed back to the search engine
- Depending on the objective, the search engine then selects the next candidate for evaluation
- Process is repeated until either the maximum number of iteration is reached or the final objective converges





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Leave-one-out cross-validation comparison for SAAS [2] and MAP using 100 training configurations. Using the SAAS prior provides good fits for both objectives while MAP estimation is unable to provide accurate model fits

(Left) Sobol search is only able to find two points that improve upon the reference point. (Right) BO is able to successfully