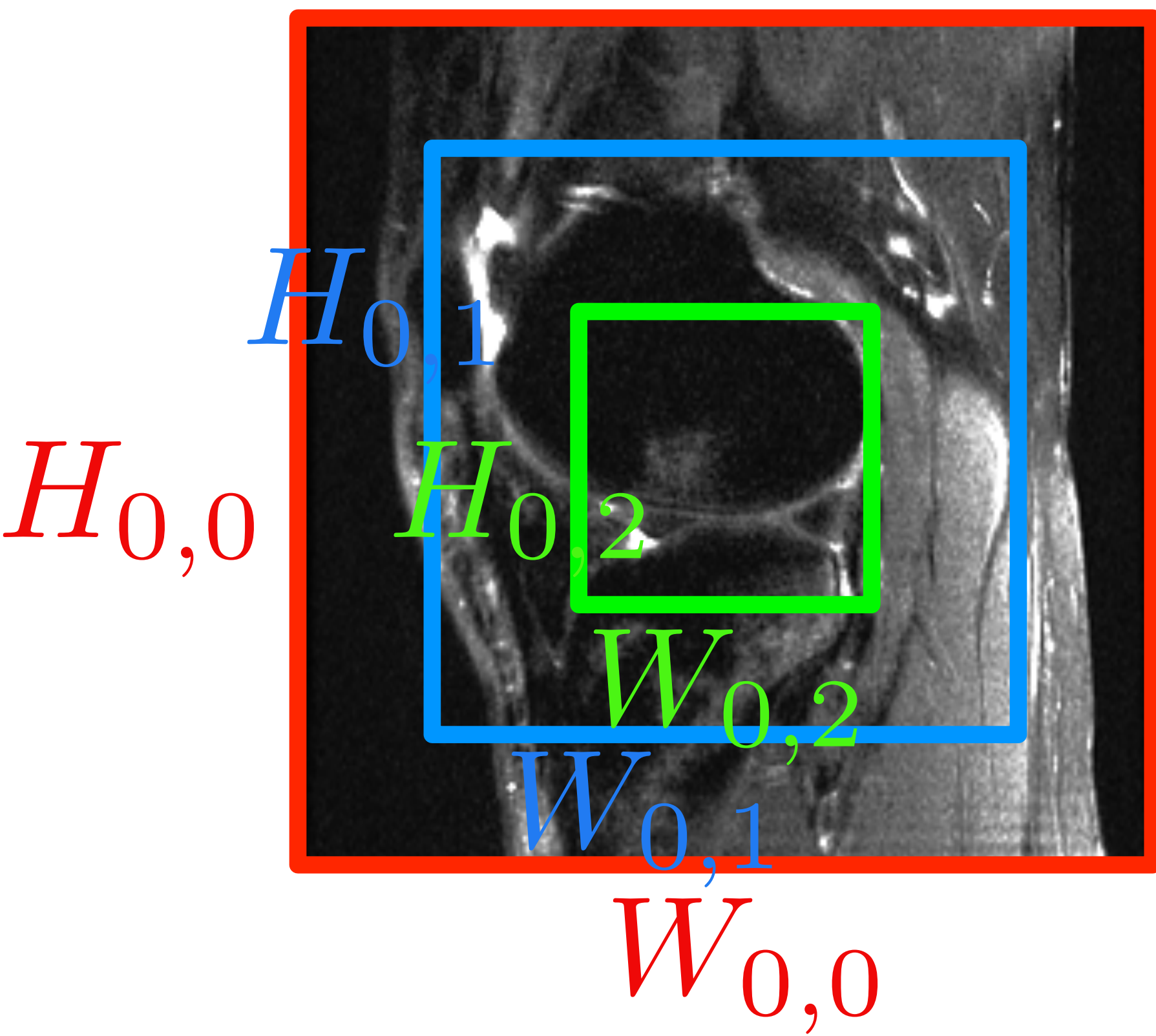
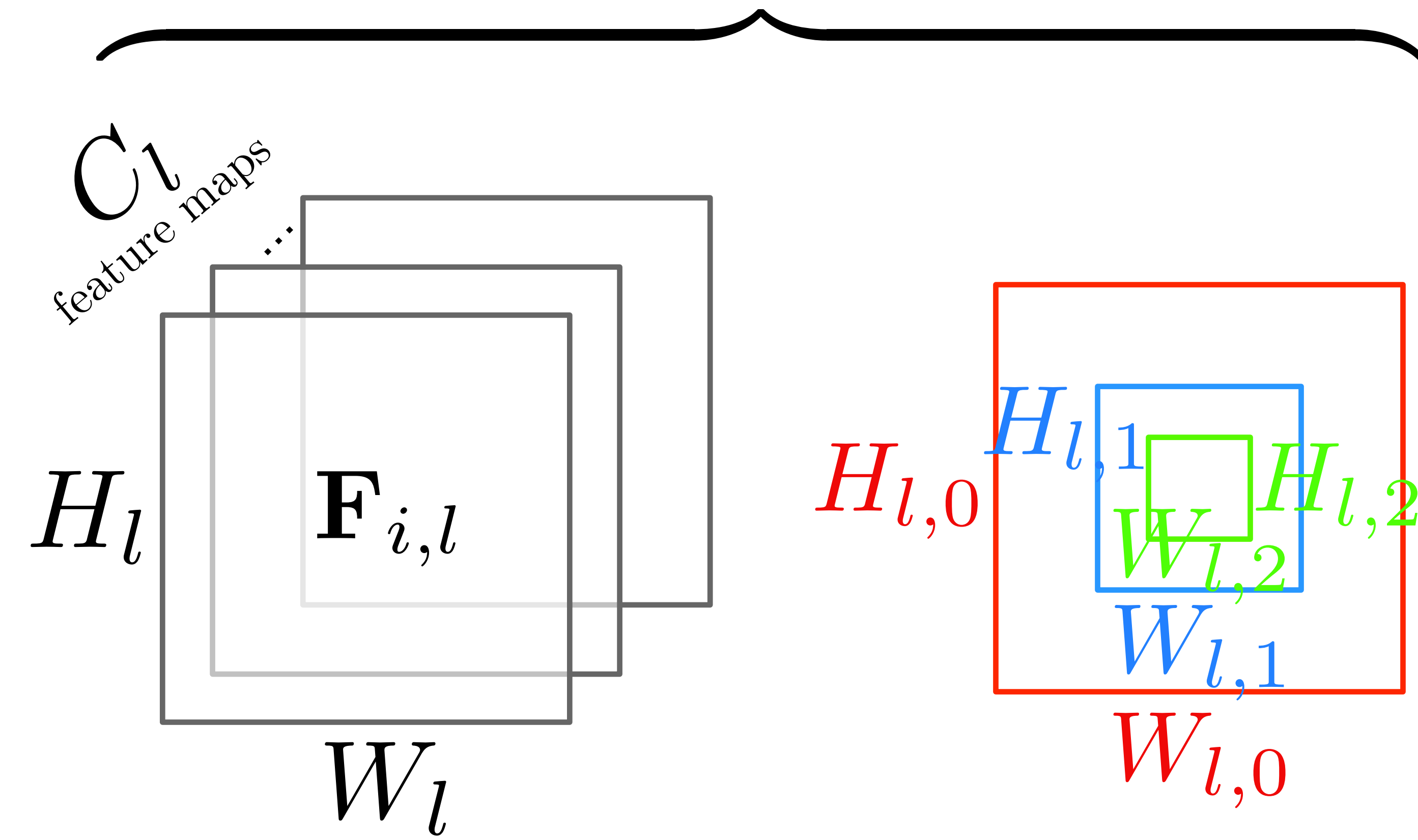


Slice-level
sub-regions



PDP inputs



PDP processing

