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21 A Task descriptions

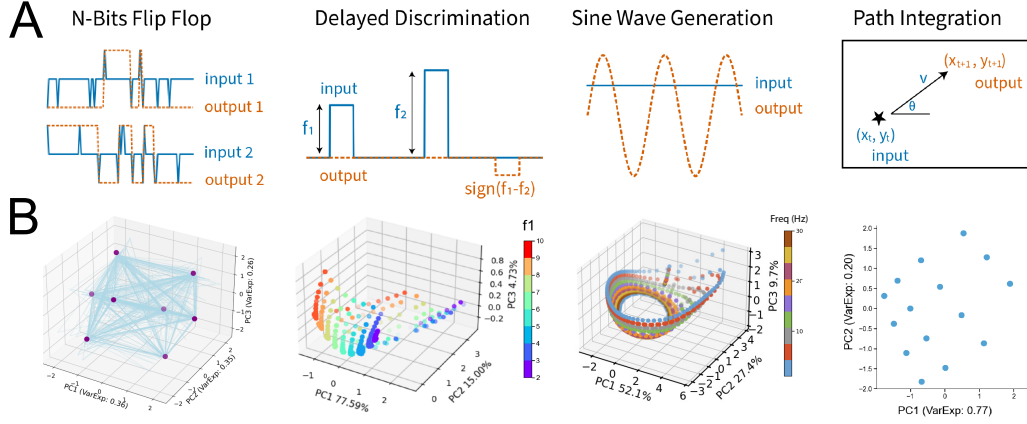


Figure 1: **Our task suite spans memory, integration, pattern generation, and decision-making.** Each task is designed to place distinct demands on the network’s dynamics. **N-Bit Flip-Flop:** The network must remember the last nonzero input on each of N independent channels. **Delayed Discrimination:** The network compares the magnitude of two pulses, separated by a variable delay, and outputs their sign difference. **Sine Wave Generation:** A static input specifies a target frequency, and the network generates the corresponding sine wave over time. **Path Integration:** The network integrates velocity inputs to track position in a bounded 2D or 3D arena (schematic shows 2D case).

22 **N-Bit Flip-Flop Task** Each RNN receives N independent input channels taking values in
 23 $\{-1, 0, +1\}$, which switch with probability p_{switch} . The network has N output channels that must
 24 retain the most recent nonzero input on their respective channels. The network dynamics form 2^N
 25 fixed points, corresponding to all binary combinations of $\{-1, +1\}^N$.

26 **Delayed Discrimination Task** The network receives two pulses of amplitudes $f_1, f_2 \in [2, 10]$,
 27 separated by a variable delay $t \in [5, 20]$ time steps, and must output $\text{sign}(f_2 - f_1)$. In the N -
 28 channel variant, comparisons are made independently across channels. The network forms task-
 29 relevant fixed points to retain the amplitude of f_1 during the delay period.

30 **Sine Wave Generation** The network receives a static input specifying a target frequency $f \in [1, 30]$
 31 and must generate the corresponding sine wave $\sin(2\pi ft)$ over time. We define N_{freq} target fre-
 32 quencies, evenly spaced within the range $[1, 30]$, and use them during training. In the N -channel
 33 variant, each input channel specifies a frequency, and the corresponding output channel generates
 34 a sine wave at that frequency. For each frequency, the network dynamics form and traverse a limit
 35 cycle that produces the corresponding sine wave.

36 **Path Integration Task** Starting from a random position in 2D, the network receives angular direc-
 37 tion θ and speed v at each time step and updates its position estimate. In the 3D variant, the network
 38 takes as input azimuth θ , elevation ϕ , and speed v , and outputs updated (x, y, z) position. The net-
 39 work performs path integration by accumulating velocity vectors based on the input directions and
 40 speeds. After training, the network forms a Euclidean map of the environment in its internal state
 41 space.

42 B Additional axes of task complexity

43 In the main text, we controlled task complexity by varying the number of independent input–output
 44 channels, effectively duplicating the task across dimensions. Here, we explore two alternative ap-
 45 proaches: increasing the task’s memory demand and adding auxiliary objectives.

46 **Changing memory demand.** Of the four tasks, only Delayed Discrimination requires extended
 47 memory, as its performance depends on maintaining the first stimulus across a variable delay. See
 48 Appendix H for a quantification of each task’s memory demand. We increased the memory load
 49 in Delayed Discrimination by lengthening the delay period. This manipulation reduced degeneracy

at the dynamical and behavioral levels but increased it at the weight level, mirroring the effect of increasing task dimensionality (Figure 2A).

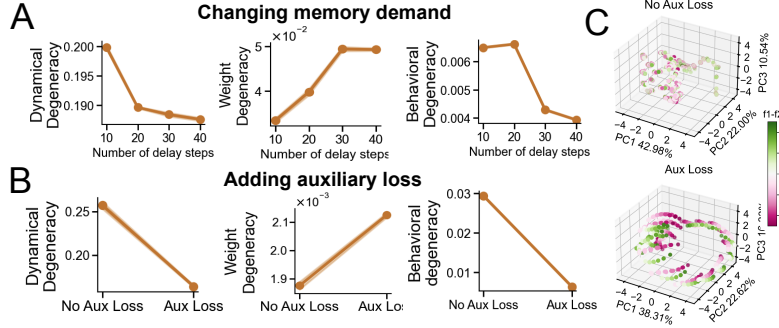


Figure 2: Memory demand and auxiliary loss modulate degeneracy in distinct ways. In the Delayed Discrimination task, both manipulations reduce dynamical and behavioral degeneracy while increasing weight degeneracy. The auxiliary loss also induces additional line attractors in the network’s dynamics, as shown in (C).

Adding auxiliary loss. We next examined how adding an auxiliary loss affects solution degeneracy in the Delayed Discrimination task. Specifically, the network outputs both the sign and the magnitude of the difference between two stimulus values ($f_2 - f_1$), using separate output channels for each. This manipulation added a second output channel and increased memory demand by requiring the network to track the magnitude of the difference between incoming stimuli. Consistent with our hypothesis, this manipulation reduced dynamical and behavioral degeneracy while increasing weight degeneracy (Figure 2B). Crucially, the auxiliary loss induced additional line attractors in the network dynamics, further structuring internal trajectories and aligning neural responses across networks (Figure 2C). While the auxiliary loss increases both output dimensionality and temporal memory demand, we interpret its effect holistically as a structured increase in task complexity.

C Higher task complexity induces more feature learning

We hypothesize that the increased weight degeneracy observed in harder tasks reflects stronger feature learning. Specifically, harder tasks may force network weights to travel farther from their initialization. If more complex task variants, like those in Section ??, truly induce greater feature learning, then networks should traverse a greater distance in weight space, resulting in more dispersed final weights. To test this idea, we measured feature learning strength in networks trained on different task variants using two complementary metrics [Liu et al., 2023, George et al., 2022]: **Weight-change norm:** $\|\mathbf{W}_T - \mathbf{W}_0\|_F$, where larger values indicate stronger feature learning. **Kernel alignment (KA):** measures the directional change of the neural tangent kernel (NTK) before and after training:

$$\text{KA}(K^{(f)}, K^{(0)}) = \frac{\text{Tr}(K^{(f)} K^{(0)})}{\|K^{(f)}\|_F \|K^{(0)}\|_F}, \text{ where } K = \nabla_W \hat{y}^\top \nabla_W \hat{y}. \text{ Lower KA indicates greater NTK rotation and thus stronger feature learning.}$$

More complex tasks consistently drive stronger feature learning and greater dispersion in weight space, as reflected by increasing weight-change norm and decreasing kernel alignment across all tasks (Figure 3).

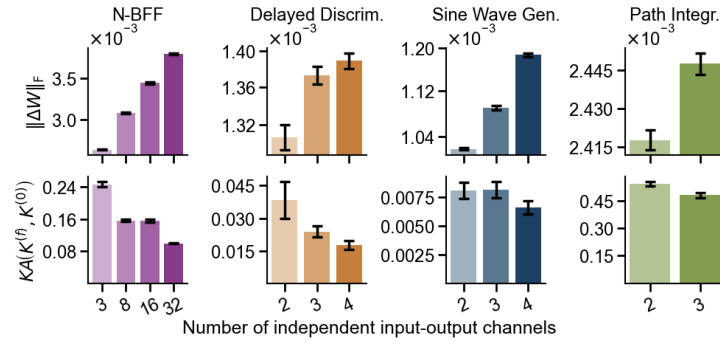


Figure 3: **More complex tasks drive stronger feature learning in RNNs.** Increased input–output dimensionality leads to higher weight-change norms and lower kernel alignment. Error bars indicate ± 1 standard error.

76 D Feature learning effect for all tasks

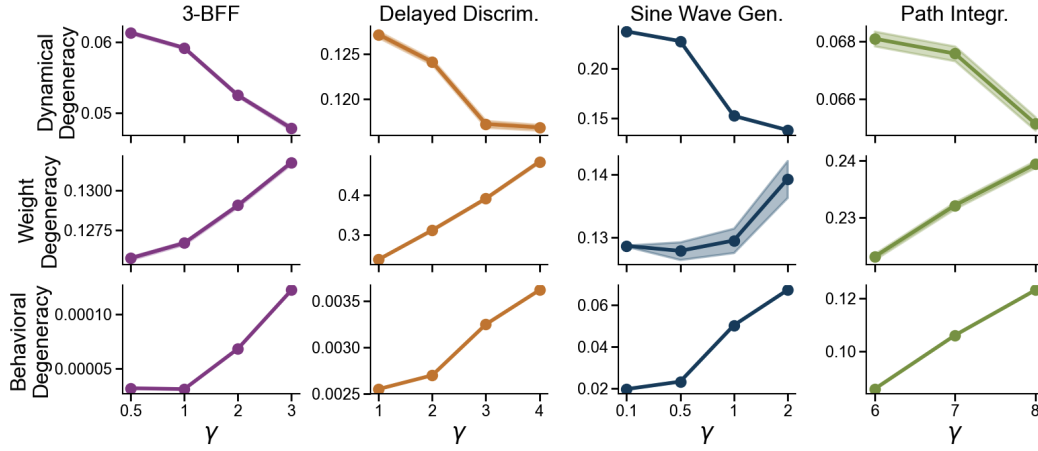


Figure 4: **Stronger feature learning reduces dynamical degeneracy but increases weight and behavioral degeneracy.** Panels show degeneracy at the dynamical, weight, and behavioral levels (top to bottom). Shaded area indicates ± 1 standard error.

77 E Network size effect for all tasks

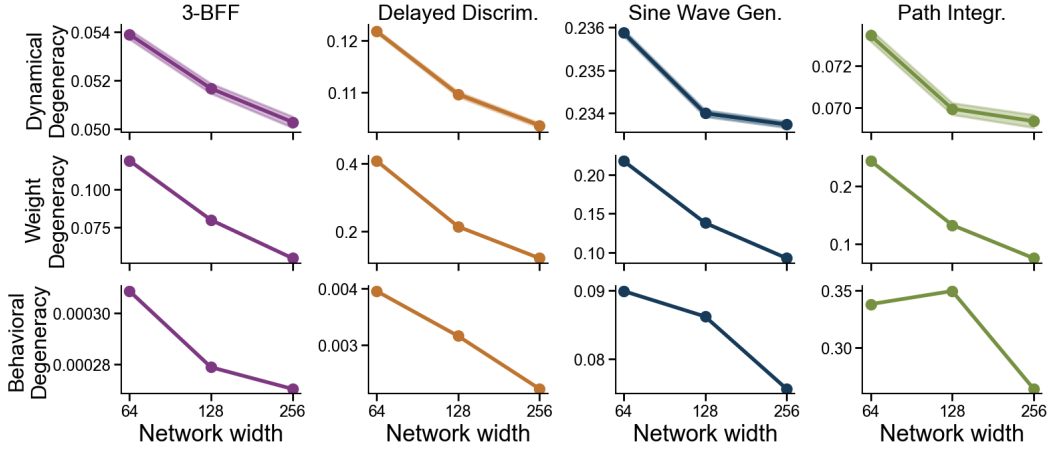


Figure 5: **Larger networks reduce degeneracy across weight, dynamics, and behavior.** Controlling for feature learning strength, wider RNNs yield more consistent solutions across all three levels of analysis. Panels show degeneracy at the dynamical, weight, and behavioral levels (top to bottom). Shaded area indicates ± 1 standard error.

78 F Task details

79 F.1 N-Bit Flip Flop

Task Parameter	Value
Probability of flip	0.3
Number of time steps	100

Task Parameter	Value
Number of time steps	60
Max delay	20
Lowest stimulus value	2
Highest stimulus value	10

80 F.2 Delayed Discrimination

81 F.3 Sine Wave Generation

Task Parameter	Value
Number of time steps	100
Time step size	0.01
Lowest frequency	1
Highest frequency	30
Number of frequencies	100

82 F.4 Path Integration

Task Parameter	Value
Number of time steps	100
Maximum speed ($v_{\max}$)	0.4
Direction increment std ($\theta_{\text{std}} / \phi_{\text{std}}$)	$\pi/10$
Speed increment std	0.1
Noise std	0.0001
Mean stop duration	30
Mean go duration	50
Environment size (per side)	10

83 G Training details

84 G.1 N-Bit Flip Flop

Training Hyperparameter	Value
Optimizer	Adam
Learning rate	0.001
Learning rate scheduler	None
Max epochs	300
Steps per epoch	128
Batch size	256
Early stopping threshold	0.001
Patience	3
Time constant (μP)	1

85 G.2 Delayed Discrimination

Training Hyperparameter	Value
Optimizer	Adam
Learning rate	0.001
Learning rate scheduler	CosineAnnealingWarmRestarts
Max epochs	500
Steps per epoch	128
Batch size	256
Early stopping threshold	0.01
Patience	3
Time constant (μP)	0.1

86 G.3 Sine Wave Generation

Training Hyperparameter	Value
Optimizer	Adam
Learning rate	0.0005
Learning rate scheduler	None
Max epochs	500
Steps per epoch	128
Batch size	32
Early stopping threshold	0.05
Patience	3
Time constant (μP)	1

87 G.4 Path Integration

Training Hyperparameter	Value
Optimizer	Adam
Learning rate	0.001
Learning rate scheduler	ReduceLROnPlateau
Learning rate decay factor	0.5
Learning rate decay patience	40
Max epochs	1000
Steps per epoch	128
Batch size	64
Early stopping threshold	0.05
Patience	3
Time constant (μP)	0.1

88 H Memory demand of each task

89 In this section, we quantify each task’s memory demand by measuring how far back in time its inputs
90 influence the next output. Specifically, for each candidate history length h , we build feature vectors

$$\mathbf{s}_t^{(h)} = [x_{t-h+1}, \dots, x_t; y_t] \in \mathbb{R}^{h d_{\text{in}} + d_{\text{out}}},$$

91 and **train a two-layer MLP to predict the subsequent target** y_{t+1} . We then evaluate the held-
92 out mean-squared error $\text{MSE}(h)$, averaged over multiple random initializations. We identify the

93 smallest history length h^* at which the error curve plateaus or has a minimum, and take h^* as the
 94 task’s intrinsic memory demand.

95 From the results, we can see that the N-Bits Flip-Flop task requires only one time-step of mem-
 96 ory—exactly what’s needed to recall the most recent nonzero input in each channel. The Sine Wave
 97 Generation task demands two time-steps, reflecting the need to track both phase and direction of
 98 change. Path Integration likewise only needs one time-step, since the current position plus instan-
 99 taneous velocity and heading suffice to predict the next position. Delayed Discrimination is the only
 100 memory-intensive task: our method estimates a memory demand of 25 time-steps, which happens
 101 to be the time interval between the offset of the first stimulus and the onset of the response period,
 102 during which the network needs to first keep track of the amplitude of the first stimulus and then its
 103 decision.

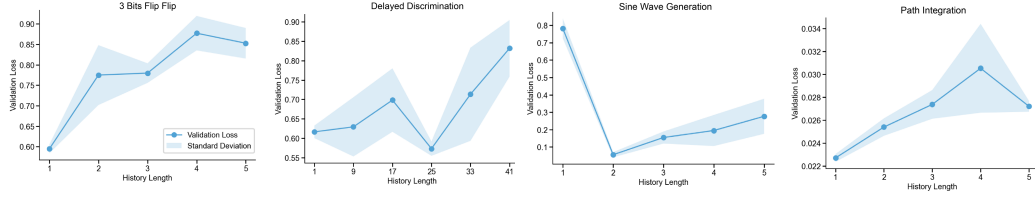


Figure 6: **Memory demand of each task.** The held-out mean-squared error $\text{MSE}(h)$ of a two-layer MLP predictor is plotted against history length h . The intrinsic memory demand h^* , defined by the plateau or minimum of each curve, is 1 for the N-Bits Flip-Flop and Path Integration tasks, 2 for Sine Wave Generation, and 25 for Delayed Discrimination—matching the inter-stimulus delay interval in that task.

104 I More details on the degeneracy metrics

105 I.1 Dynamical Degeneracy

106 Briefly, DSA proceeds as follows: Given two RNNs with hidden states $\mathbf{h}_1(t) \in \mathbb{R}^n$ and $\mathbf{h}_2(t) \in \mathbb{R}^n$,
 107 we first generate a delay-embedded matrix, $\mathbf{H}_1$ and $\mathbf{H}_2$ of the hidden states in their original state
 108 space. Next, for each delay-embedded matrix, we use Dynamic Mode Decomposition (DMD)
 109 [Schmid, 2022] to extract linear forward operators $\mathbf{A}_1$ and $\mathbf{A}_2$ of the two systems’ dynamics. Fi-
 110 nally, a Procrustes distance between the two matrices $\mathbf{A}_1$ and $\mathbf{A}_2$ is used to quantify the dissimilarity
 111 between the two dynamical systems and provide an overall DSA score, defined as:

$$d_{\text{Procrustes}}(\mathbf{A}_1, \mathbf{A}_2) = \min_{\mathbf{Q} \in O(n)} \|\mathbf{A}_1 - \mathbf{Q}\mathbf{A}_2\mathbf{Q}^{-1}\|_F$$

112 where $\mathbf{Q}$ is a rotation matrix from the orthogonal group $O(n)$ and $\|\cdot\|_F$ is the Frobenius norm. This
 113 metric quantifies how dissimilar the dynamics of the two RNNs are after accounting for orthogonal
 114 transformations. We quantify Dynamical Degeneracy across many RNNs as the average pairwise
 115 distance between pairs of RNN neural-dynamics (hidden-state trajectories).

116 After training, we extract each network’s hidden-state activations for every trial in the training set,
 117 yielding a tensor of shape (trials $\times$ time steps $\times$ neurons). We collapse the first two dimensions and
 118 yield a matrix of size (trials $\times$ time steps) $\times$ neurons. We then apply PCA to retain the components
 119 that explain 99% of the variance to remove noisy and low-variance dimensions of the hidden state
 120 trajectories. Next, we perform a grid search over candidate delay lags, with a minimum lag of 1
 121 and a maximum lag of 30, selecting the lag that minimizes the reconstruction error of DSA on the
 122 dimensionality reduced trajectories. Finally, we fit DSA with full rank and the optimal lag to these
 123 PCA-projected trajectories and compute the pairwise DSA distances between all networks.

124 I.2 Weight degeneracy

125 We computed the pairwise distance between the recurrent matrices from different networks using
 126 Two-sided Permutation with One Transformation [Schönemann, 1966, Ding et al., 2008] function
 127 from the Procrustes Python package [Meng et al., 2022].

128 J Representational degeneracy

129 We further quantified solution degeneracy at the representational level—that is, the variability in
 130 each network’s internal feature space when presented with the same input dataset—using Singular
 131 Vector Canonical Correlation Analysis (SVCCA). SVCCA works by first applying singular value
 132 decomposition (SVD) to each network’s activation matrix, isolating the principal components that
 133 capture most of its variance, and then performing canonical correlation analysis (CCA) to find the
 134 maximally correlated directions between the two reduced subspaces. The resulting canonical cor-
 135 relations therefore measure how similarly two networks represent the same inputs: high average
 136 correlations imply low representational degeneracy (i.e., shared feature subspaces), whereas lower
 137 correlations reveal greater divergence in what the models learn. We define the representational de-
 138 generacy (labeled as the SVCCA distance below) as

$$d_{\text{repr}}(A_x, A_y) = 1 - \text{SVCCA}(A_x, A_y).$$

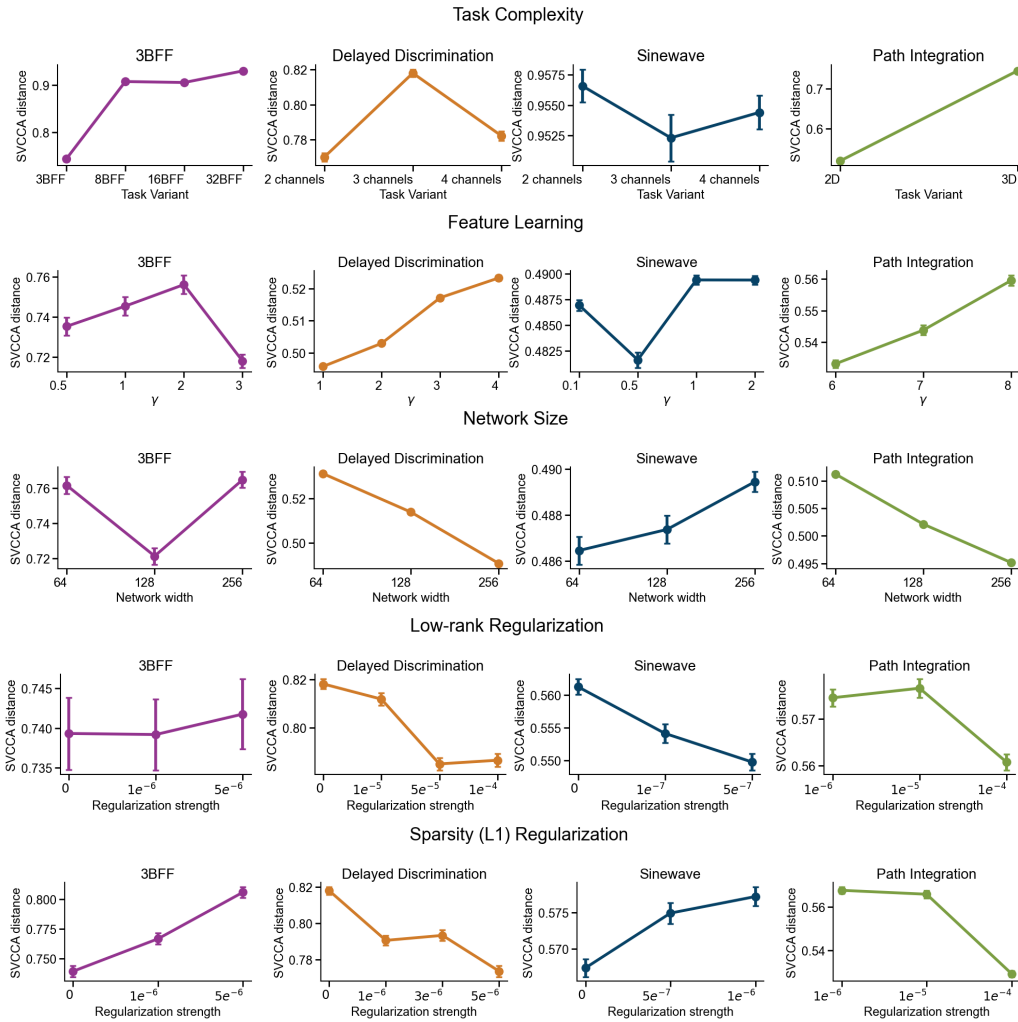


Figure 7: **Representational degeneracy, as measured by the average SVCCA distance between networks, does not necessarily change uniformly as we vary task complexity, feature learning strength, network size, and regularization strength.**

139 We found that as we vary the four factors that robustly control the dynamical degeneracy across task-
 140 trained RNNs, the representational-level degeneracy isn’t necessarily constrained by those same

141 factors in the same way. In RNNs, task-relevant computations are implemented at the level of network's dynamics instead of static representations, and RNNs that implement similar temporal dynamics can have disparate representational geometry. Therefore, it is expected that task complexity, learning regime, and network size change the task-relevant computations learned by the networks by affecting their neural dynamics instead of representations. DSA captures the dynamical aspect of the neural computation by fitting a forward operator matrix A that maps the network's activity at one time step to the next, therefore directly capturing the temporal evolution of neural activities. By contrast, SVCCA aligns the principal subspaces of activation vectors at each time point but treats those vectors as independent samples—it never examines how one state evolves into the next. As a result, SVCCA measures only static representational similarity and cannot account for the temporal dependencies that underlie RNN computations. Nonetheless, we expect SVCCA might be more helpful in measuring the solution degeneracy in feedforward networks.

153 **K Detailed characterization of OOD generalization performance**

154 In addition to showing the behavioral degeneracy in the main text, here we provide a more detailed characterization of the OOD behavior of networks by showing the mean versus standard deviation, and the distribution of the OOD losses.

157 **K.1 Changing task complexity**

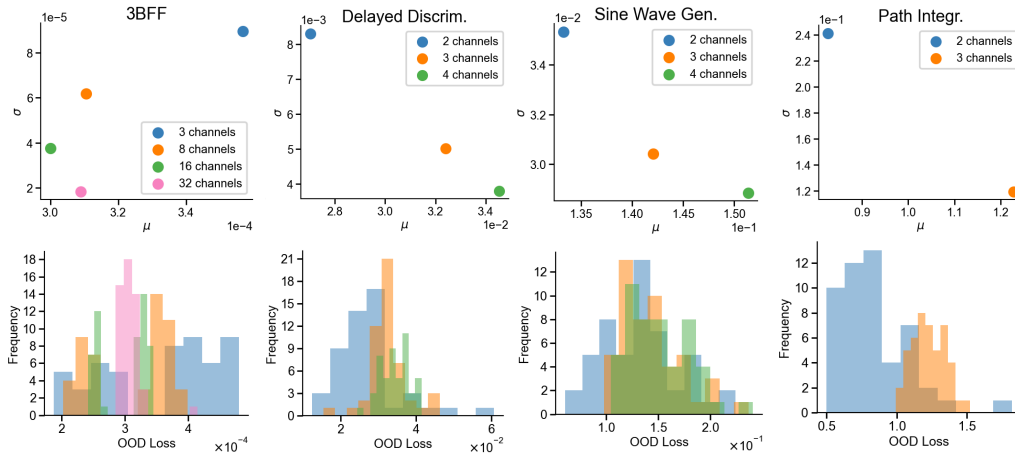


Figure 8: Detailed characterization of the OOD performance of networks while changing task complexity.

158 K.2 Changing feature learning strength

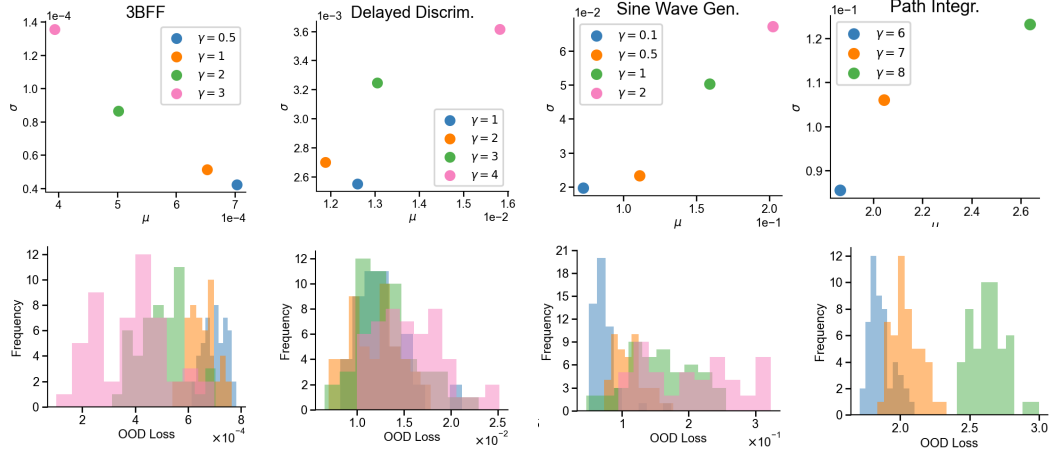


Figure 9: Detailed characterization of the OOD performance of networks while changing feature learning strength. Across Delayed Discrimination, Sine Wave Generation, and Path Integration tasks, networks trained with larger γ – and thus undergoing stronger feature learning – exhibit higher mean OOD generalization loss together with higher variability, potentially reflecting overfitting to the training task.

159 K.3 Changing network size

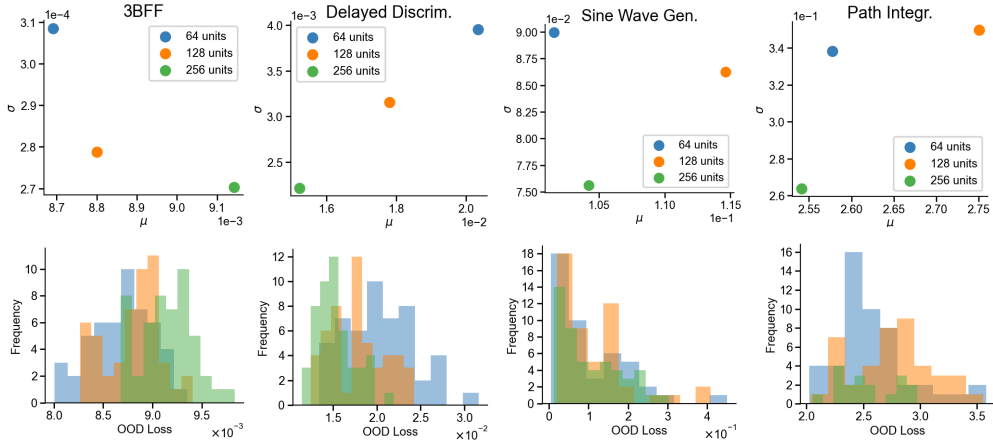


Figure 10: Detailed characterization of the OOD performance of networks while changing network size.

160 K.4 Changing regularization strength

161 K.4.1 Low-rank regularization

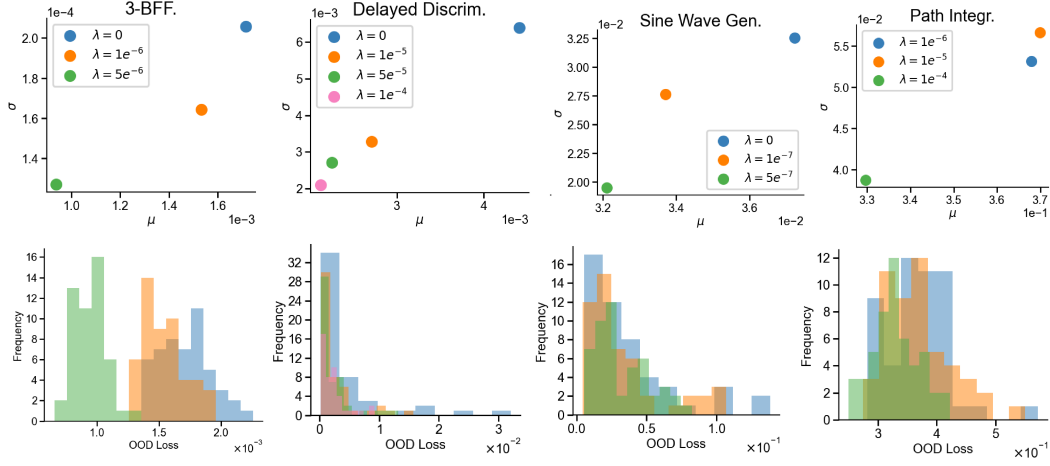


Figure 11: Detailed characterization of the OOD performance of networks while changing low-rank regularization strength.

162 K.4.2 Sparsity (L1) regularization

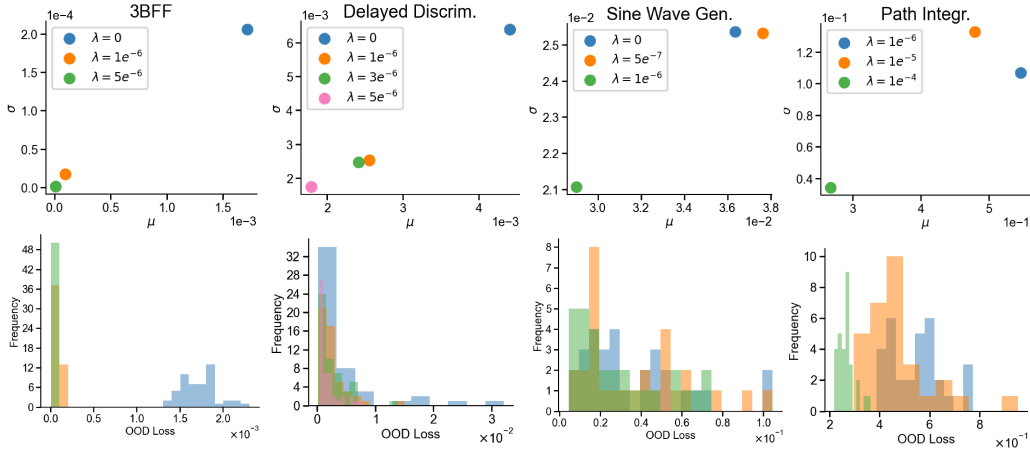


Figure 12: Detailed characterization of the OOD performance of networks while changing sparsity (L1) regularization strength.

163 L A short introduction to Maximal Update Parameterization (μP)

164 Under the NTK parametrization, as the network width goes to infinity, the network operates in the
 165 *lazy* regime, where its functional evolution is well-approximated by a first-order Taylor expansion
 166 around the initial parameters [Jacot et al., 2018, Lee et al., 2019, Chizat et al., 2019, Woodworth
 167 et al., 2020]. In this limit feature learning is suppressed and training dynamics are governed by the
 168 fixed Neural Tangent Kernel (NTK).

169 To preserve non-trivial feature learning at large width, the *Maximal Update Parametrization*
 170 (μP) rescales both the weight initialisation and the learning rate. μP keeps three quantities
 171 *width-invariant* at every layer—(i) the norm/variance of activations (ii) the norm/variance of the

172 gradients, and (iii) the parameter updates applied by the optimizer [Yang et al., 2022, 2023, Geiger
173 et al., 2020, Bordelon and Pehlevan, 2022].

174 For recurrent neural networks, under Stochastic Gradient Descent (SGD), the network output, ini-
175 tialization, and learning rates are scaled as

$$f = \frac{1}{\gamma_0 N} \vec{w} \cdot \phi(h), \quad (1)$$

$$\partial_t h = -h + \frac{1}{\sqrt{N}} J \phi(h), \quad J_{ij} \sim \mathcal{N}(0, 1), \quad (2)$$

$$\eta_{\text{SGD}} = \eta_0 \gamma_0^2 N. \quad (3)$$

176 Under Adam optimizer, the network output, initialization, and learning rates are scaled as

$$f = \frac{1}{\gamma_0 N} \vec{w} \cdot \phi(h), \quad (4)$$

$$\partial_t h = -h + \frac{1}{N} J \phi(h), \quad J_{ij} \sim \mathcal{N}(0, N), \quad (5)$$

$$\eta_{\text{Adam}} = \eta_0 \gamma_0. \quad (6)$$

177 M Theoretical relationship between parameterizations

178 We compare two RNN formalisms used in different parts of the main manuscript: a standard
179 discrete-time RNN trained with fixed learning rate and conventional initialization, and a μ P-style
180 RNN trained with leaky integrator dynamics and width-aware scaling.

181 In the standard discrete-time RNN, the hidden activations are updated as

$$h(t+1) = \phi(W_h h(t) + W_x x(t)),$$

182 In μ P RNNs, the hidden activations are updated as

$$h(t+1) - h(t) = \tau(-h(t) + \frac{1}{N} J \phi(h(t)) + Ux(t))$$

183 When $\tau = 1$,

$$h(t+1) - h(t) = -h(t) + \frac{1}{N} J \phi(h(t)) + Ux(t)$$

184

$$h(t+1) = \frac{1}{N} J \phi(h(t)) + Ux(t)$$

185 Aside from the overall scaling factor, the difference between the two parameterizations lies in the
186 placement of the non-linearity:

- 187 • **Standard RNN:** ϕ is applied *post-activation*, i.e. after the recurrent and input terms are
188 linearly combined,
- 189 • **μ P RNN:** ϕ is applied *pre-activation*; i.e. before the recurrent weight matrix, so the hidden
190 state is first non-linearized and then linearly combined

191 Miller and Fumarola [Miller and Fumarola, 2012] demonstrated that two classes of continuous-time
192 firing-rate models which differ in their placement of the non-linearity are mathematically equivalent
193 under a change of variables:

$$\text{v-model} \quad \tau \frac{dv}{dt} = -v + \tilde{I}(t) + W f(v)$$

$$\text{r-model:} \quad \tau \frac{dr}{dt} = -r + f(Wr + I(t))$$

194 with equivalence holding under the transformation $v(t) = Wr(t) + I(t)$ and $\tilde{I}(t) = I(t) + \tau \frac{dI}{dt}$,
 195 assuming matched initial conditions.

196 Briefly, they show that $Wr + I$ evolves according to the v -equation as follows:

$$\begin{aligned}
 v(t) &= Wr(t) + I(t) \\
 \frac{dv}{dt} &= \frac{d}{dt}(Wr(t) + I(t)) \\
 &= W \frac{dr}{dt} + \frac{dI}{dt} \\
 &= W \left(\frac{1}{\tau} (-r + f(Wr + I)) \right) + \frac{dI}{dt} \\
 \tau \frac{dv}{dt} &= -Wr + Wf(Wr + I) + \tau \frac{dI}{dt} \\
 &= -(v - I) + Wf(v) + \tau \frac{dI}{dt} \\
 &= -v + I + \tau \frac{dI}{dt} + Wf(v) \\
 \tau \frac{dv}{dt} &= -v + \tilde{I}(t) + Wf(v)
 \end{aligned}$$

197 This mapping applies directly to RNNs viewed as continuous-time dynamical systems and helps
 198 relate v-type μP -style RNNs to standard discrete-time RNNs. It suggests that the μP RNN (in v-
 199 type form) and the standard RNN (in r-type form) can be treated as different parameterizations of
 200 the same underlying dynamical system when:

- 201 • Initialization scales are matched
- 202 • The learning rate is scaled appropriately with γ
- 203 • Output weight norms are adjusted according to width

204 In summary, while a theoretical equivalence exists, it is contingent on consistent scaling across all
 205 components of the model. In this manuscript, we use the standard discrete-time RNNs due to its
 206 practical relevance for task-driven modeling community, while switching to μP to isolate the effect
 207 of feature learning and network size. Additionally, we confirm that the feature learning and network
 208 size effects on degeneracy hold qualitatively the same in standard discrete-time RNNs, unless where
 209 altering network width induces unstable and lazier learning in larger networks (Figure Q and R).

210 N Verifying larger γ reliably induces stronger feature learning in μP

211 In μP parameterization, the parameter γ interpolates between lazy training and rich, feature-learning
 212 dynamics, without itself being the absolute magnitude of feature learning. Here, we assess feature-
 213 learning strength in RNNs under varying γ using two complementary metrics:

214 **Weight-change norm** which measures the magnitude of weight change throughout training. A
 215 larger weight change norm indicates that the network undergoes richer learning or more feature
 216 learning.

$$\frac{\|\mathbf{W}_T - \mathbf{W}_0\|_F}{N},$$

217 where N is the number of parameters in the weight matrices being compared.

218 **Kernel alignment (KA)**, which measures the directional change of the neural tangent kernel (NTK)
 219 before and after training. A lower KA score corresponds to a larger NTK rotation and thus stronger
 220 feature learning.

$$\text{KA}(K^{(f)}, K^{(0)}) = \frac{\text{Tr}(K^{(f)} K^{(0)})}{\|K^{(f)}\|_F \|K^{(0)}\|_F}, \quad K = \nabla_W \hat{y}^\top \nabla_W \hat{y}.$$

221 We demonstrate that higher γ indeed amplifies feature learning inside the network.

222 N.1 N-BFF

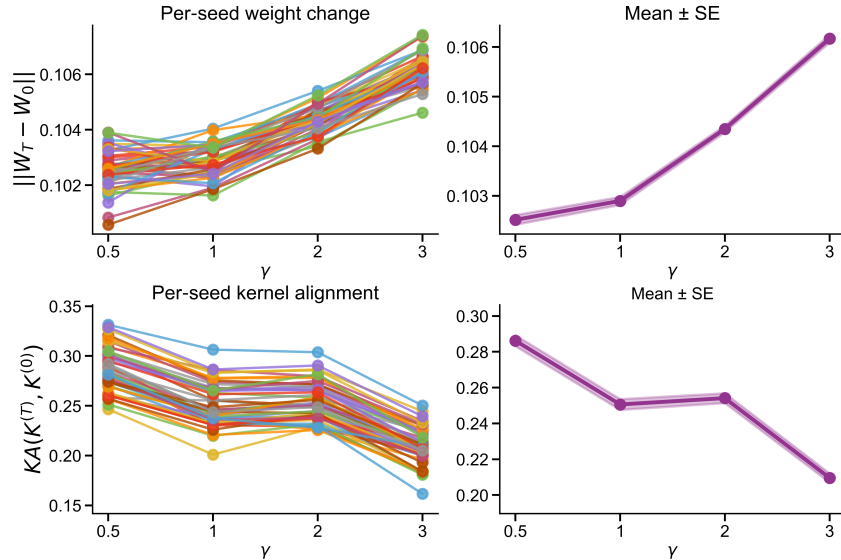


Figure 13: Weight change norm and kernel alignment for networks trained on the 3-Bits Flip Flop task as we vary γ . On the left panels, we show the per-seed metrics where connected dots of the same color are networks of identical initialization trained with different γ . On the right panels, we show the mean and standard error of the metrics across 50 networks. For larger γ , the weights move further from their initializations as shown by the larger weight change norm, and their NTK evolves more distinct from the network’s NTK at initialization as shown by the reduced KA. Both indicate stronger feature learning for networks trained under larger γ .

223 **N.2 Delayed Discrimination**

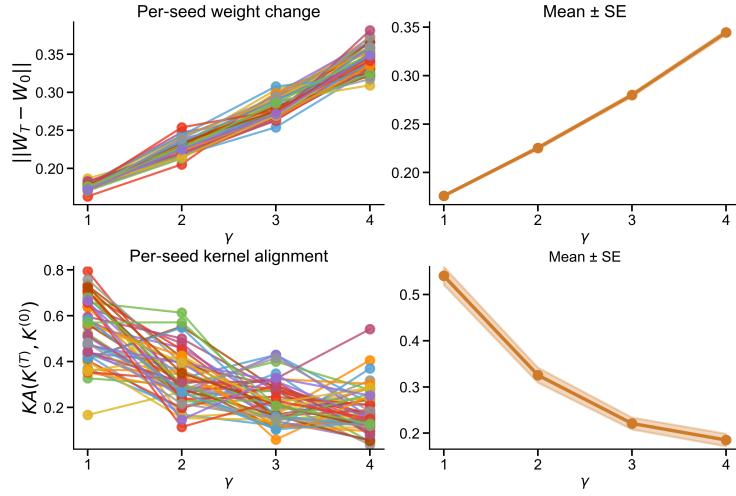


Figure 14: Stronger feature learning for networks trained under larger γ on the Delayed Discrimination task.

224 **N.3 Sine Wave Generation**

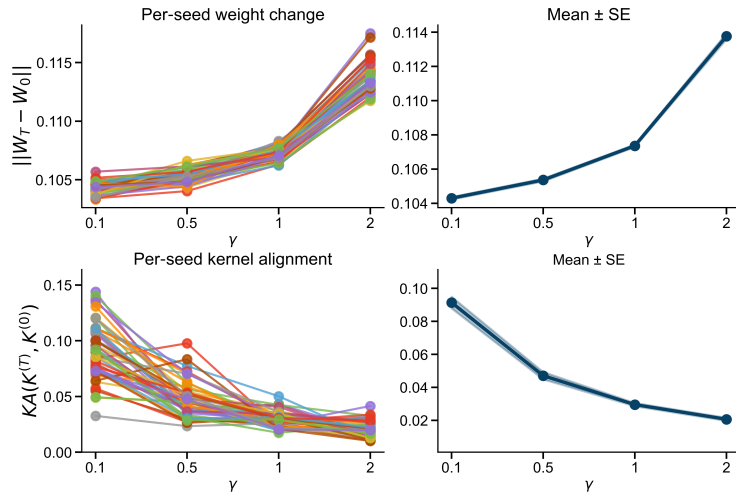


Figure 15: Stronger feature learning for networks trained under larger γ on the Sine Wave Generation task.

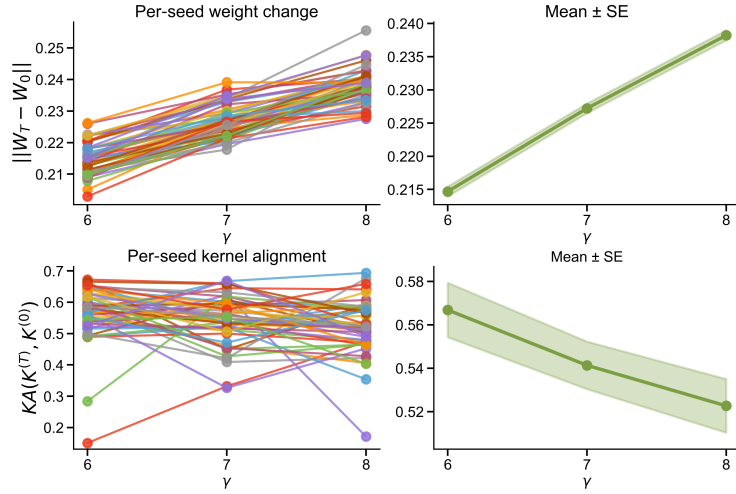


Figure 16: Stronger feature learning for networks trained under larger γ on the Path Integration task.

226 **O Verifying μP reliably controls for feature learning across network width**

227 Here, we only use Kernel Alignment to assess the feature learning strength in the networks since
 228 the unnormalized weight-change norm $\|\mathbf{W}_T - \mathbf{W}_0\|_F$ scales directly with matrix size (therefore
 229 network size) and there exists no obvious way to normalize across different dimensions. In our ear-
 230 lier analysis where we compared weight-change norms at varying γ , network size remained fixed,
 231 so those Frobenius-norm measures were directly comparable. We found that, for all tasks except
 232 Delayed Discrimination, the change in mean KA across different network sizes remains extremely
 233 small (less than 0.1), which demonstrates that μP parameterization with the same γ has effec-
 234 tively controlled for feature learning strength across network sizes. On Delayed Discrimination, the
 235 networks undergo slightly lazier learning for larger network sizes. Nevertheless, we still include
 236 Delayed Discrimination in our analyses of solution degeneracy to ensure *our conclusions remain*
 237 *robust even when μP can't perfectly equalize feature-learning strength across widths*. As shown
 238 in the main paper, lazier learning regime generally increases dynamical degeneracy; yet, larger net-
 239 works which exhibit lazier learning in the N-BFF task actually display lower dynamical degeneracy.
 240 This reversed trend confirms that the changes in solution degeneracy arise from network size itself,
 241 not from residual variation in feature learning strength.

242 **O.1 N-BFF**

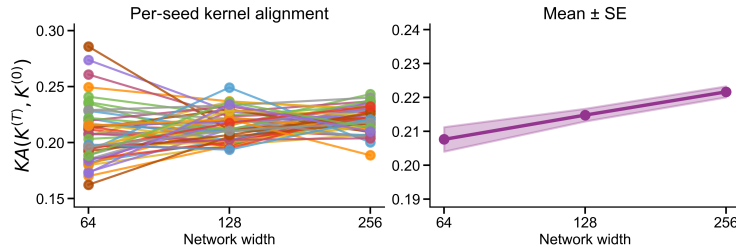


Figure 17: Kernel alignment (KA) for different network width on the 3 Bits Flip-Flop task. (Lower KA implies more feature learning.)

243 O.2 Delayed Discrimination

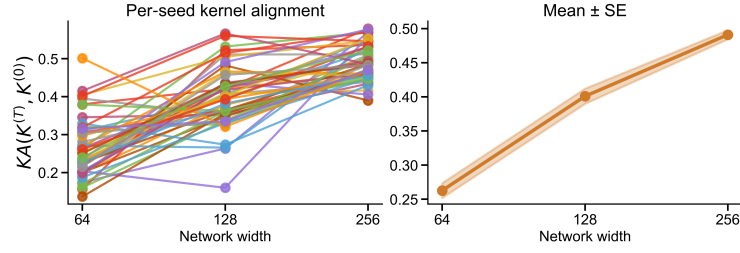


Figure 18: Kernel alignment for different network width on the Delayed Discrimination task.

244 O.3 Sine Wave Generation

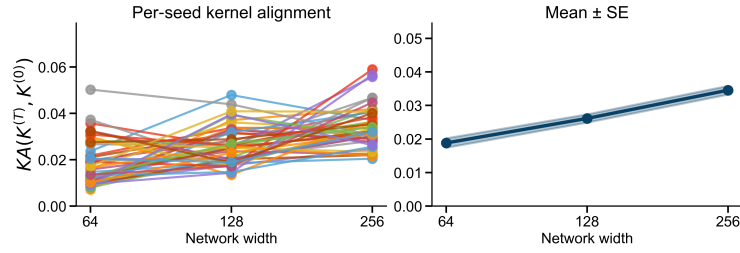


Figure 19: Kernel alignment for different network width on the Sine Wave Generation task.

245 O.4 Path Integration

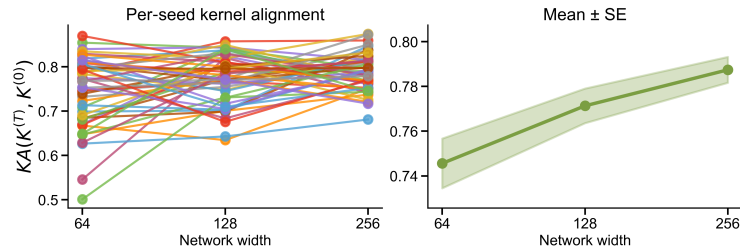


Figure 20: Kernel alignment for different network width on the Path Integration task.

246 P Regularization's effect on degeneracy for all tasks

247 In addition to showing regularization's effect on degeneracy in Delayed Discrimination task in the
 248 main paper, here we show that heavier low-rank regularization and sparsity regularization also re-
 249 liably reduce solution degeneracy across neural dynamics, weights, and OOD behavior in the other
 250 three tasks.

251 P.1 Low-rank regularization

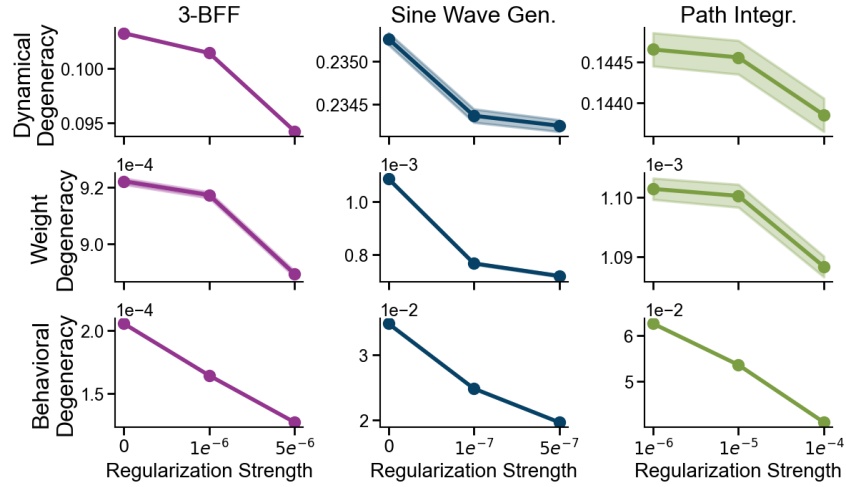


Figure 21: Low-rank regularization reduces degeneracy across neural dynamics, weight, and OOD behavior on the N-BFF, Sinewave Generation, and Path Integration task.

252 P.2 Sparsity regularization

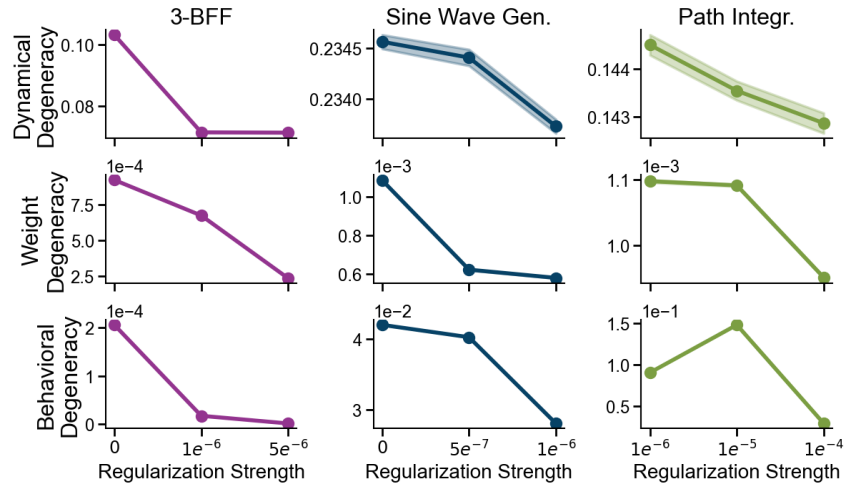


Figure 22: Sparsity regularization reduces degeneracy across neural dynamics, weight, and OOD behavior on the N-BFF, Sinewave Generation, and Path Integration task.

253 Q Test feature learning effect on degeneracy in standard parameterization

254 While μP lets us systematically vary feature-learning strength to study its impact on solution degeneracy, we confirm that the same qualitative pattern appears in *standard* discrete-time RNNs: stronger
 255 feature learning **lowers dynamical degeneracy** and **raises weight degeneracy** (Figure 23).

257 To manipulate feature-learning strength in these ordinary RNNs we applied the γ -**trick**—scaling the
 258 network’s outputs by γ —and multiplied the learning rate by the same factor. With width fixed, these
 259 two operations replicate the effective changes induced by μP . Figure 24 shows that this combination
 260 reliably tunes feature-learning strength. Besides weight-change norm and kernel alignment, we
 261 also report **representation alignment (RA)**, giving a more fine-grained view of how much the
 262 learned features deviate from their initialization [Liu et al., 2023]. Representation alignment is the
 263 directional change of the network’s representational dissimilarity matrix before and after training,
 264 and is defined by

$$\text{RA}(R^{(T)}, R^{(0)}) := \frac{\text{Tr}(R^{(T)} R^{(0)})}{\|R^{(T)}\| \|R^{(0)}\|}, \quad R := H^\top H,$$

265 A lower RA means more change in the network’s representation of inputs before and after training,
 266 and indicates stronger feature learning.

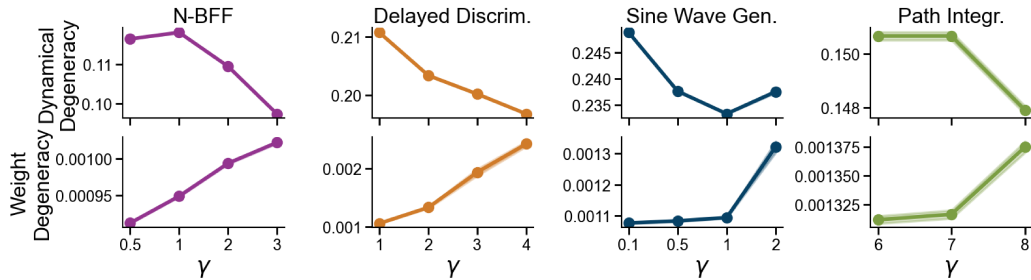


Figure 23: Stronger feature learning reliably decreases dynamical degeneracy while increasing weight degeneracy in standard discrete-time RNNs.

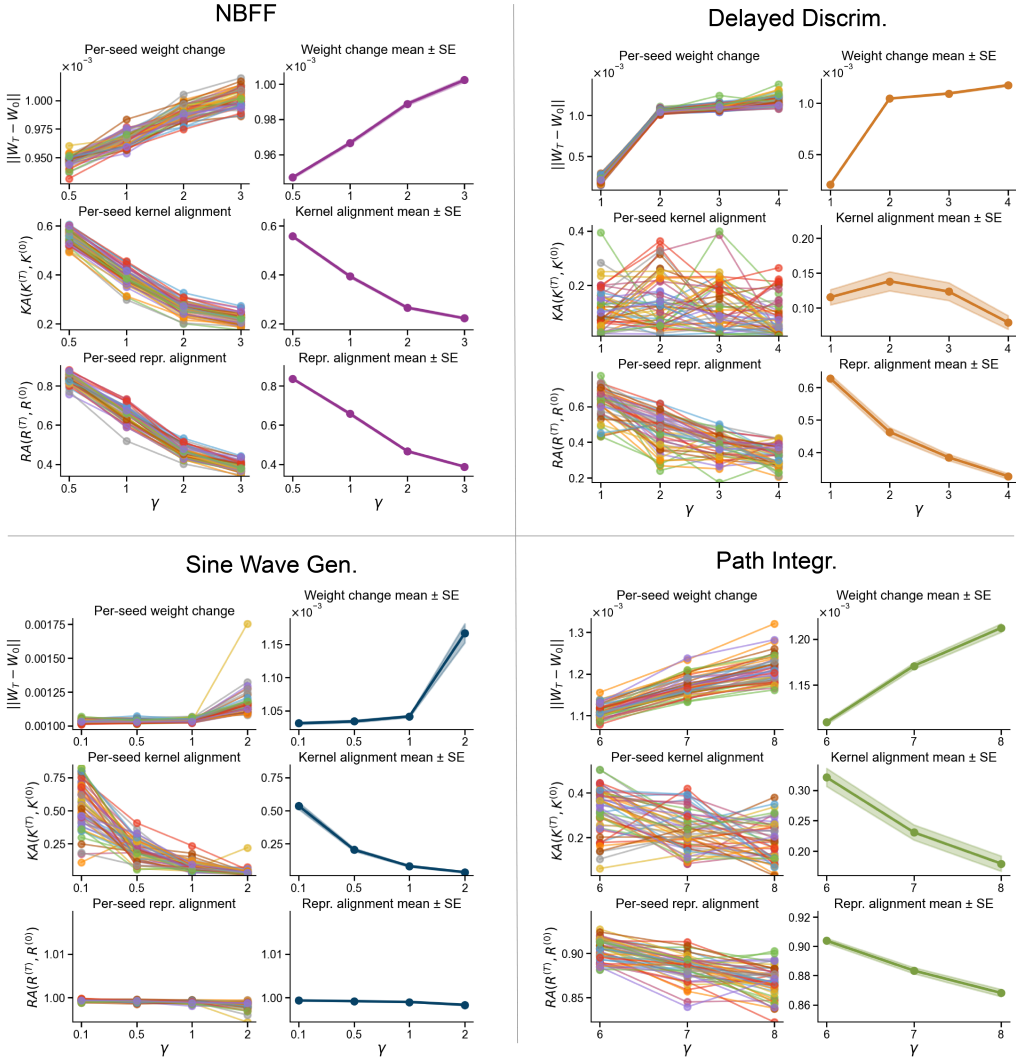


Figure 24: Larger γ reliably induces stronger feature learning in standard discrete-time RNNs.

267 R Test network size effect on degeneracy in standard parameterization

268 When we vary network width, both the standard parameterization and μP parameterization display
 269 the same overall pattern: **larger networks exhibit lower dynamical and weight degeneracy**. An
 270 exception arises in the 3BFF task, where feature learning becomes unstable and collapses in the
 271 wider models. In that setting we instead see *higher* dynamical degeneracy, which we suspect because
 272 the feature learning effect (lazier learning leads to higher dynamical degeneracy) dominates the
 273 network size effect.

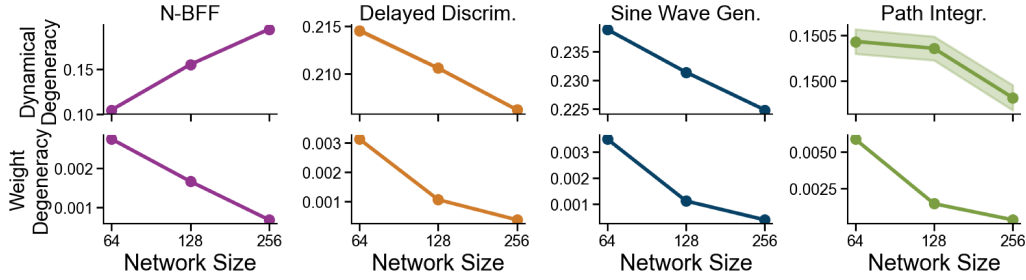


Figure 25: Larger network sizes lead to lower dynamical and weight degeneracy, except in the case where feature learning is unstable across width (in N-BFF).

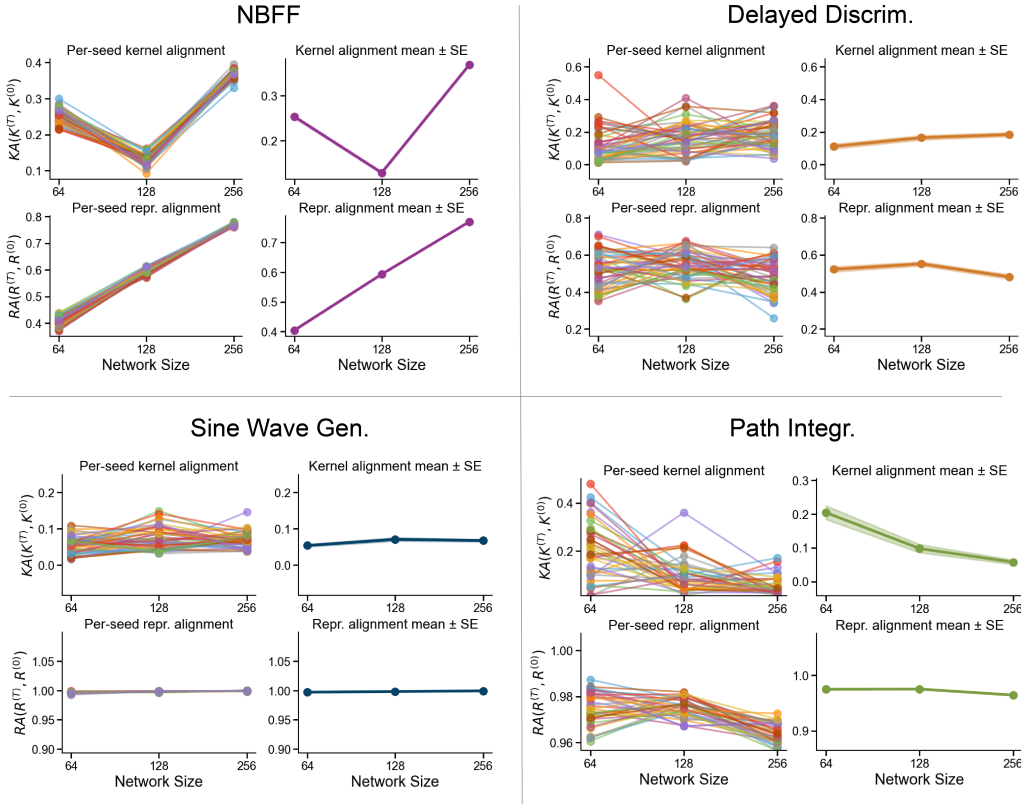


Figure 26: When changing network width in standard discrete-time RNNs, feature learning strength remains stable across width except in N-BFF, where notably lazier learning happens in the widest network.

274 **S Disclosure of compute resources**

275 In this study, we conducted 50 independent training runs on each of four tasks, systematically sweep-
276 ing four factors that modulate solution degeneracy—task complexity (15 experiments), learning
277 regime (15 experiments), network size (12 experiments), and regularization strength (26 experi-
278 ments), resulting in a total of 3400 networks. Each experiment was allocated 5 NVIDIA V100/A100
279 GPUs, 32 CPU cores, 256 GB of RAM, and a 4-hour wall-clock limit, for a total compute cost of
280 approximately 68 000 GPU-hours.

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