

# Deployable Neuromorphic AI in Space: Taxonomy and Architecture

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## Abstract

001 *Neuromorphic computing is increasingly considered for on-*  
 002 *board artificial intelligence in space due to its potential for*  
 003 *energy-efficient, low-latency inference and its natural fit to*  
 004 *sparse, event-driven sensing. Yet, much of the existing lit-*  
 005 *erature remains focused on algorithms, device characteris-*  
 006 *tics, or single-application demonstrations, offering limited*  
 007 *guidance on how neuromorphic acceleration should be in-*  
 008 *tegrated and operated at spacecraft system level. Conse-*  
 009 *quently, architectural decisions are often technology-driven*  
 010 *and difficult to transfer across missions and autonomy func-*  
 011 *tions. This position paper addresses this gap by introduc-*  
 012 *ing an application-driven, system-level framework for neu-*  
 013 *romorphic on-board AI. We propose a two-dimensional tax-*  
 014 *onomy based on (i) criticality (safety-critical vs. mission-*  
 015 *critical) and (ii) temporal behavior (persistent vs. event-*  
 016 *driven), yielding four workload categories capturing key ar-*  
 017 *chitectural drivers. We map these categories to reusable in-*  
 018 *tegration patterns, including execution modes, power man-*  
 019 *agement, and host–accelerator interaction assumptions. We*  
 020 *ground the framework with feasibility-level quantitative an-*  
 021 *chors from representative workloads on neuromorphic and*  
 022 *hybrid platforms, focusing on indicative latency, energy,*  
 023 *and duty-cycle behavior to support early system-level ar-*  
 024 *chitectural trade studies for autonomous spacecraft.*

## 025 1. Introduction

026 Space missions increasingly rely on onboard autonomy un-  
 027 der limited ground contact, tight power budgets, and grow-  
 028 ing sensing/telemetry volumes. Neuromorphic computing,  
 029 which comprises spiking neural networks [22], neuromor-  
 030 phic hardware to run them, and event-based sensing [10],  
 031 is therefore attracting interest for low-power, low-latency  
 032 onboard inference, particularly for sparse or event-driven  
 033 workloads [13]. In parallel, the move toward heterogeneous  
 034 onboard compute stacks and AI accelerators reinforces that  
 035 *system-level integration* is the key challenge, not model ac-  
 036 curacy alone [32] [43].

037 A central difficulty is the diversity of autonomy func-

tions: some are safety-relevant and run continuously or at  
 038 fixed cadence, while others are mission-oriented and bursty,  
 039 driven by sensing context. These differences often drive  
 040 architectural choices—execution modes, host–accelerator  
 041 coupling, and power management—more than the model  
 042 itself. Yet much of the neuromorphic-in-space discussion  
 043 remains technology- or single-use-case-driven, leaving mis-  
 044 sion designers without a consistent way to decide *when* neu-  
 045 romorphic acceleration is appropriate, *where* it fits in the  
 046 stack, and *how* it should be operated across functions. 047

## 1.1. Related Work 048

Neuromorphic computing for space has been motivated in  
 049 survey and perspective works that consolidate candidate  
 050 mission use cases and deployment considerations [13]. In  
 051 parallel, in-orbit and space-adjacent demonstrations have  
 052 validated the practical relevance of event-driven sensing  
 053 in operational environments, exemplified by Falcon Neuro  
 054 event-based imaging experiments [25, 26]. These efforts  
 055 provide strong feasibility signals for event-driven sens-  
 056 ing pipelines, but they focus primarily on sensing, data  
 057 products, and mission demonstrations rather than reusable  
 058 system-engineering abstractions for integrating heteroge-  
 059 neous autonomy functions. 060

A related stream of work examines energy-efficient on-  
 061 board processing and AI acceleration for satellite applica-  
 062 tions, including studies of accelerator-assisted payload pro-  
 063 cessing chains [32] and neuromorphic approaches in spec-  
 064 ific onboard contexts such as communications resource  
 065 management [19, 33]. In the AI4Space/CVPR workshop  
 066 community, spiking neural networks have been explored for  
 067 energy-efficient onboard vision tasks [18], and event-based  
 068 remote sensing methods further motivate event-driven per-  
 069 ception workloads [21]. While these works motivate feasi-  
 070 bility and demonstrate application potential, they typically  
 071 do not provide a general decision framework that connects  
 072 application properties to system-level integration patterns. 073

## 1.2. Contributions 074

- **2D application taxonomy:** criticality  $\times$  temporal behav-  
 075 ior, yielding four workload categories (A–D). 076

077	• <b>Application-to-architecture mapping:</b> reusable integration patterns (execution modes, host coupling, power expectations) per class.	128
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080	• <b>Reference architecture + anchors:</b> a modular stack and feasibility-level latency/energy/duty-cycle anchors (illustrative, not benchmarking).	131
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083	<b>2. Space Autonomy Applications</b>	134
084	Neuromorphic and event-based techniques have found practical use across a range of space autonomy functions; the following subsections summarize the main patterns and takeaways within each function area.	135
085		136
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088	<b>Planetary landing.</b> Scientific interest here mainly revolves around neuromorphic computing on event camera streams to either provide direct estimates of time-to-contact (TTC)[37], enabling robust navigation without full image [1], or for tackling divergence[27], as contrast maximization on motion-compensated event frames by applying branch-and-bound methods via GPU acceleration[44].	139
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095	<b>Attitude Determination.</b> Event cameras can act as star-tracker imagers for fast, low-power attitude sensing: events are aggregated into event frames and processed either by fused rotation estimation [4], or by progressive Hough transforms that recover relative attitude from star-streak geometry [2]. Validation uses real starlight with Earth rotation as a precise reference achieving about 18 arcsec RMSE. [28].	146
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103	<b>Pose Estimation.</b> Neuromorphic computing targets directly rendezvous and on-orbit servicing use cases using event cameras to overcome classical frame camera artifacts. Recent work either uses reconstructed event frames to run conventional neural pose estimation algorithms [5][35], or converts events into intensity-like images to reuse classic structure-from-motion for trajectory and 3D reconstruction [23]. Domain-gap studies to reduce sim-to-real fragility produced the SEENIC [14], FRESH [15] and SPADES [35] datasets, among the most relevant public resources.	154
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113	<b>On-Board SSA.</b> On-board space situational awareness for nano-satellites increasingly uses event cameras to autonomously detect and characterize debris while limiting power usage and downlink; EventSat demonstrate on-board inference in LEO despite highly dynamic imagery [7][36]. Payload studies quantify detectability via sensor characteristics and size baffles/vanes for stray-light suppression, while COTS neuromorphic experiments confirm viability but expose low-light noise and low-dynamics degradation[11].	164
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123	<b>EO Payloads.</b> Event cameras have already flown for lightning and other fast atmospheric onsets, where microsecond latency and sparse output cut exposure and downlink[3]. ISS lightning demos further support neuromorphic sensing as a feasible EO instrument class [26]. For radio-	174
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	metric scenes, events are fused with images to reconstruct single-exposure HDR products while addressing image–event domain gaps and sparsity[21]. System concepts also use events as always-on triggers for change detection concepts[38], while onboard research spans SNN land-cover classification[17], physics-aware multispectral disaster change mapping to prioritize downlink[45], and neuromorphic processor architectures for methane payloads[29].	179
	<b>Communications.</b> Neuromorphic SatCom targets on-board RRM, beamforming, and spectrum awareness where classical optimization or dense CNNs are too costly: Spiking RRM models on Loihi-2 show competitive accuracy at far lower power than CNN baselines [33]. Sparse spiking beamforming maps LASSO-style designs onto locally competitive networks to reduce active RF chains with minimal performance loss [24] while interference is cast as neuromorphic classification, with large power savings on reduced-DFT features[8]. Enabling work defines on-board processor KPIs[32], explores split SNN inference over optical ISL to reduce inter-satellite transmission[39], and benchmarks neuromorphic processors against conventional AI accelerators[19] and datasets [42].	180
	<b>FDIR.</b> Neuromorphic FDIR targets always-on, low-SWaP on-board anomaly detection by replacing threshold logic with learned nominal-behaviour models[31]. A common approach is reconstruction-based autoencoders, where anomalies appear as elevated reconstruction error; spiking variants leverage sparse spike-coded representations for streaming inference under tight compute and memory [40]. Flight relevance is strengthened by benchmarking ML-enabled FDIR on low-power boards and by FPGA mappings of spiking autoencoders reporting utilization and power compatible with continuous on-board execution [30][34].	181
	<b>3. Taxonomy and Application-to-Architecture Mapping for Neuromorphic AI</b>	182
	While all works presented in Section 2 highlight feasibility and promise, they do not provide a compact system-level abstraction that helps mission designers reason about <i>how</i> heterogeneous autonomy applications should be integrated and operated within a spacecraft compute stack. In this section, we introduce a two-dimensional taxonomy that captures two dominant architectural drivers across autonomy functions: <b>criticality</b> and <b>temporal behavior</b> . We then use this taxonomy as the basis for a structured application-to-architecture mapping.	183
	<b>3.1. Criticality × Temporal Behavior</b>	184
	The proposed taxonomy classifies onboard autonomy applications along two axes: (i) <b>criticality</b> , distinguishing functions that are safety-critical (spacecraft protection and fault containment) from those that are mission-critical (per-	185
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Table 1. Application-to-architecture mapping for representative space autonomy functions. Hybrid strategy denotes integration patterns (triggering/gating, voting/diversity, distillation). References for each row: Landing/TRN [27, 37, 44], Star tracking [2, 20, 28], SSA [25, 36], EO [18, 21], Comms [19, 32, 33], FDIR [13]. Categories A–D reflect criticality and temporal behavior and guide expected execution mode, power profile, and host coupling at the system level.

Function	Cat	Mode	Power	Host	Hybrid
Landing/TRN ; Pose/VBPE [27, 37, 44]	D	Burst	Idle $\approx$ 0, bursts	Loose–mod	Triggering; Distillation
Star tracking [2, 20, 28]	C	Cont.	Steady low	Mod–tight	Vote; Distillation
SSA (windowed obs.) [25, 36]	D	Windowed	Low idle, bursts	Loose	Triggering; Vote
EO payloads [18, 21]	C/D	Cont./burst	Steady/duty	Loose	Distillation; Triggering
Comms (monitor/adapt) [19, 32, 33]	C/D	Periodic+evt	Steady+spikes	Tight	Trig; Vote/Distillation
FDIR (monitor/safe) [13]	A/B	Periodic+evt	Steady / idle $\approx$ 0	Tight	Vote; Triggering

179 formance, science return, or operational efficiency), and  
 180 (ii) **temporal behavior**, distinguishing **persistent/periodic**  
 181 processing from **event-driven/sporadic** processing. The  
 182 second axis is particularly relevant for neuromorphic sys-  
 183 tems because event-driven sensing and sparse computation  
 184 enable activity-proportional resource usage [13, 25].

185 We define the **Categories** as:

186 **A. Safety-critical & persistent:** continuous or periodic  
 187 monitoring and protection functions (e.g., health mon-  
 188 itoring / protection loops), where predictable execution  
 189 and conservative behavior dominate.

190 **B. Safety-critical & event-driven:** contingency-triggered  
 191 safeguards that remain near-idle until a detected condi-  
 192 tion requires fast response (e.g., event-triggered fault  
 193 containment).

194 **C. Mission-critical & persistent:** sustained onboard pro-  
 195 cessing pipelines supporting mission objectives under  
 196 continuous resource budgets (e.g., persistent payload in-  
 197 telligence or comms intelligence), often benefiting from  
 198 energy-efficient acceleration [32, 33].

199 **D. Mission-critical & event-driven:** bursty, context-  
 200 driven perception or data-reduction workloads, com-  
 201 monly aligned with event-based sensing and sparse in-  
 202 ference (e.g., event-driven perception and onboard data  
 203 triage) [18, 21, 25].

### 204 3.2. From Taxonomy to Architecture Mapping

205 The role of this taxonomy is not to categorize missions ex-  
 206 haustively, but to provide a *decision lens* that links appli-  
 207 cation properties to architectural consequences. In the next  
 208 subsection, we map Categories A–D to system-level inte-  
 209 gration patterns (execution modes, power profiles, host cou-  
 210 pling, and optional hybrid strategies), enabling consistent  
 211 architectural trade-offs for neuromorphic onboard AI across  
 212 heterogeneous autonomy functions [19, 32].

### 213 3.3. Application-to-Architecture Mapping

214 Table 1 operationalizes the taxonomy by mapping applica-  
 215 tions to criticality x temporal-behavior categories (A-D). It  
 216 highlights execution mode, power profile and how neuro-

217 morphic is coupled to the host compute. It also indicates a  
 218 proposed hybrid collaboration strategy: neuromorphic trig-  
 219 gers the host (*triggering*), the host distills knowledge to neu-  
 220 romorphic (*distillation*), or a voting scheme combines both  
 221 (*voting*).

## 222 4. Reference Architecture, Feasibility Anchors

223 We introduce a layered reference architecture for neuro-  
 224 morphic onboard AI and summarize feasibility anchors for  
 225 latency, energy, and scaling, providing a compact systems  
 226 view for early mission design.

### 227 4.1. From Classical Stacks to Neuromorphic

228 Modern spacecrafts employ heterogeneous stacks of sen-  
 229 sors, processors, accelerators, and supervisory flight com-  
 230 puters [32] [16]. Neuromorphic computing extends this  
 231 model with an execution paradigm optimized for sparse,  
 232 event-driven workloads [6, 12]. Rather than replacing con-  
 233 ventional pipelines, neuromorphic processors complement  
 234 existing accelerators by enabling activity-proportional in-  
 235 ference within heterogeneous onboard AI systems.

### 236 4.2. A Layered Neuromorphic Spacecraft Stack

237 We propose a layered neuromorphic stack that preserves  
 238 sparsity across the sensing-to-decision pipeline while re-  
 239 maining compatible with existing architectures. The model  
 240 emphasizes sparsity preservation, asynchronous execution,  
 241 and duty-cycle-aware orchestration.

242 **Layer 1: Sensing.** Event-driven sparsity originates from  
 243 native event sensors or eventized dense signals. Event cam-  
 244 eras have demonstrated in-orbit feasibility [1, 25, 37].

245 **Layer 2: Front-end Encoding.** Preprocessing regulates  
 246 activity via filtering or temporal aggregation, analogous to  
 247 FPGA-style front ends [27, 44].

248 **Layer 3: Neuromorphic Acceleration.** Asynchronous  
 249 inference is performed using spiking or event-driven mod-  
 250 els, exploiting sparse activation for energy efficiency [6, 9].

251 **Layer 4: Host Supervision.** A supervisory proces-  
 252 sor manages orchestration, redundancy, and interfaces, en-  
 253 abling hybrid neuromorphic–classical pipelines [32, 41].

**Layer 5: Autonomy Integration.** Outputs feed GNC, perception, and FDIR loops, typically augmenting rather than replacing classical decision stacks [13].

**Cross-layer insight.** Neuromorphic components integrate into hybrid stacks rather than operating in isolation. Recurring patterns include hierarchical triggering, redundancy across paradigms, and gradual migration from deep learning to spiking implementations. Such hybridization combines low-latency perception with the robustness of conventional accelerators.

### 4.3. Operating Regimes

While Section 3 establishes the taxonomy and mapping, the question is how these categories manifest in practice. Fig. 1 provides empirical anchors by situating representative operating regimes within a latency–power design space.

The power–latency map reveals continuous operating envelopes rather than discrete clusters. Persistent workloads occupy stable mid-latency, low-power regions consistent with steady-state neuromorphic co-processing [6, 9], while event-driven pipelines span broader vertical envelopes with low idle power and brief latency excursions, reflecting activity-proportional execution [19, 37]. Sensing-dominated front ends define the lower-latency frontier, where sparse acquisition gates heavier downstream processing [44]. These regimes align with activity-dependent scaling in neuromorphic systems: latency grows with temporal integration depth, energy with spike counts, and power follows duty cycle [12].

From a design perspective, temporal workload behavior emerges as a strong indicator of operating regime: event-driven workloads favor low-duty-cycle, bursty operation, while persistent workloads align with steadier power–latency bands. This trend is visible in Fig. 1, where event-driven examples (e.g., points 1–5 and 9–12) span broader power ranges, while more persistent workloads (e.g., points 6–8) cluster into tighter bands.

## 5. Discussion: Design Implications

**End-to-end sparsity preservation.** A primary system-level takeaway is that neuromorphic advantage is rarely delivered by a single component in isolation. Instead, efficiency emerges when sparsity is preserved across the full pipeline (sensing/encoding → inference → decision). Converting sparse signals into dense intermediate representations too early can shift the dominant cost to data movement and host-side preprocessing, eroding the benefits implied by Fig. 1 and the integration patterns in Table 1.

**Criticality drives coupling and assurance.** The taxonomy highlights that safety-critical categories (A/B) require tighter coupling to supervisory control, conservative confirmation paths, and explicit fallback behaviors. In contrast, mission-critical categories (C/D) can exploit duty-cycling

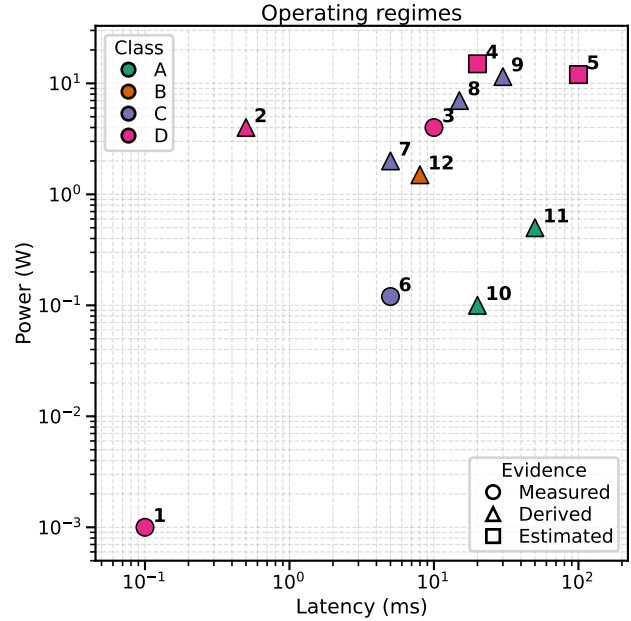


Figure 1. Neuromorphic spacecraft operating regimes (log–log latency vs. power). Numbers correspond to representative systems: (1) ESA optic-flow landing [44]; (2–3) Falcon Neuro ISS sensing and lightning observations [25, 26]; (4) EvSat3D event-based pose estimation [23]; (5) event-based divergence landing [27]; (6) Loihi-based onboard RRM [33]; (7) Brainsat neuromorphic onboard computer [29]; (8) event-based nanosat pointing [20]; (9) EventSat payload studies [7]; (10–11) neuromorphic onboard monitoring and FDIR architectures [30, 31]; (12) event-driven nanosatellite safeguard / mission-architecture anchor [36]. Marker shape denotes evidence type (measured/derived/estimated), and color indicates functional category.

and context-driven activation, and can tolerate graceful degradation (e.g., reduced fidelity or reduced duty cycle) without compromising spacecraft safety.

**Hybrid strategies are integration tools.** Triggering/gating, voting/diversity, and distillation are best viewed as system-integration patterns that improve robustness or fit within tight compute/energy envelopes (Table 1), rather than as new algorithms. These patterns help reconcile heterogeneous autonomy requirements with practical constraints on embedded hardware and operations.

**When neuromorphic is not the right fit.** Sustained, dense pipelines with limited intrinsic sparsity may be better served by conventional accelerators unless eventization/encoding preserves meaningful sparsity. The proposed framework is intended to make such cases explicit early, supporting technology selection rather than assuming neuromorphic is universally optimal.

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