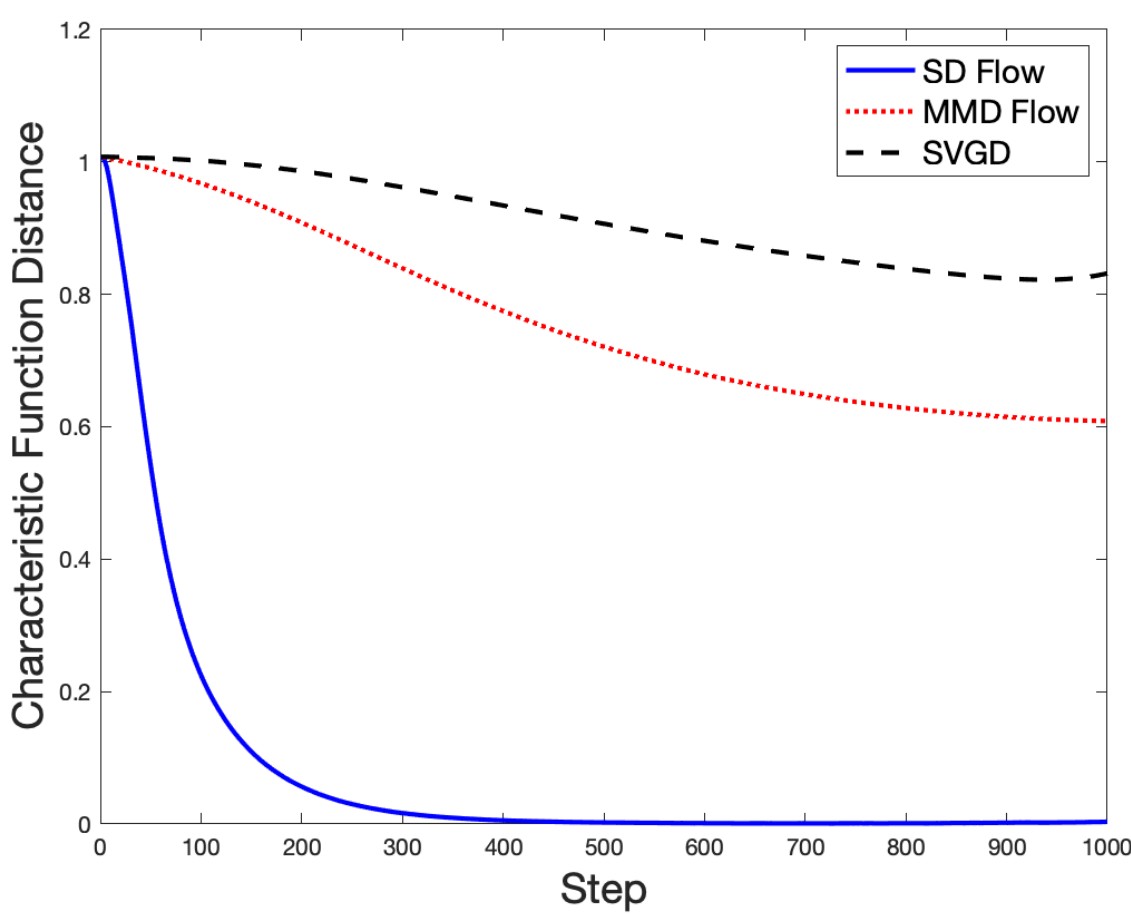
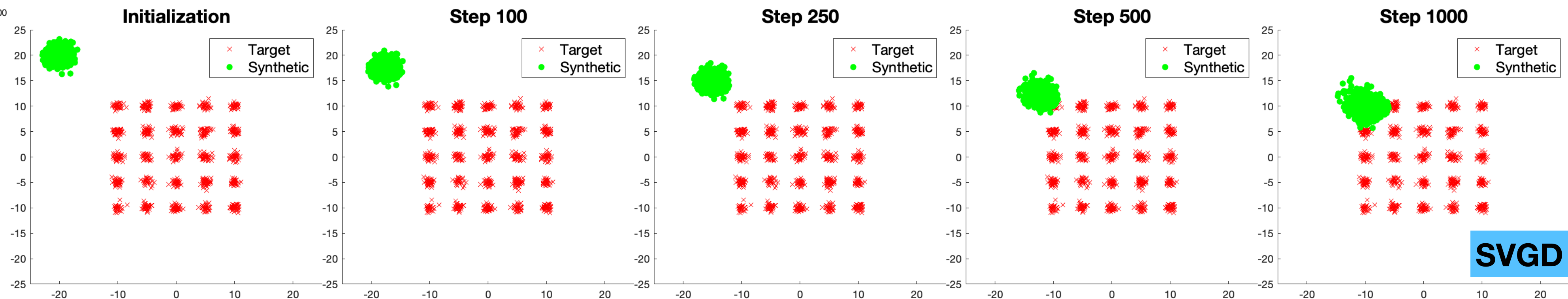
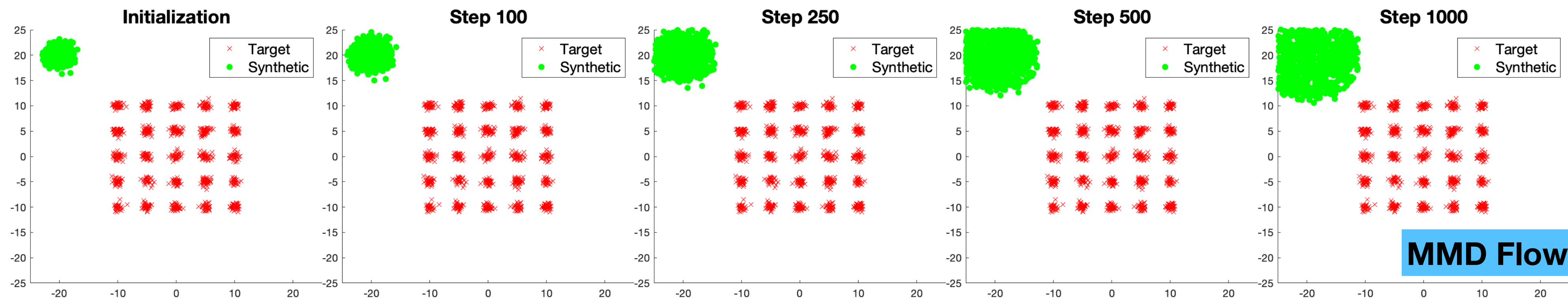
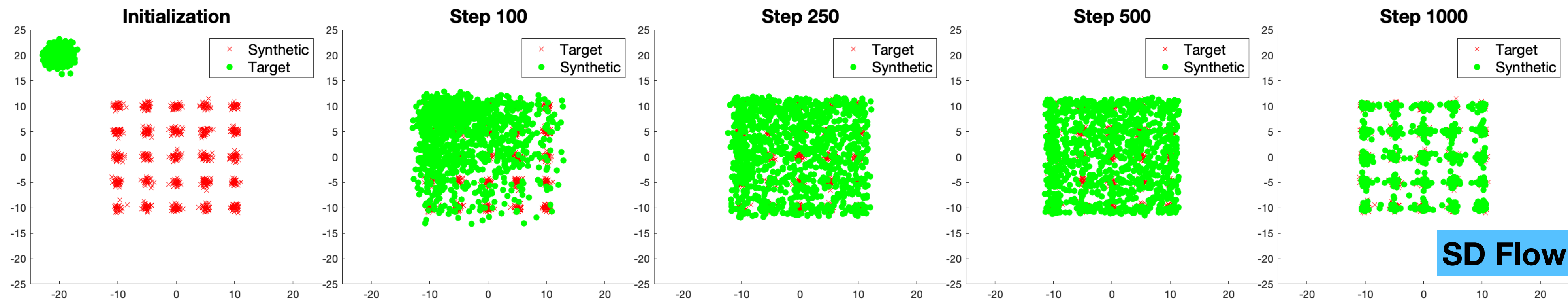
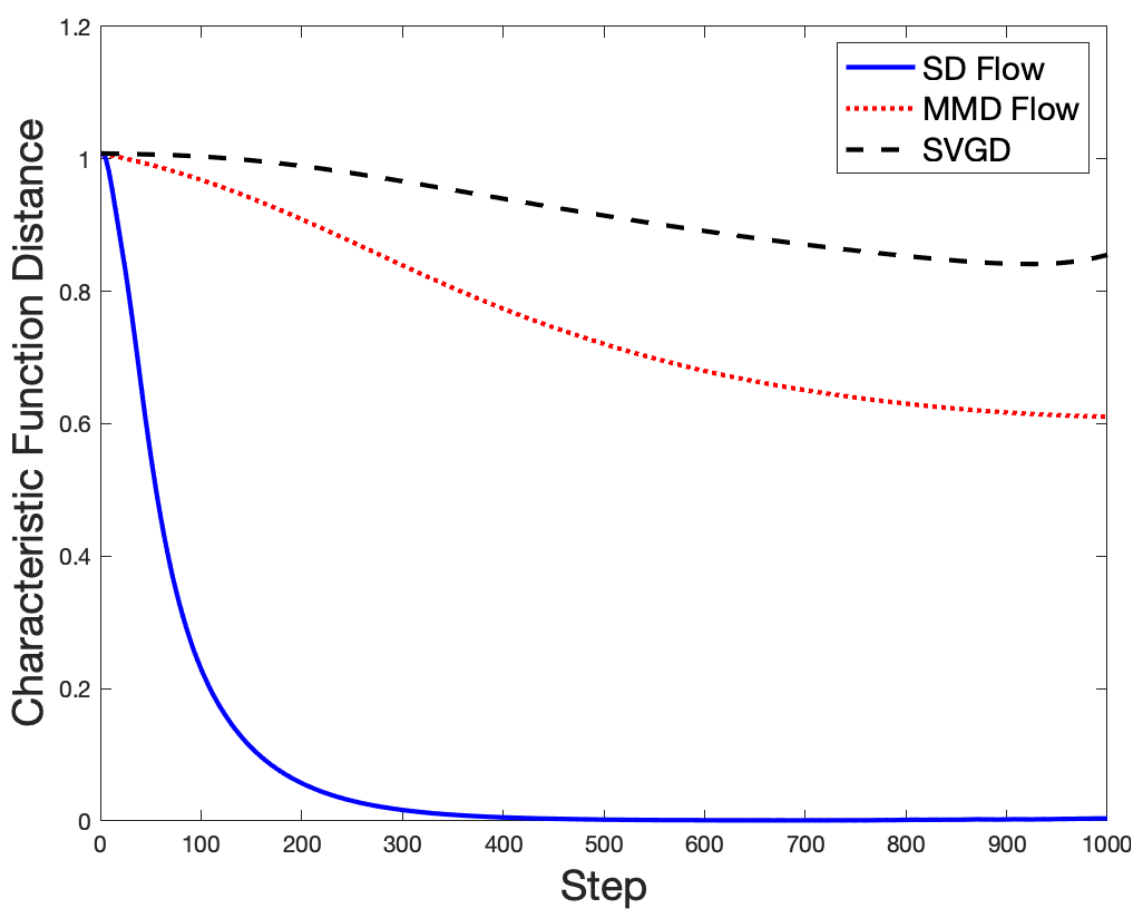
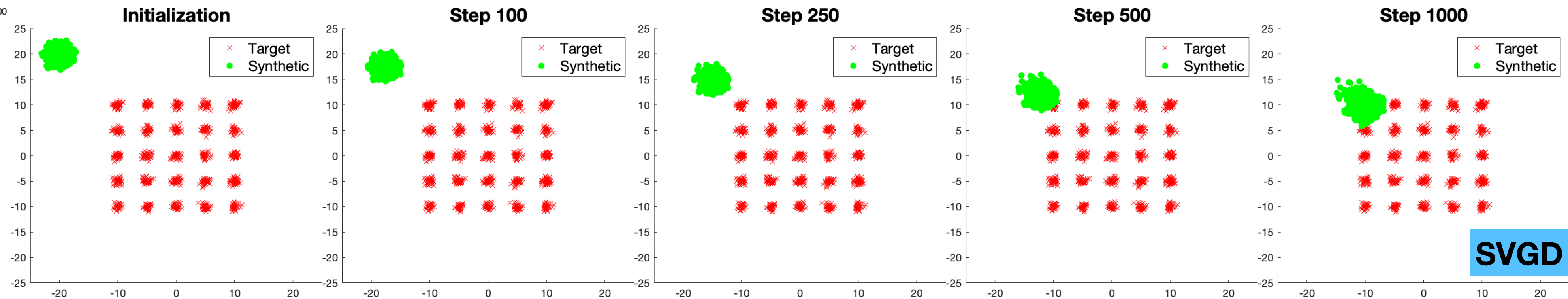
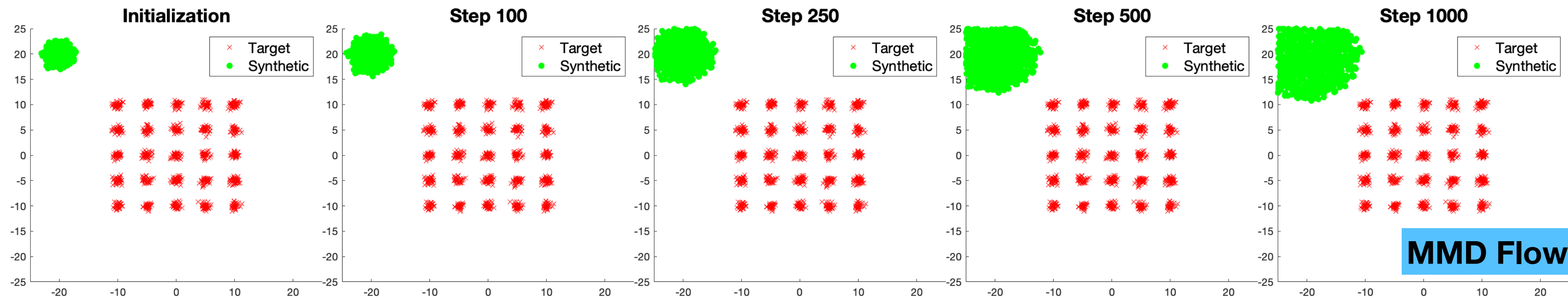
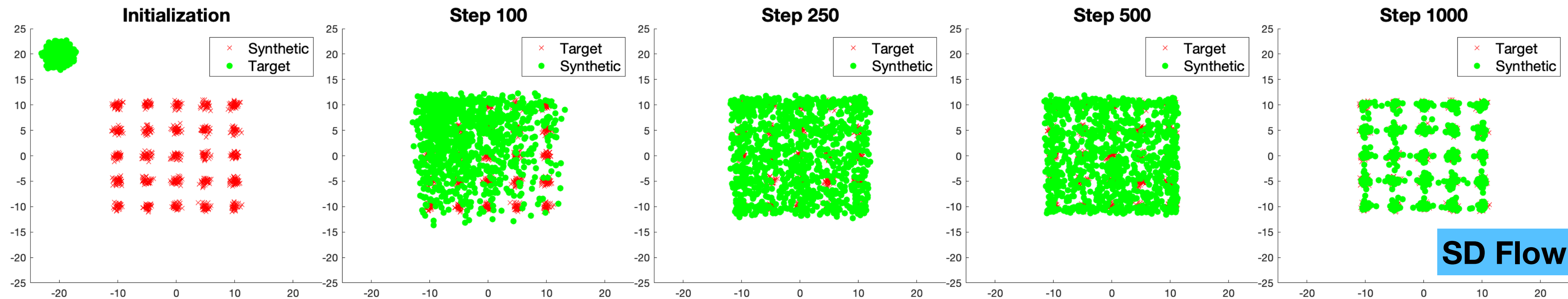


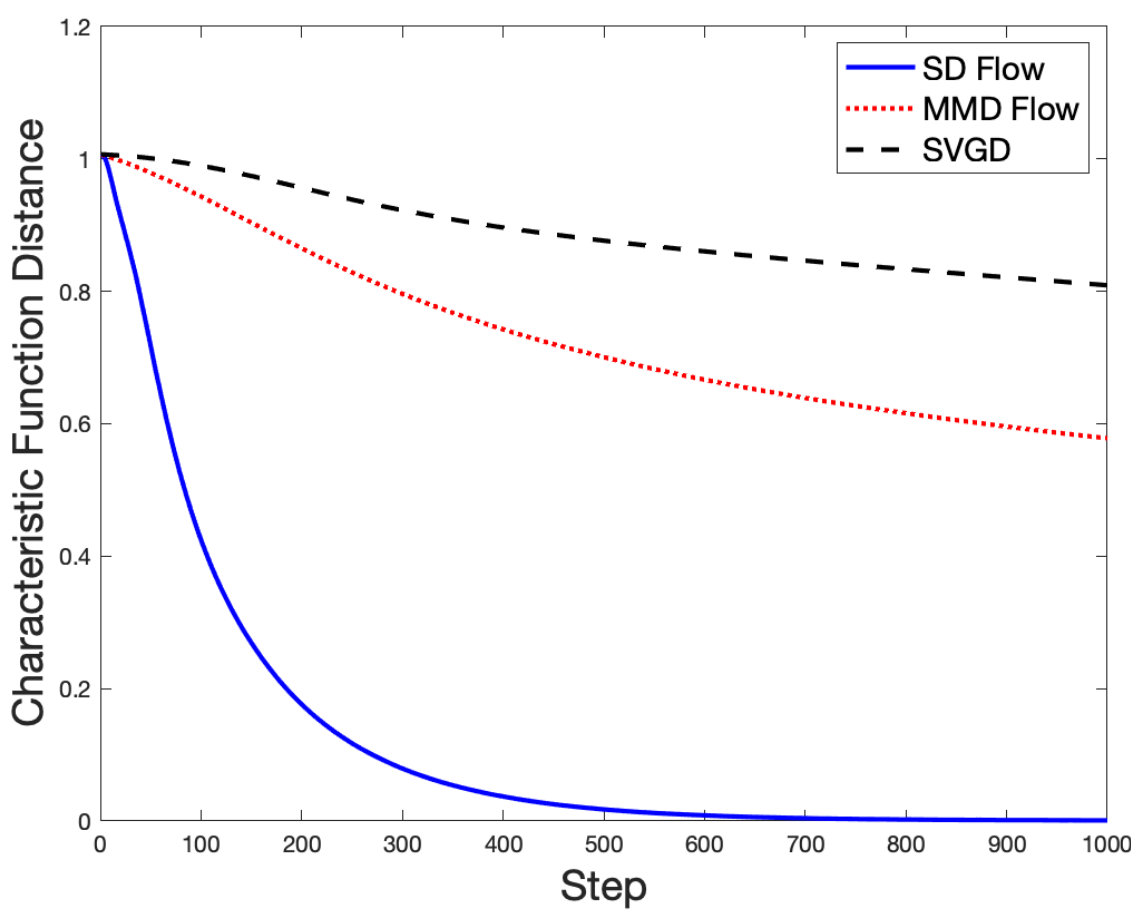
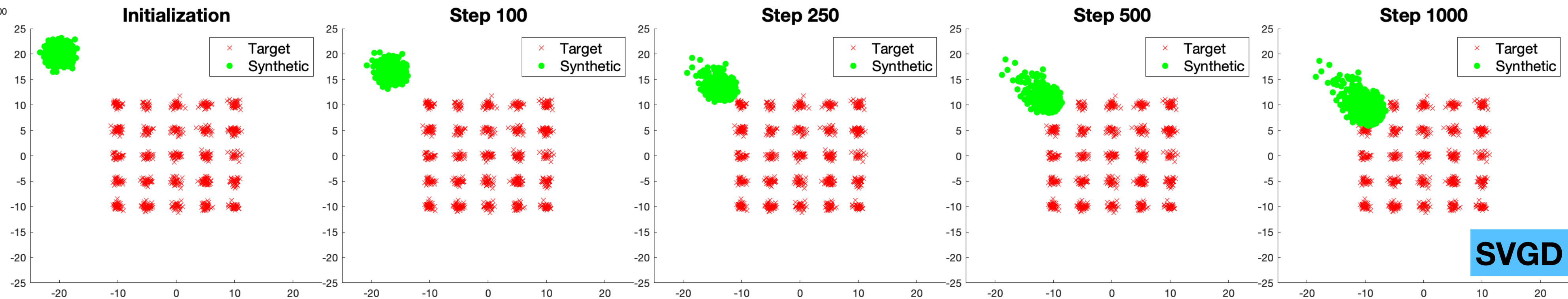
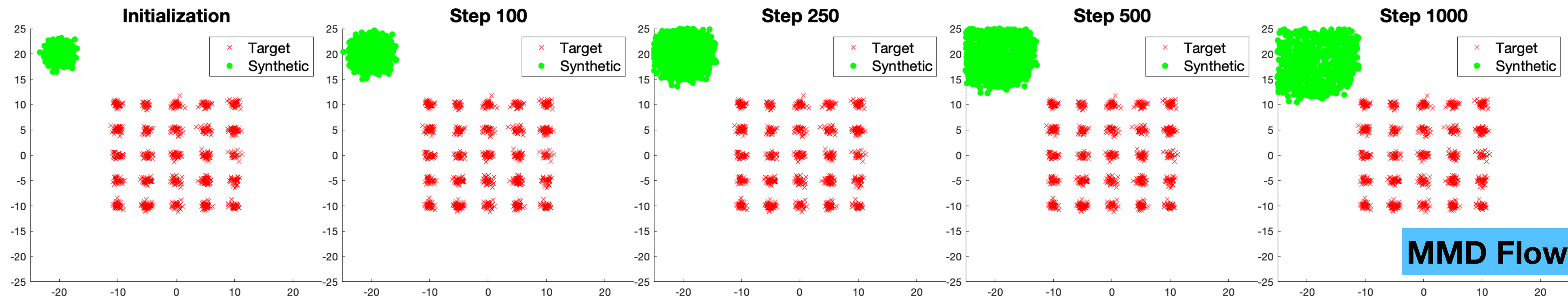
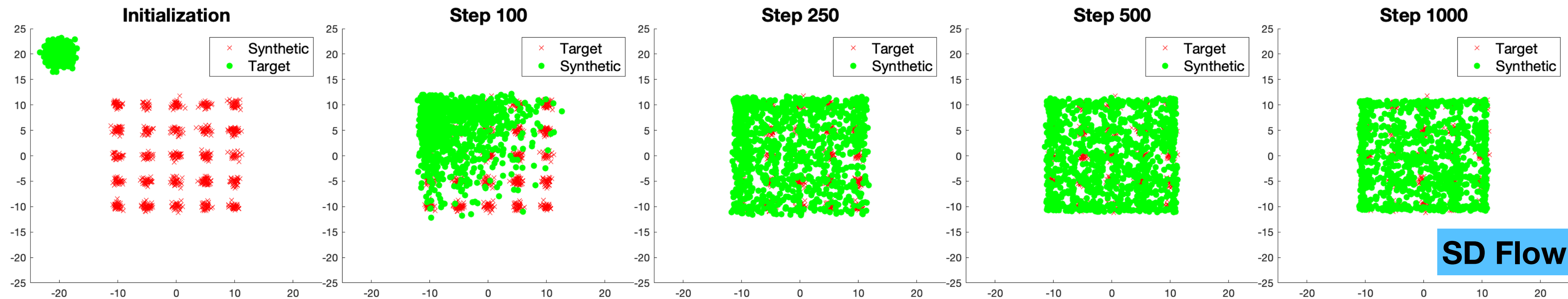
ADAGRAD	BATCH	CONST	ANNEAL
N	N	N	N



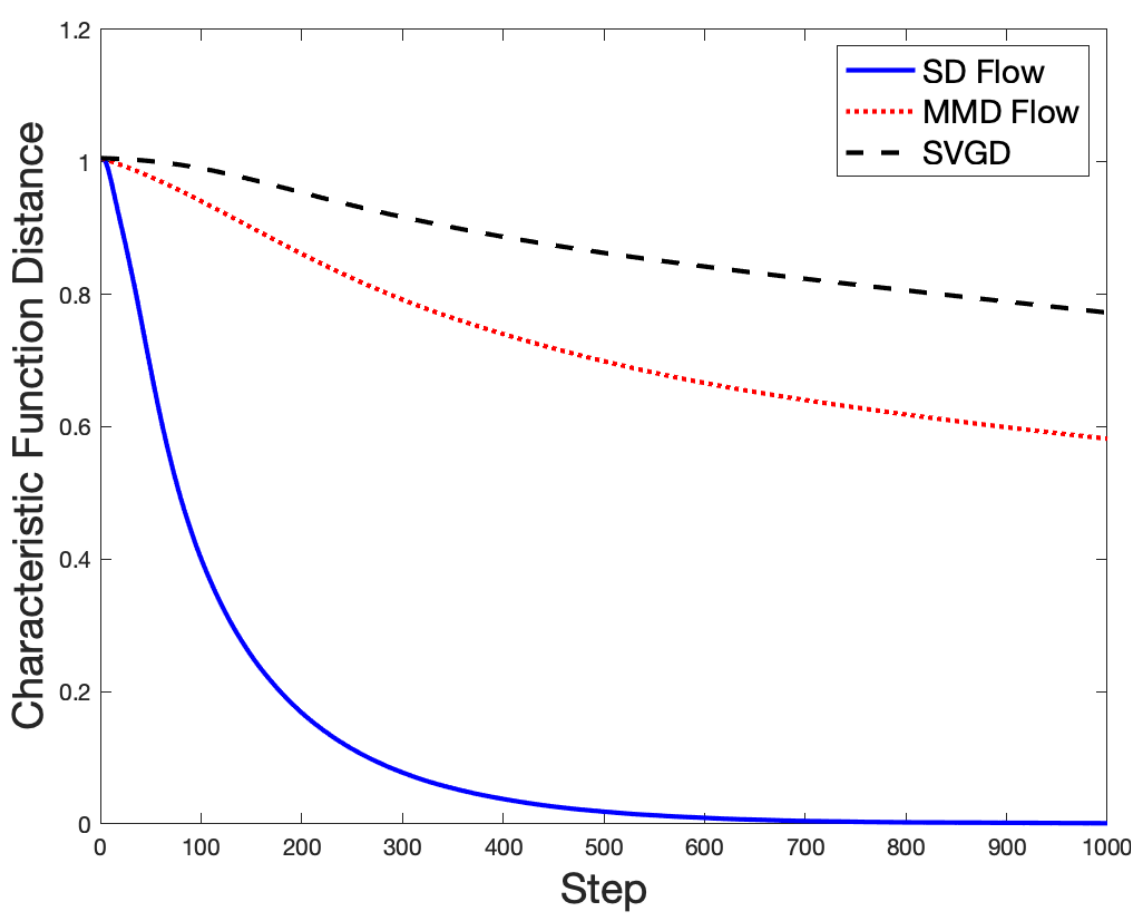
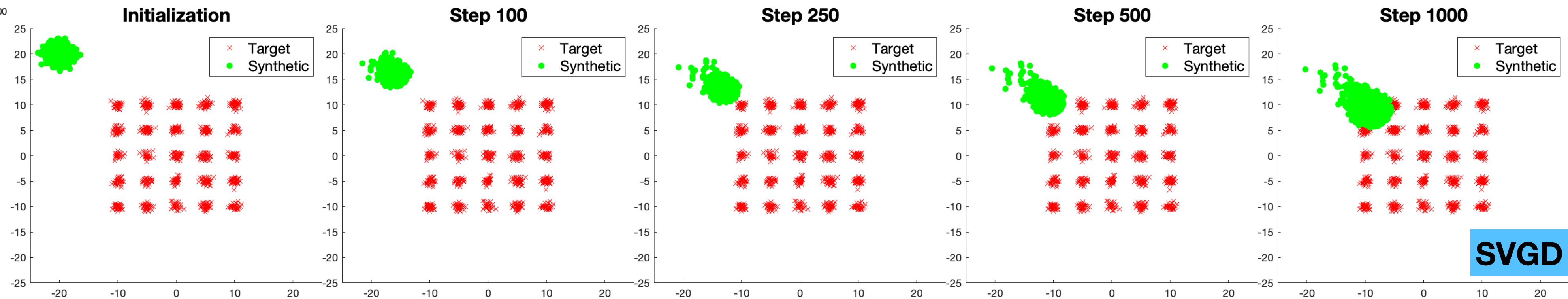
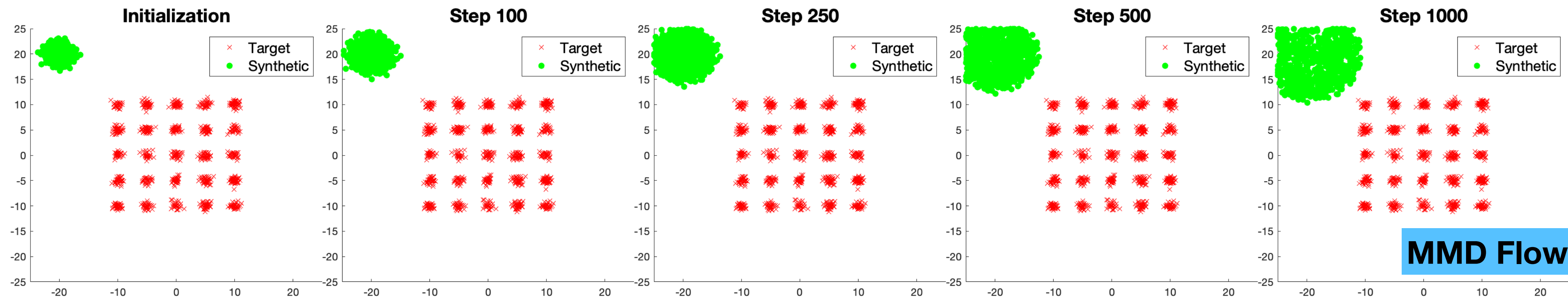
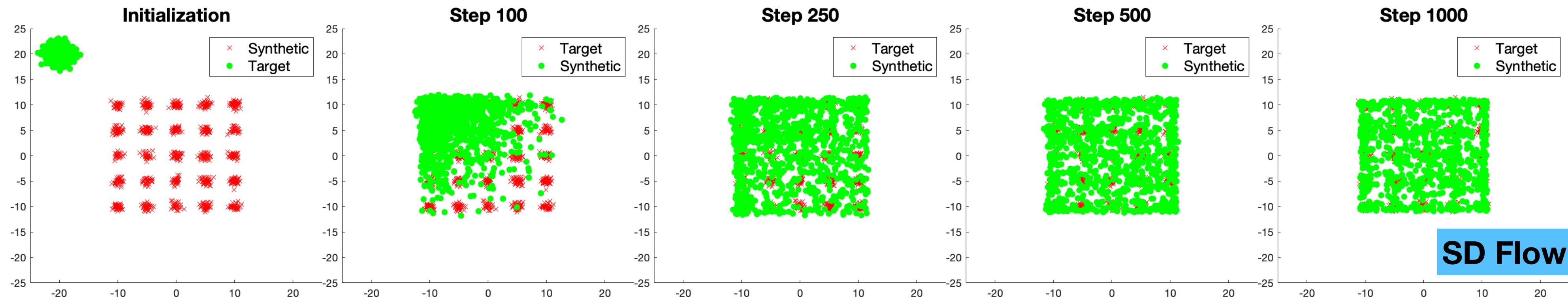
ADAGRAD	BATCH	CONST	ANNEAL
N	N	N	Y



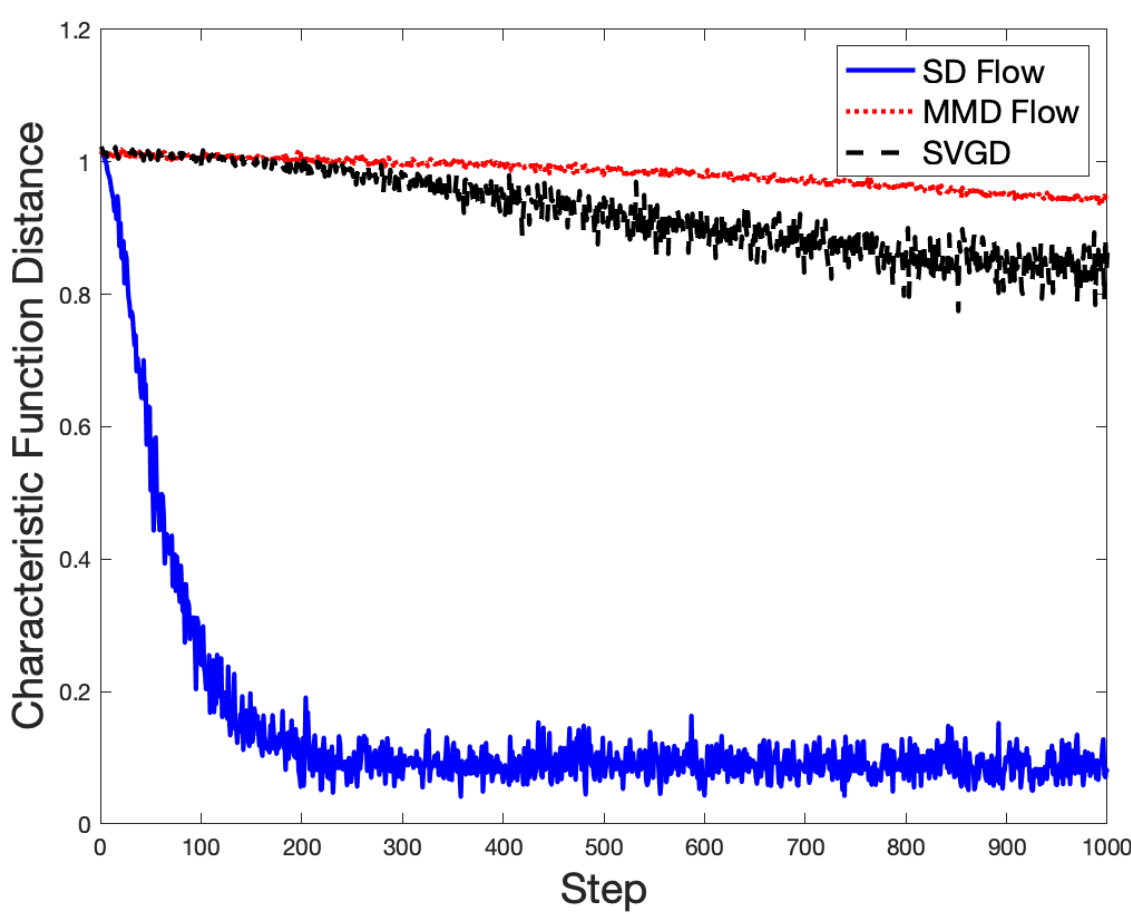
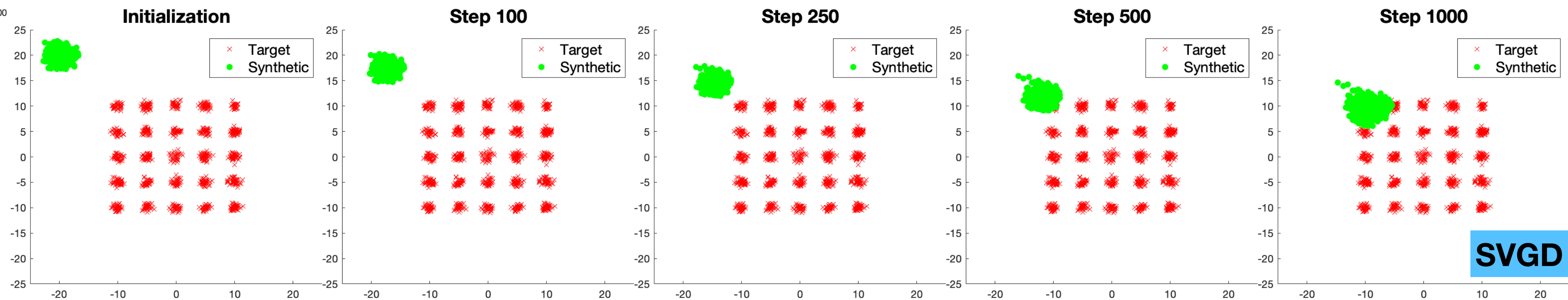
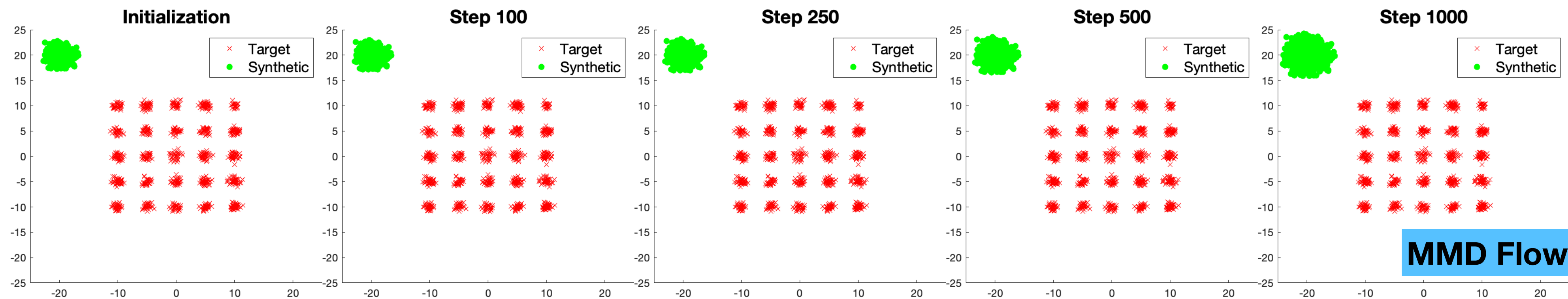
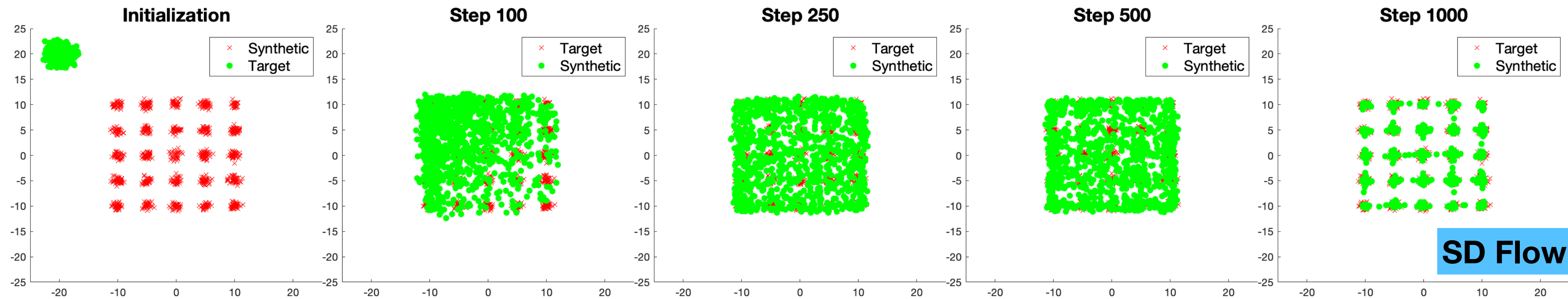
ADAGRAD	BATCH	CONST	ANNEAL
N	N	Y	N



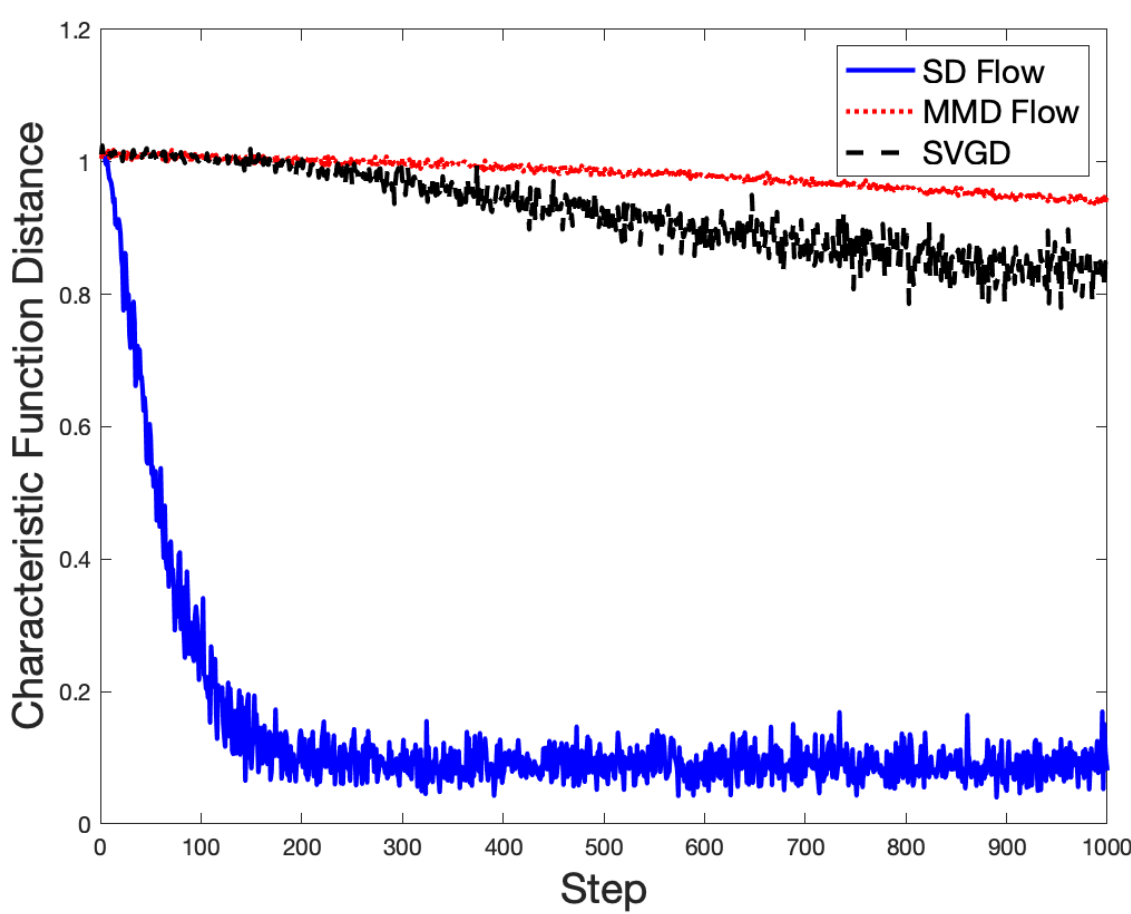
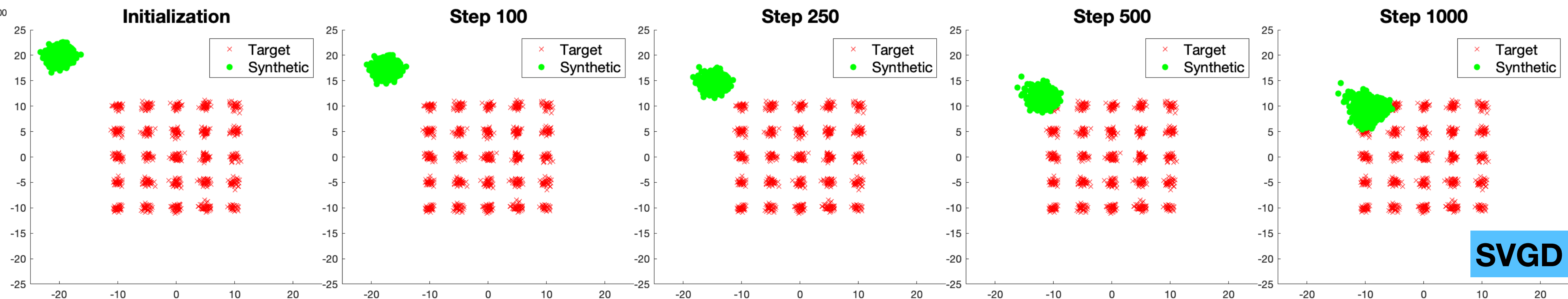
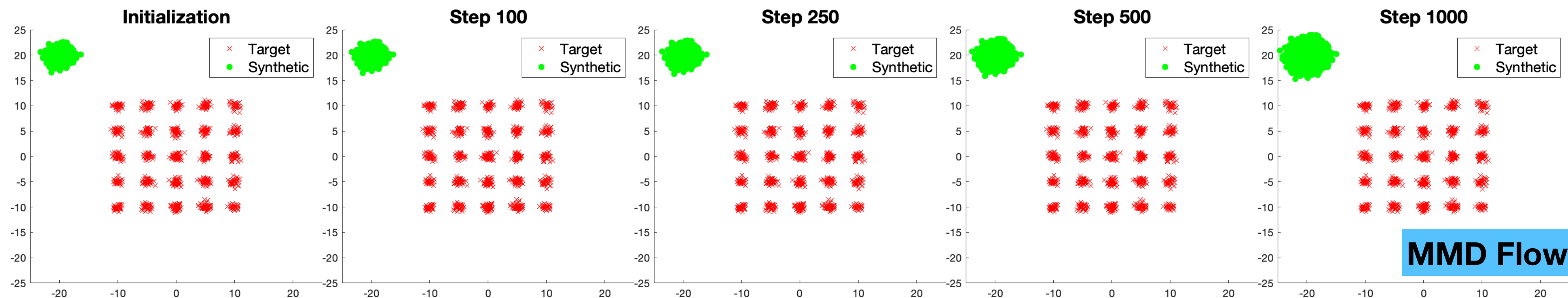
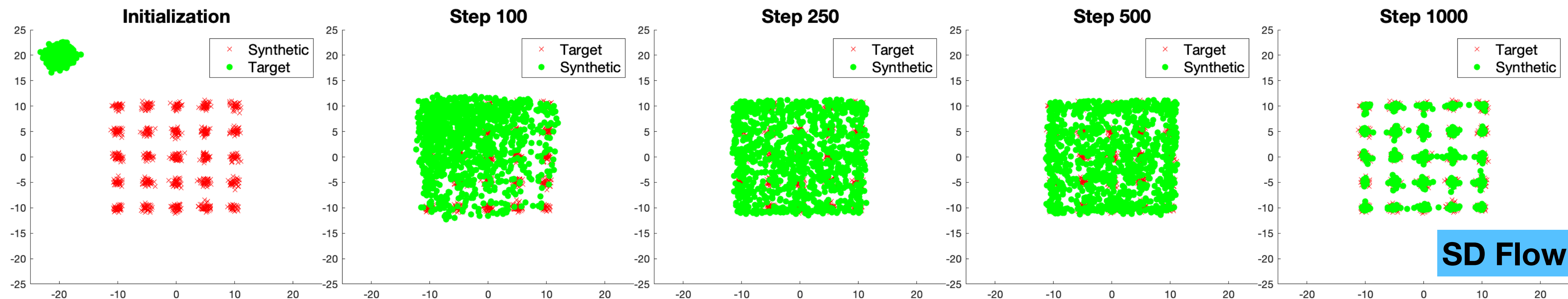
ADAGRAD	BATCH	CONST	ANNEAL
N	N	Y	Y



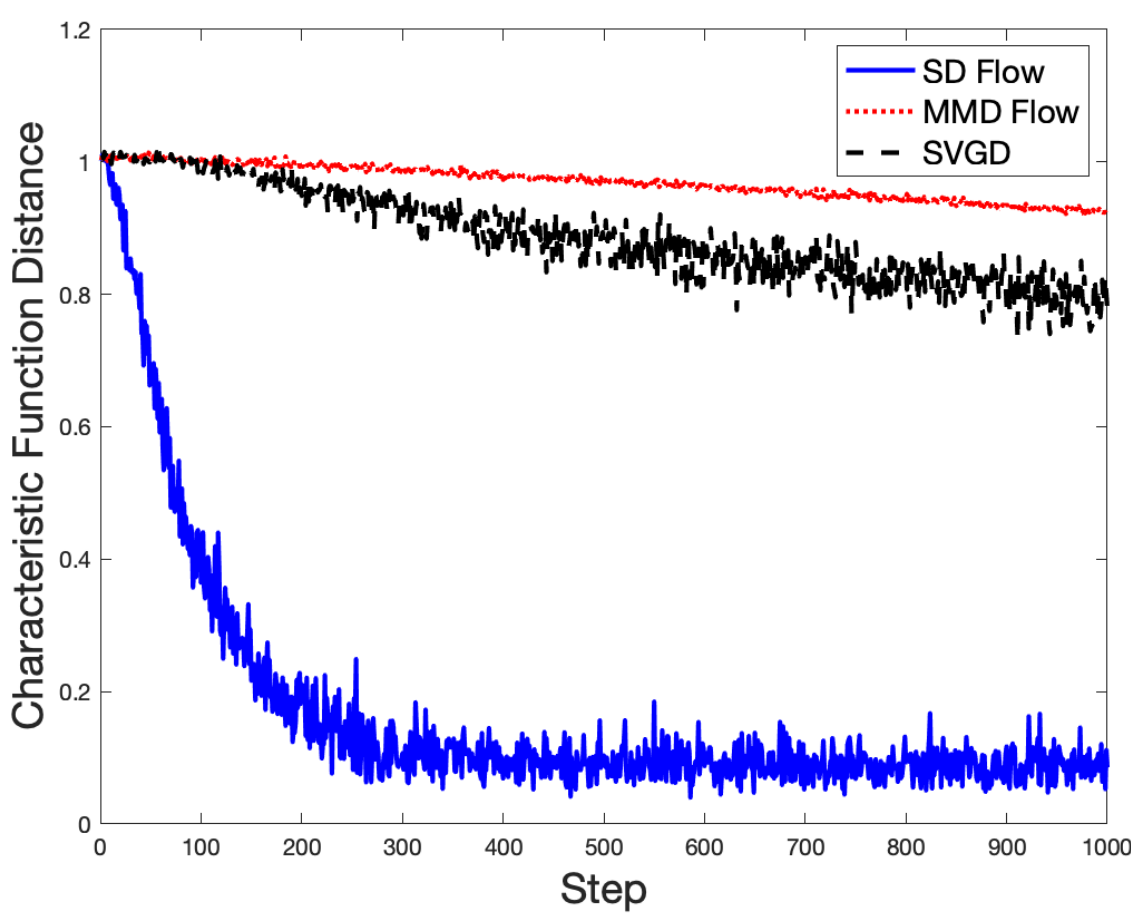
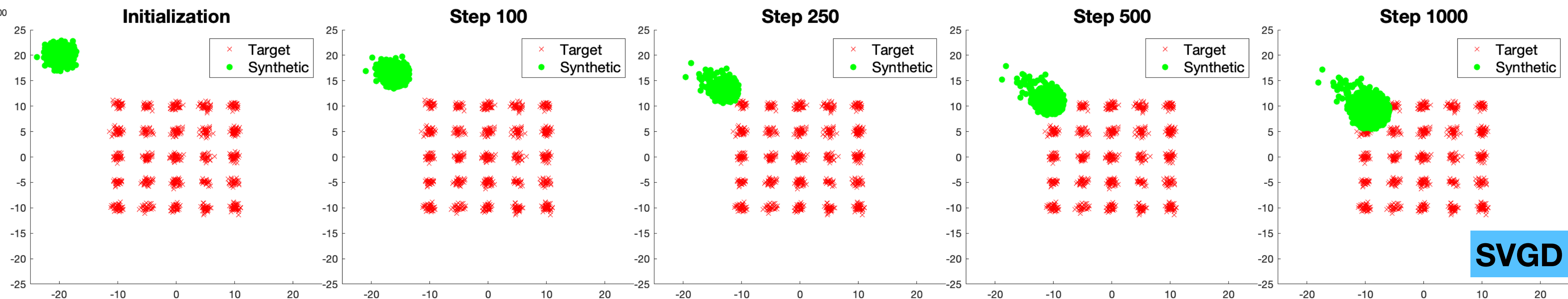
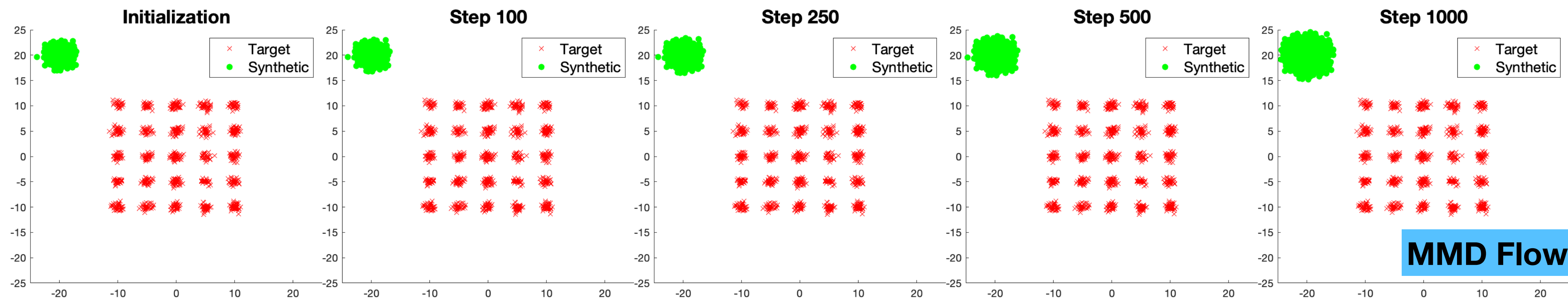
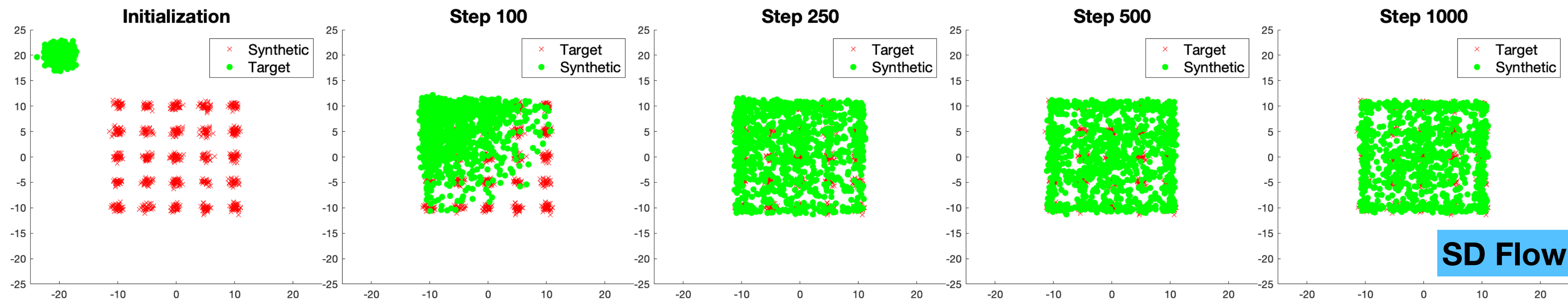
ADAGRAD	BATCH	CONST	ANNEAL
N	Y	N	N



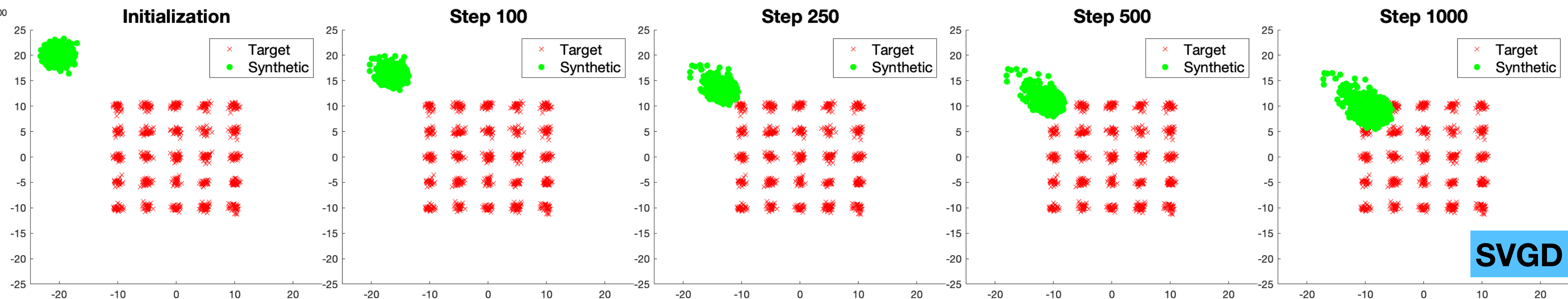
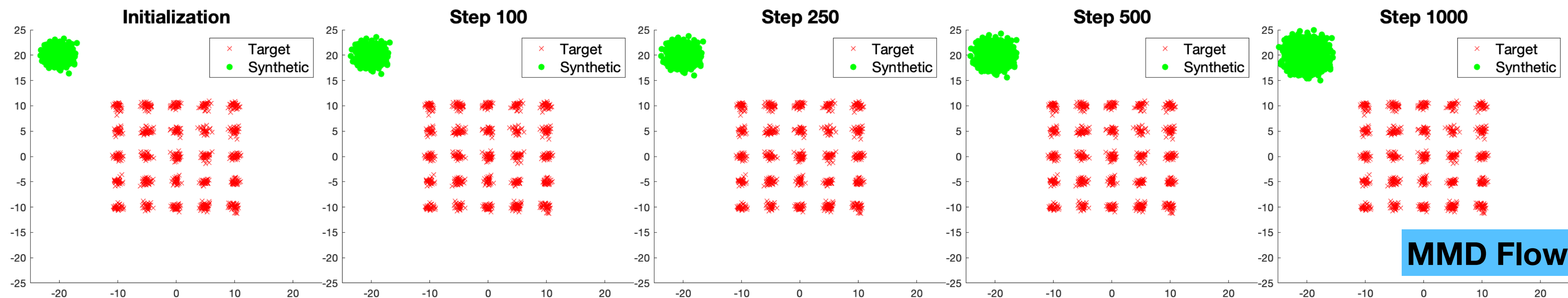
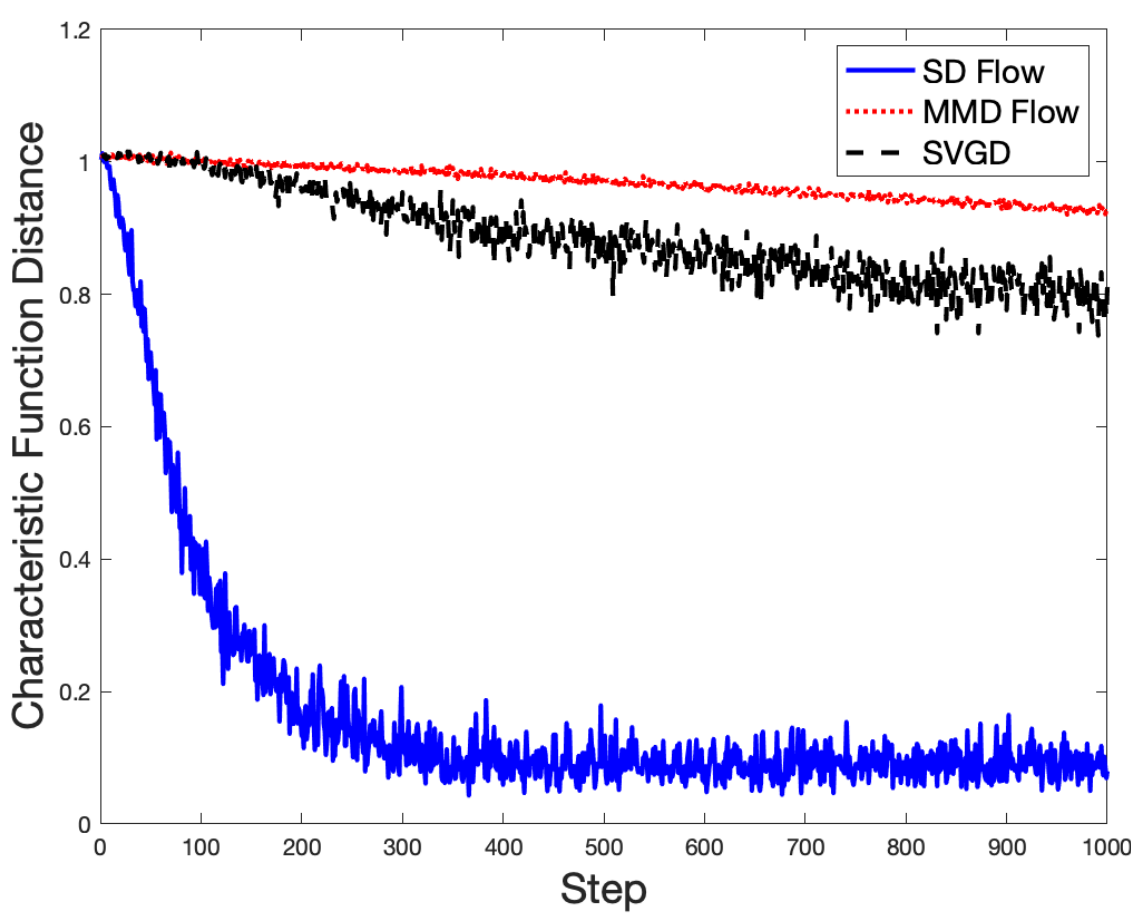
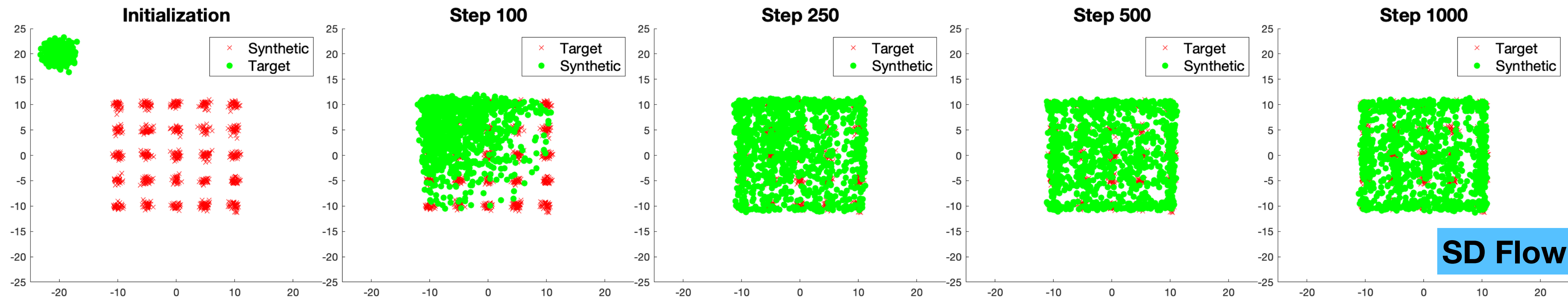
ADAGRAD	BATCH	CONST	ANNEAL
N	Y	N	Y



ADAGRAD	BATCH	CONST	ANNEAL
N	Y	Y	N

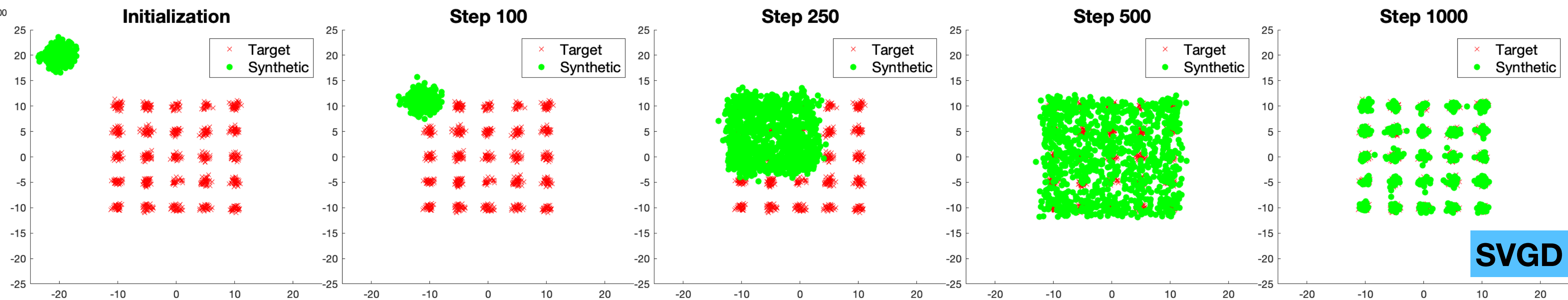
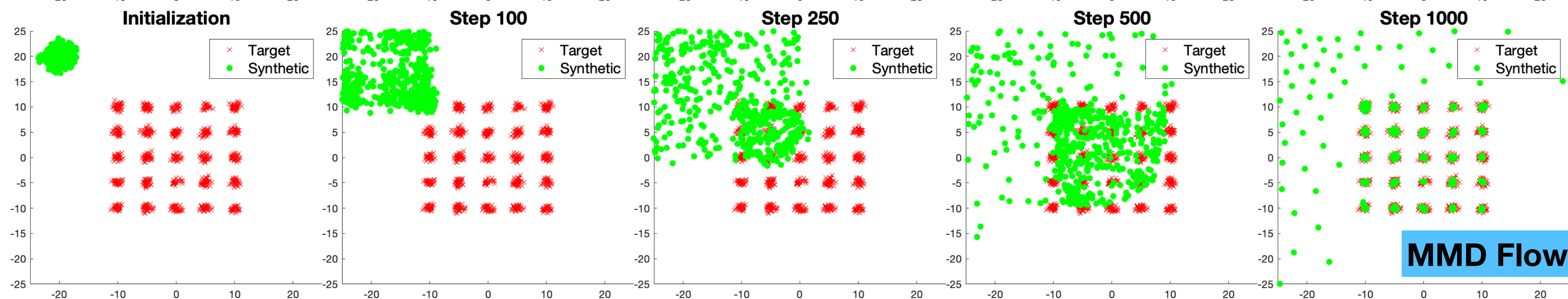
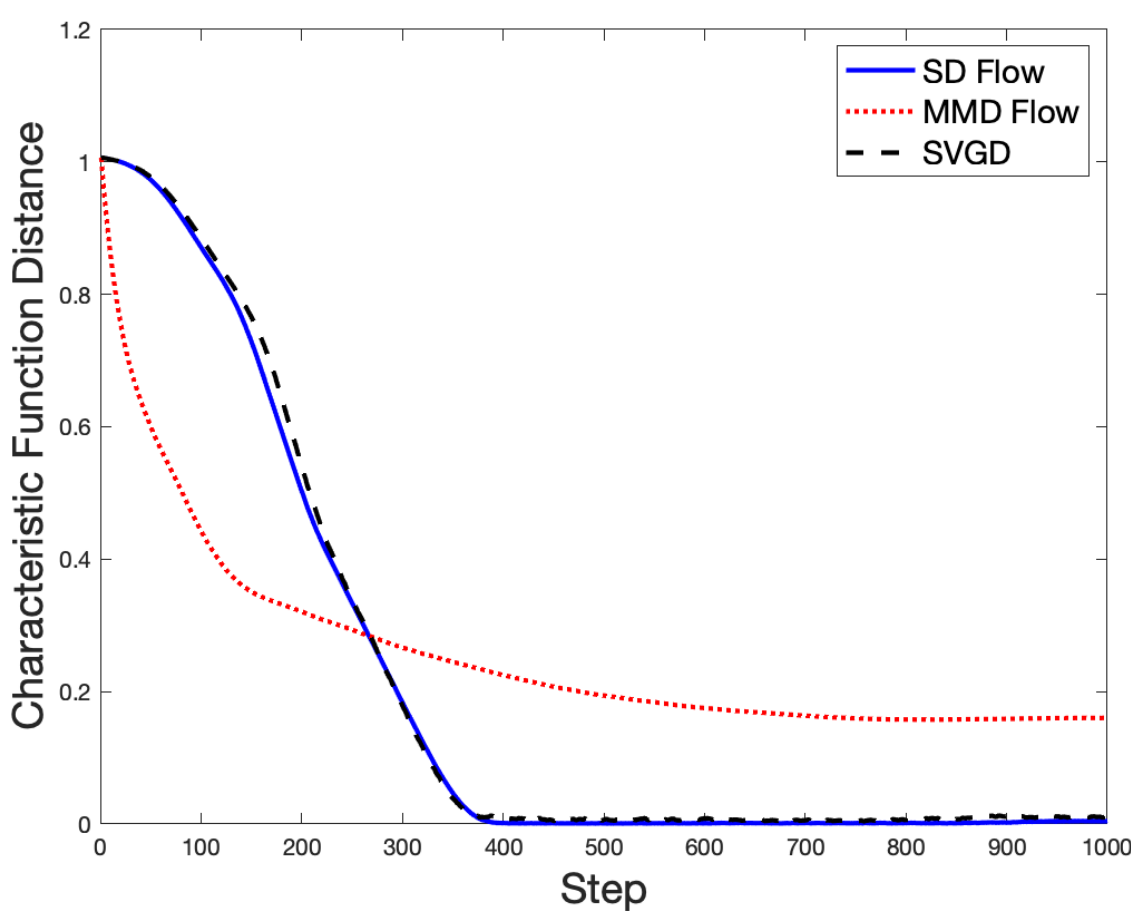
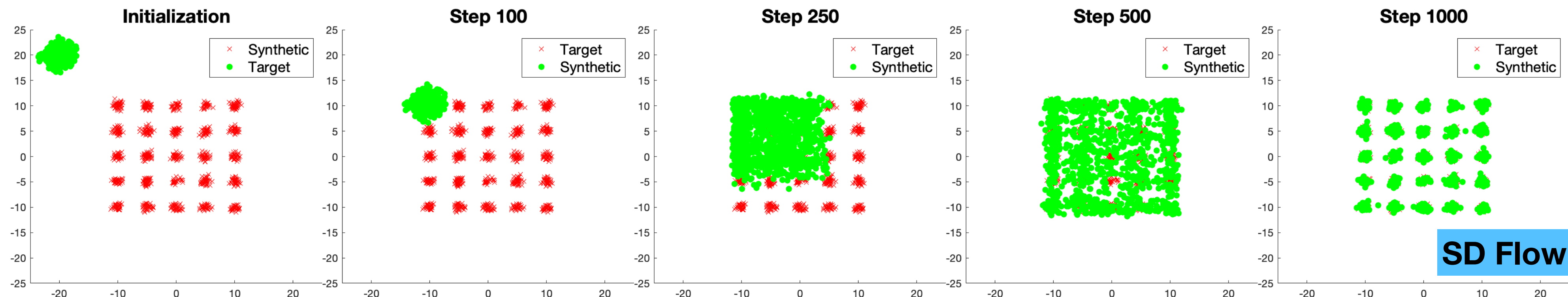


ADAGRAD	BATCH	CONST	ANNEAL
N	Y	Y	Y

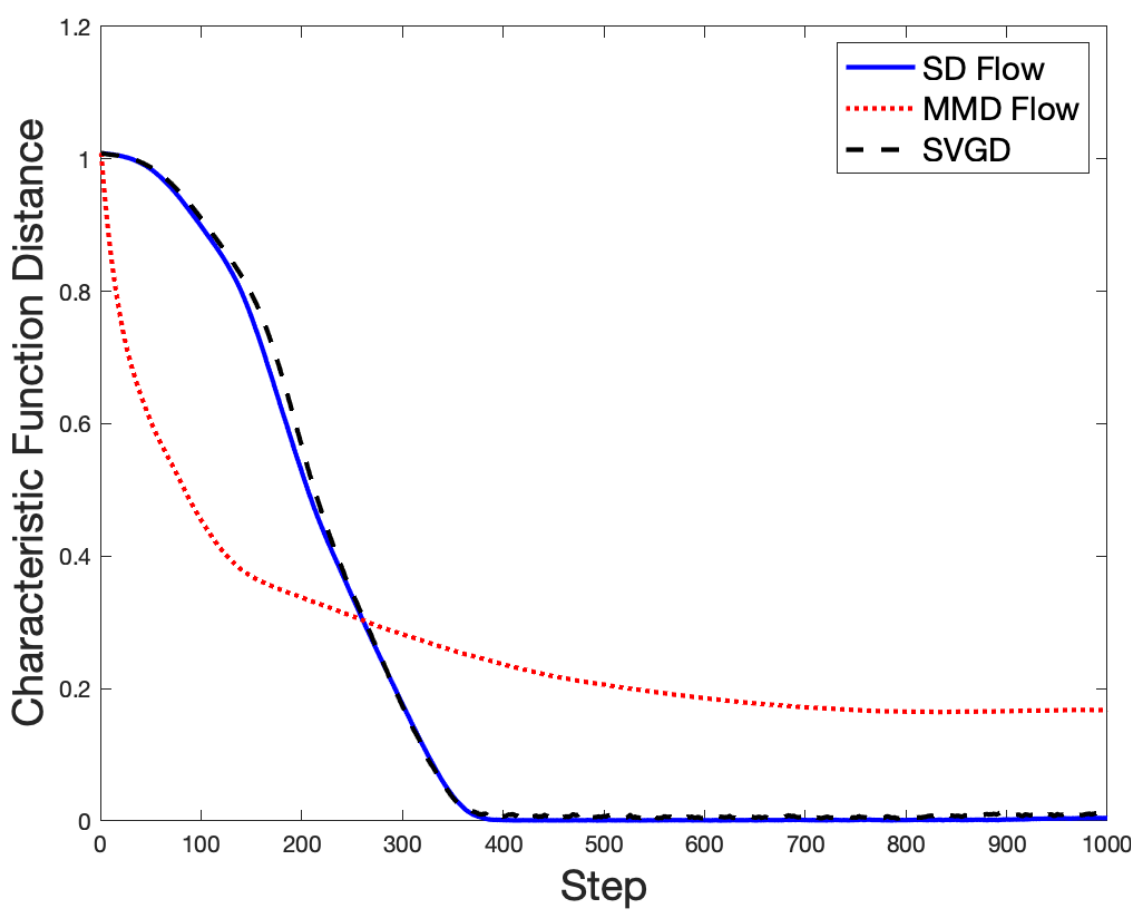
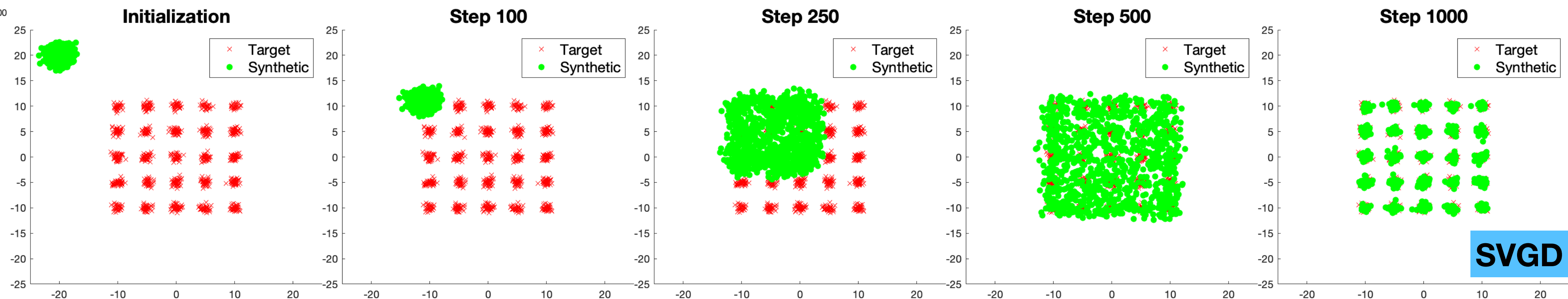
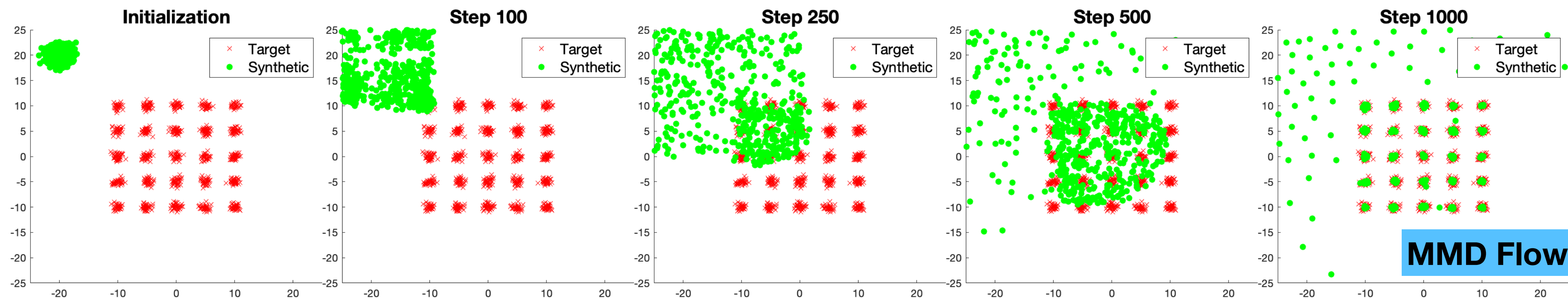
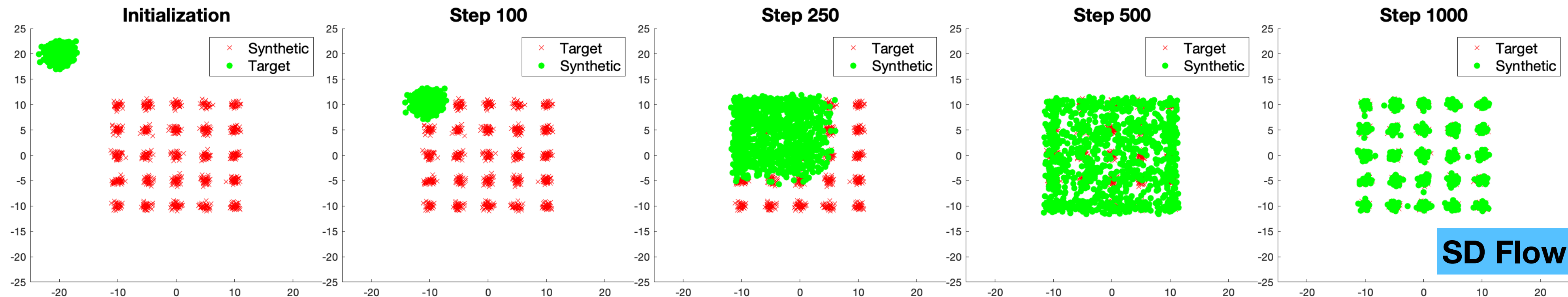




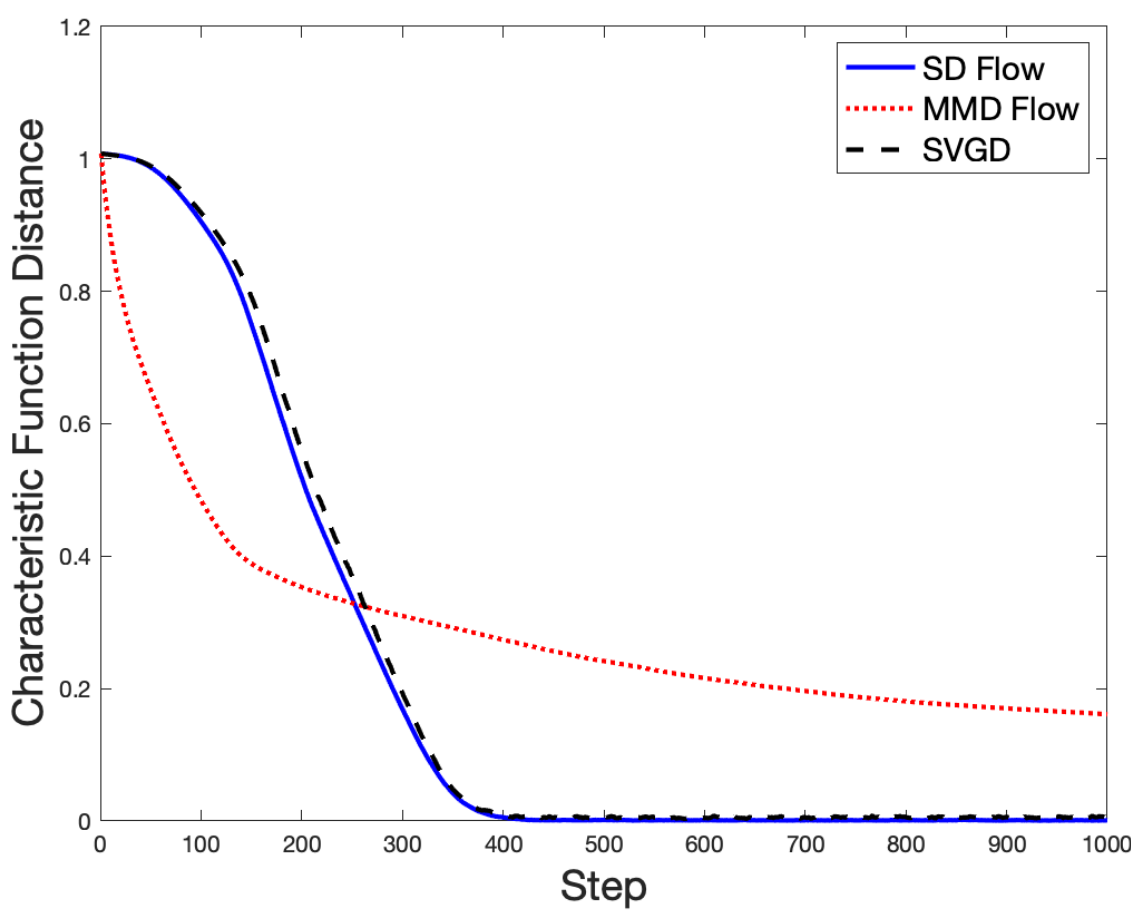
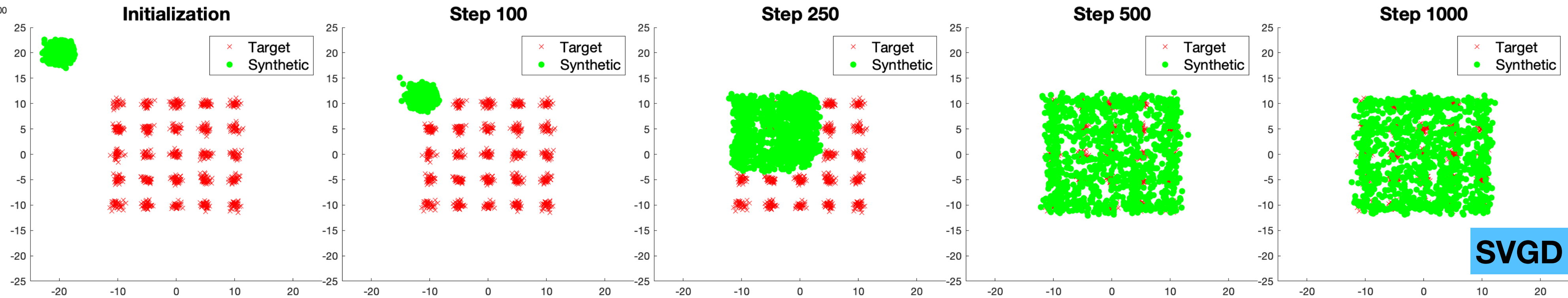
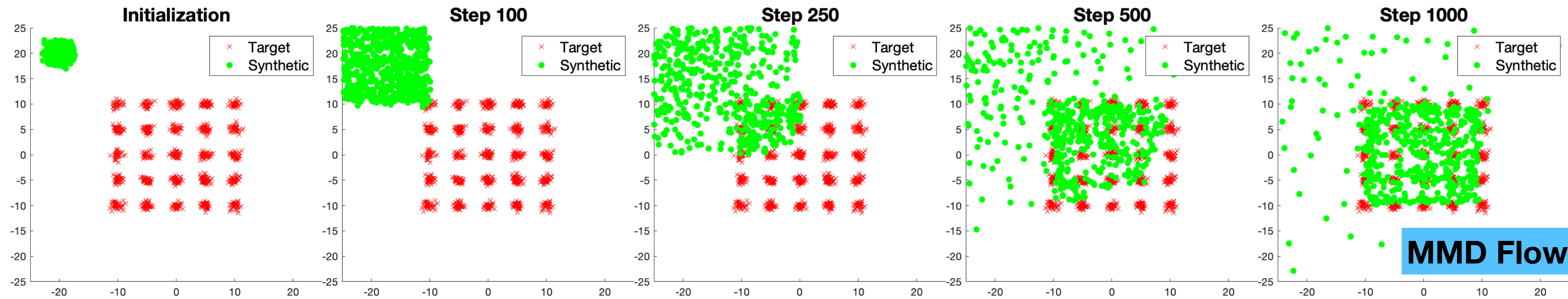
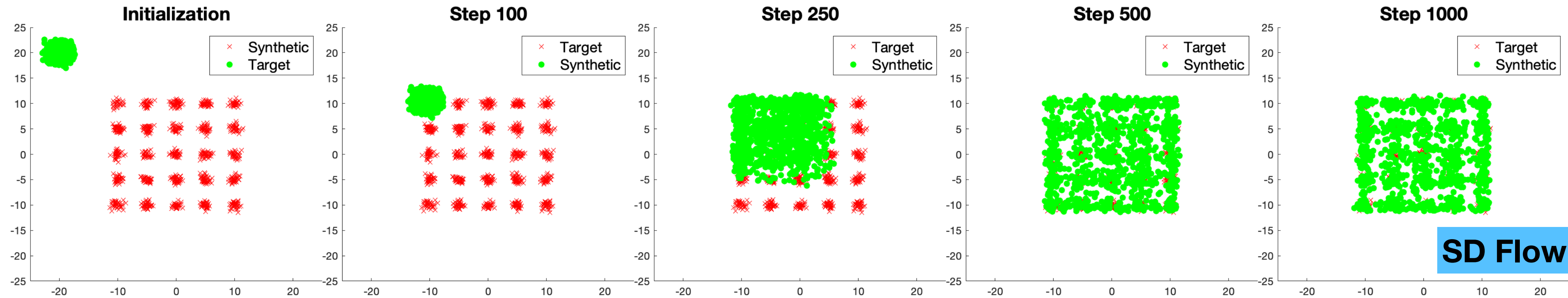
ADAGRAD	BATCH	CONST	ANNEAL
Y	N	N	N



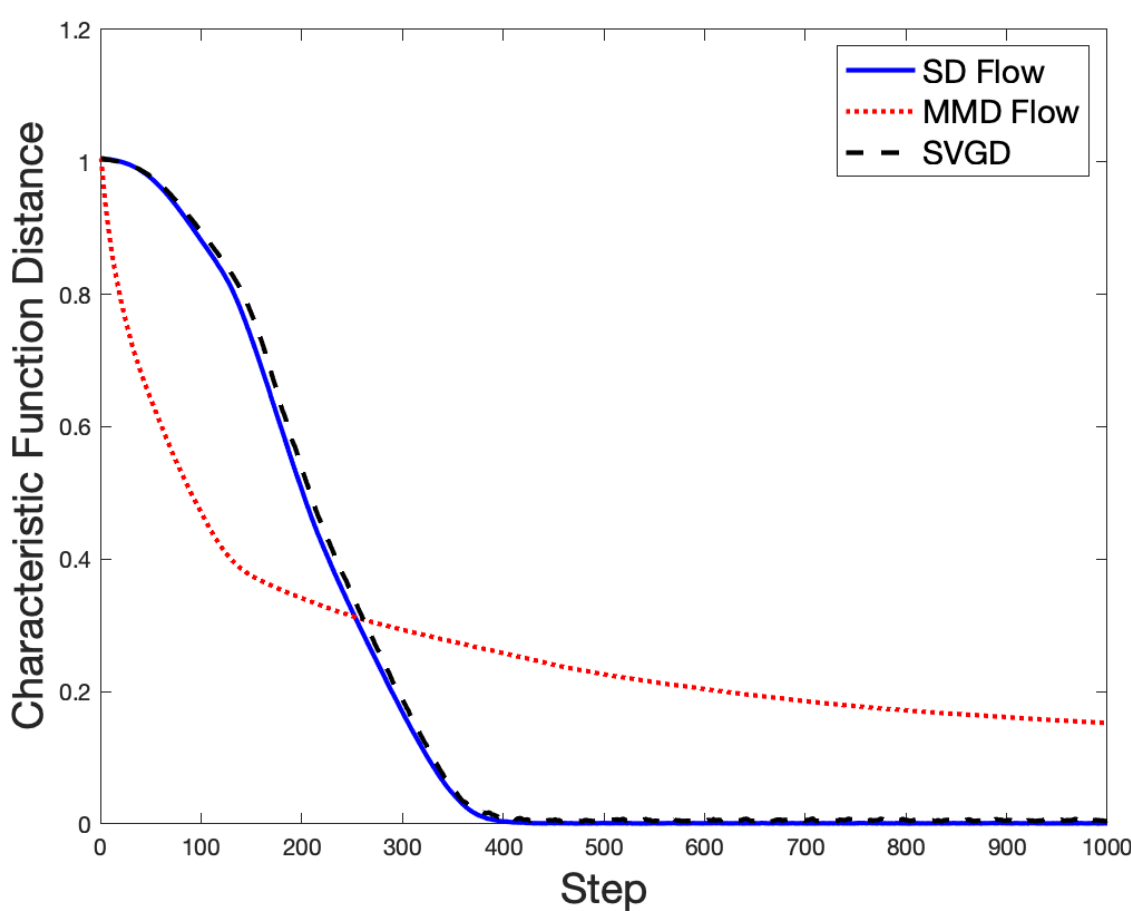
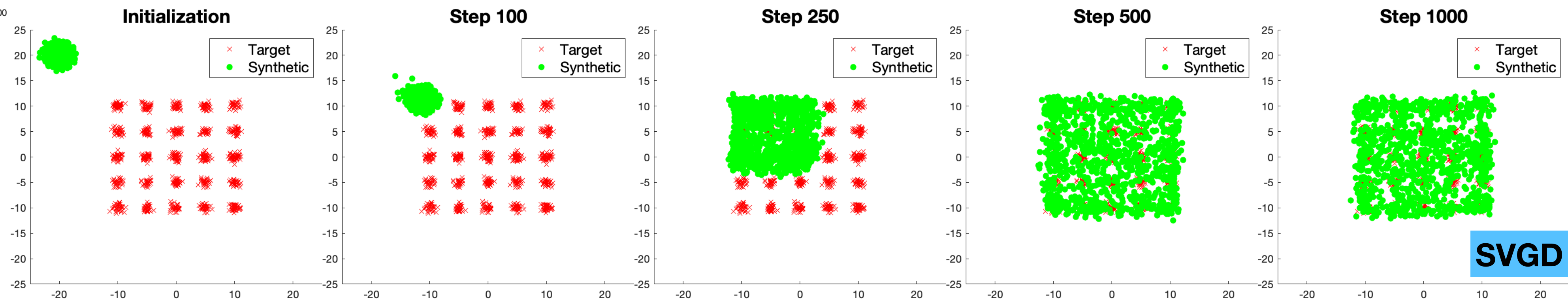
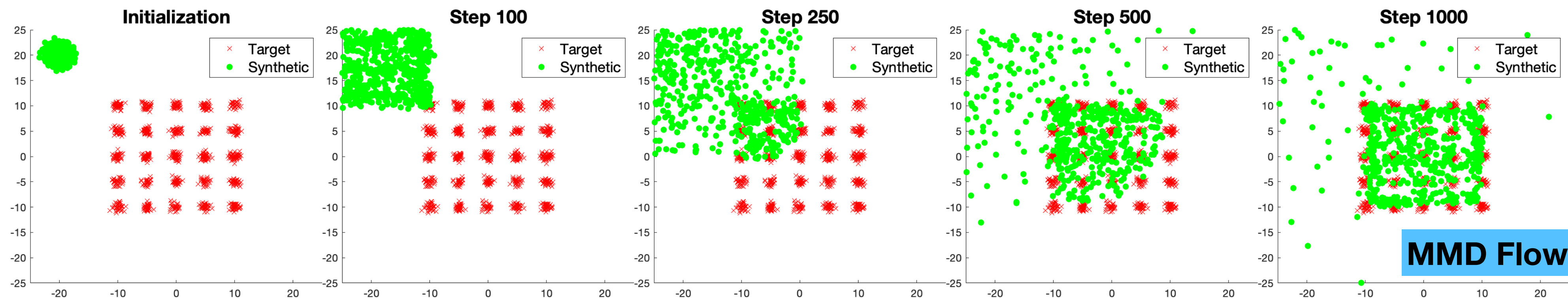
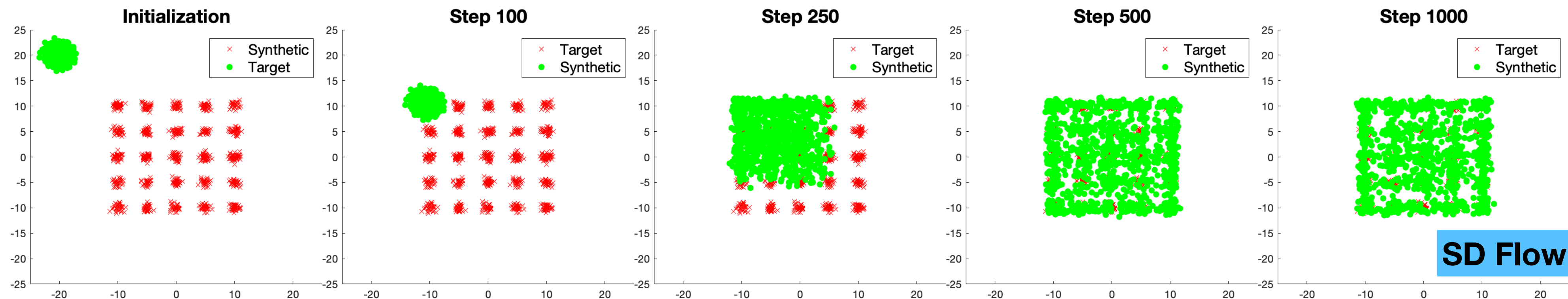
ADAGRAD	BATCH	CONST	ANNEAL
Y	N	N	Y



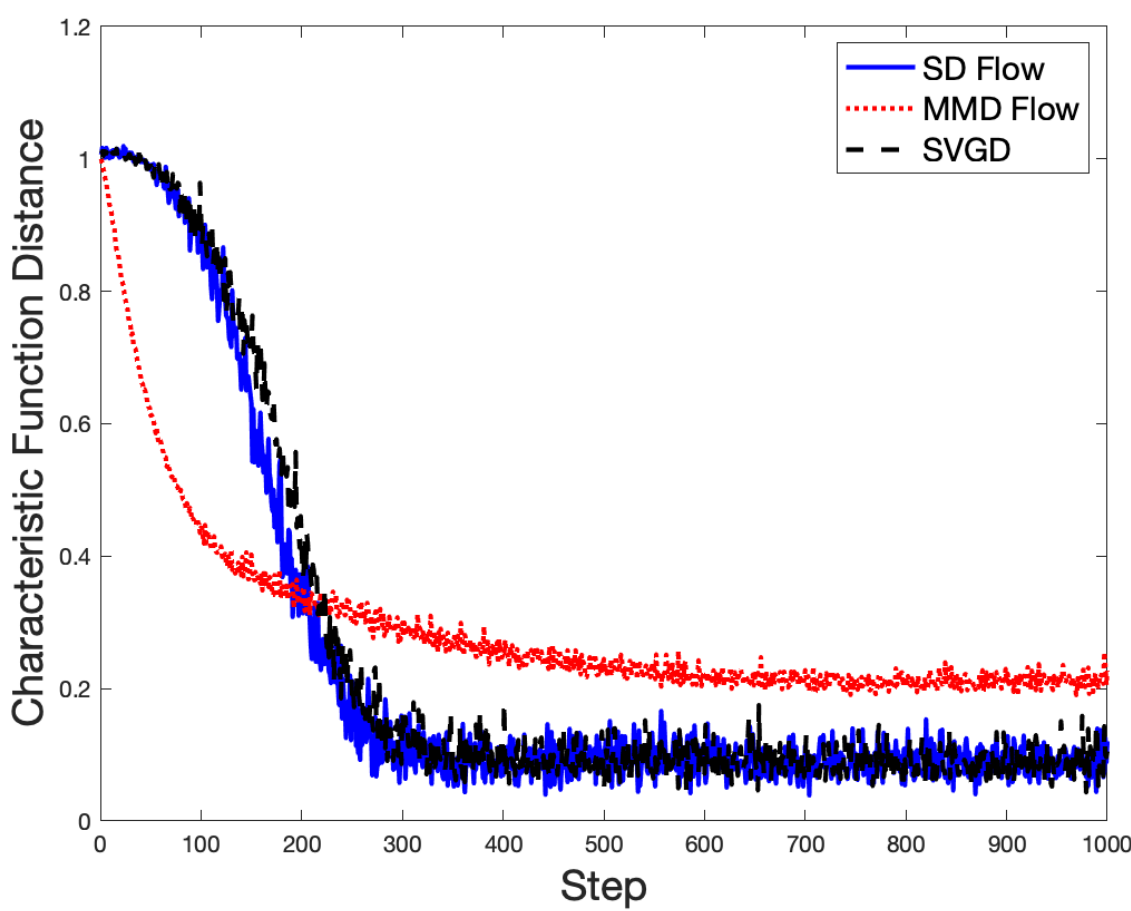
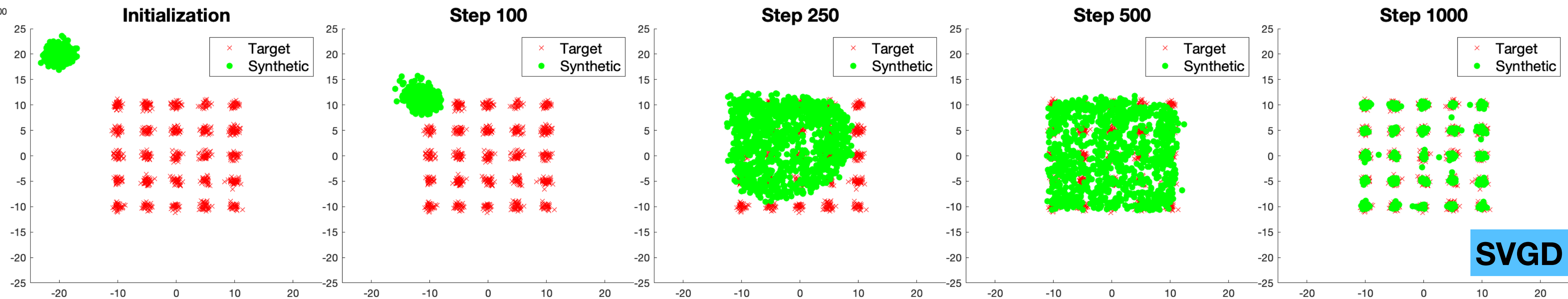
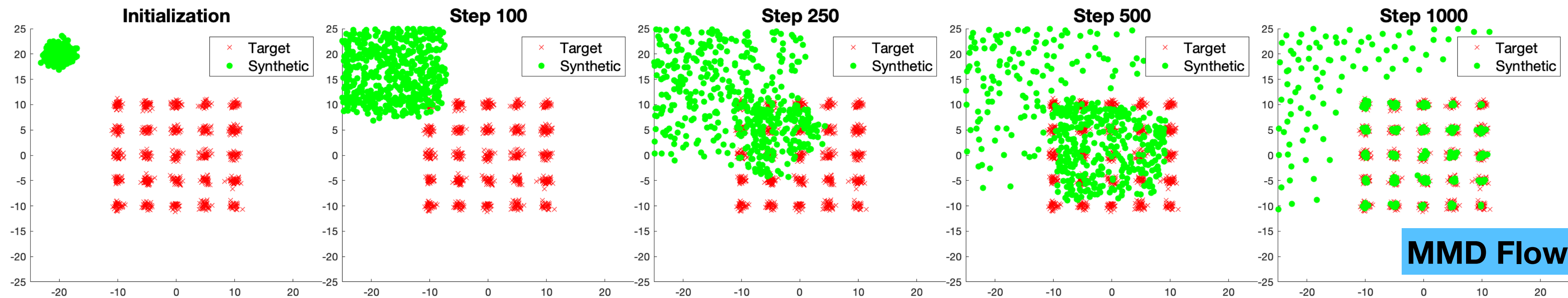
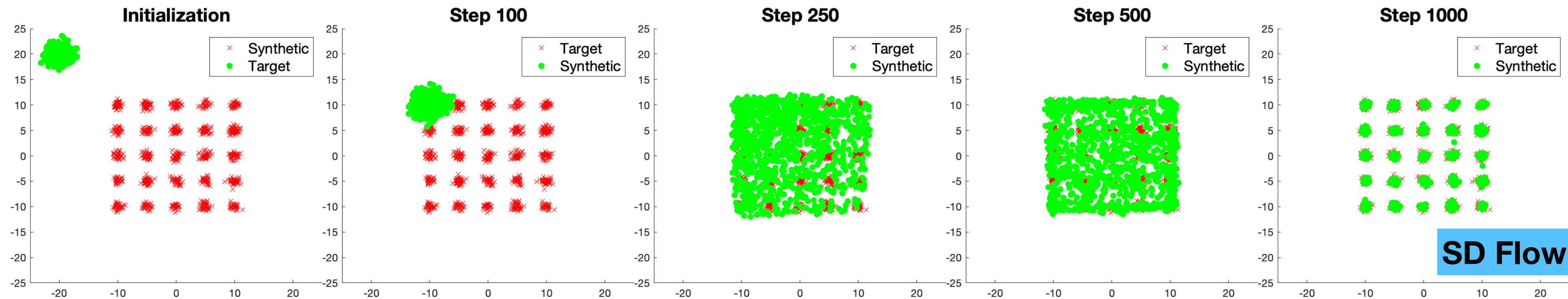
ADAGRAD	BATCH	CONST	ANNEAL
Y	N	Y	N



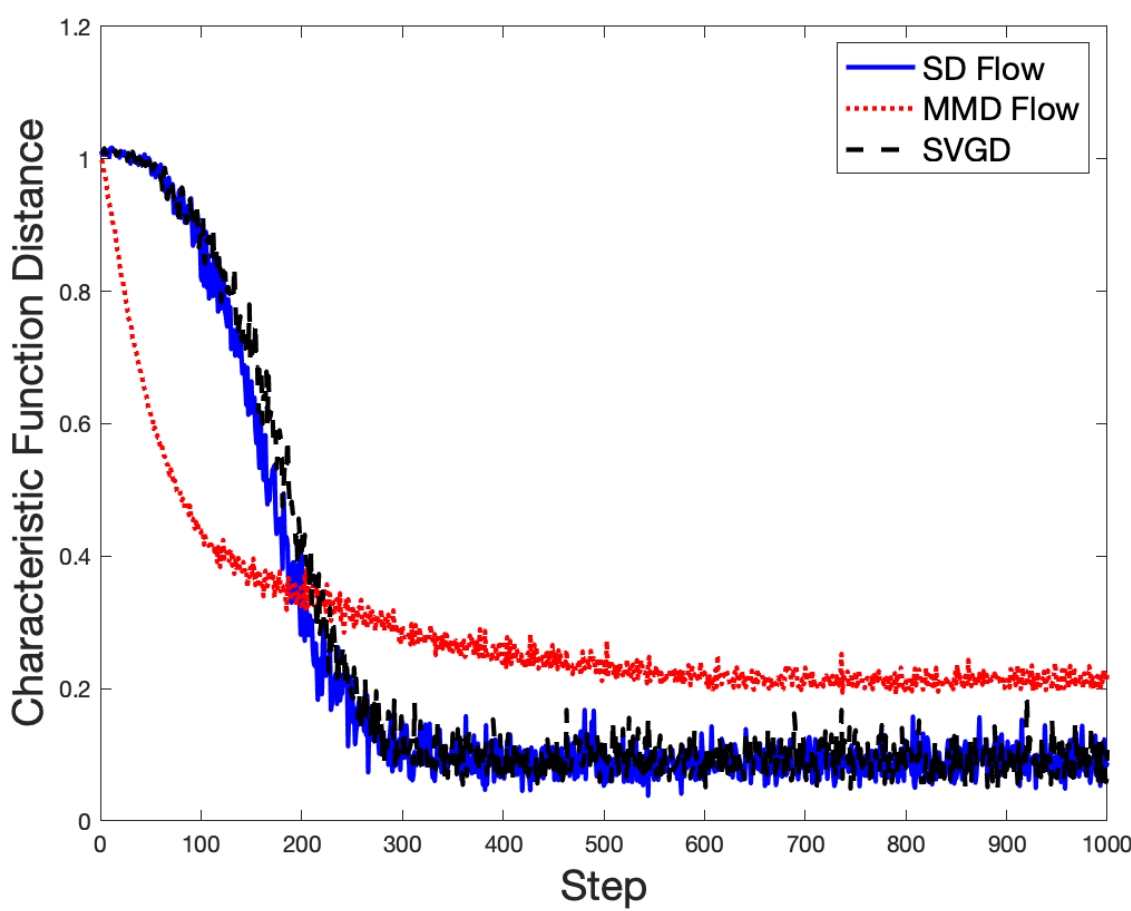
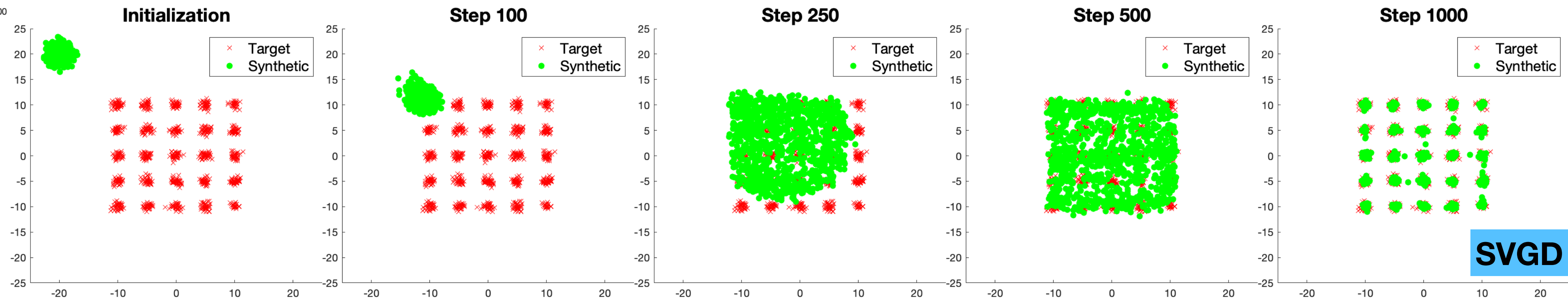
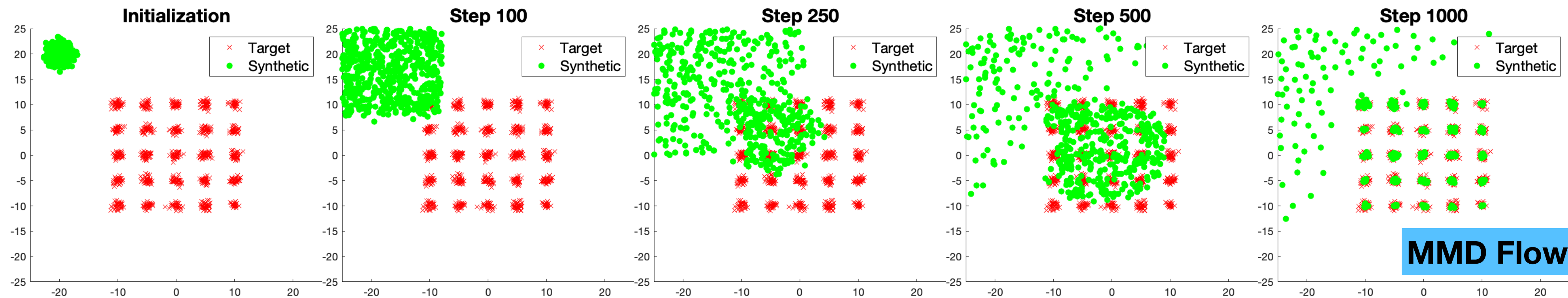
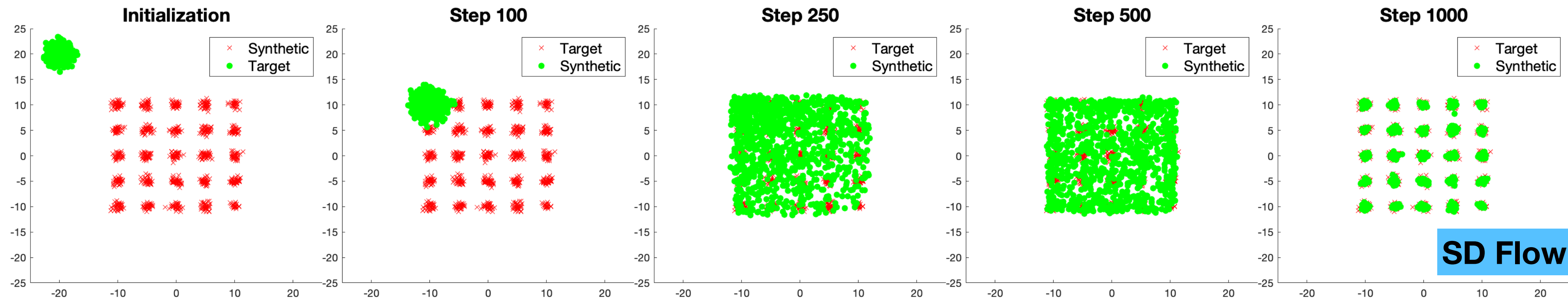
ADAGRAD	BATCH	CONST	ANNEAL
Y	N	Y	Y



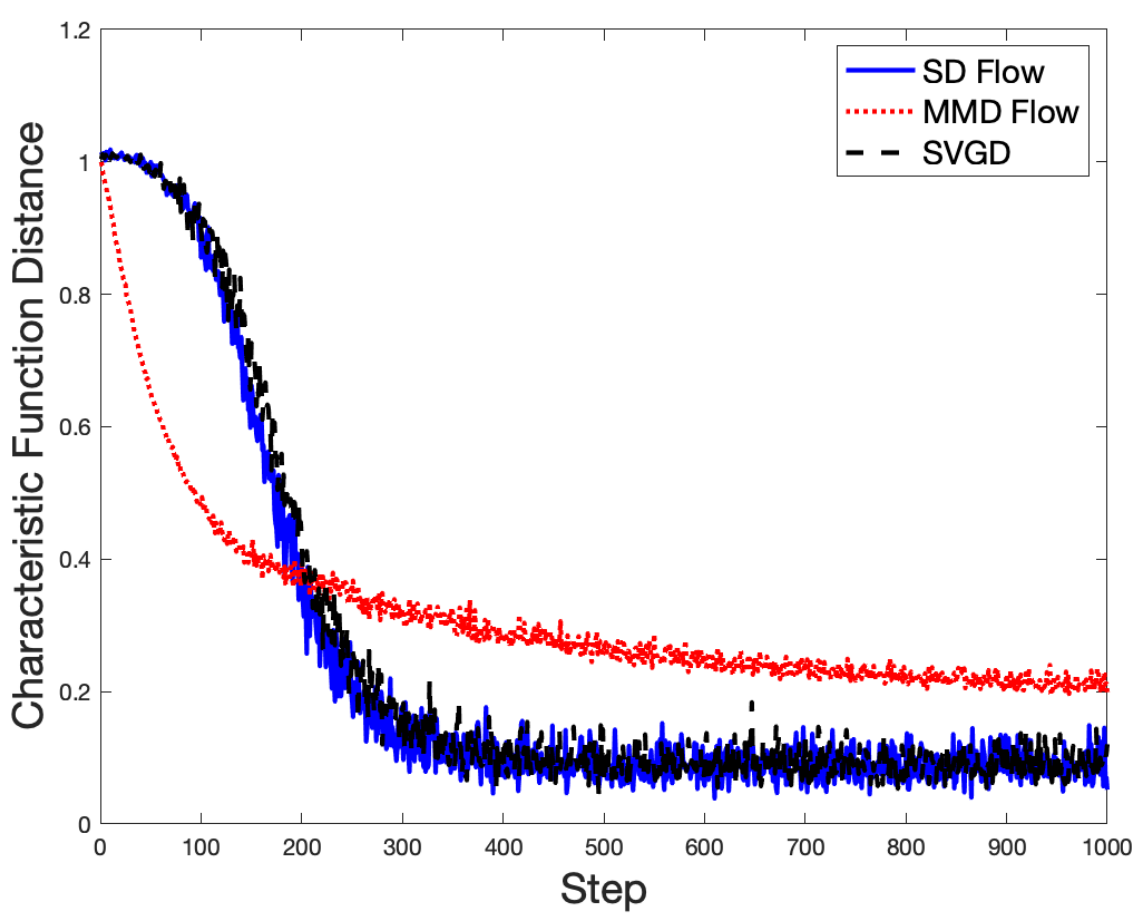
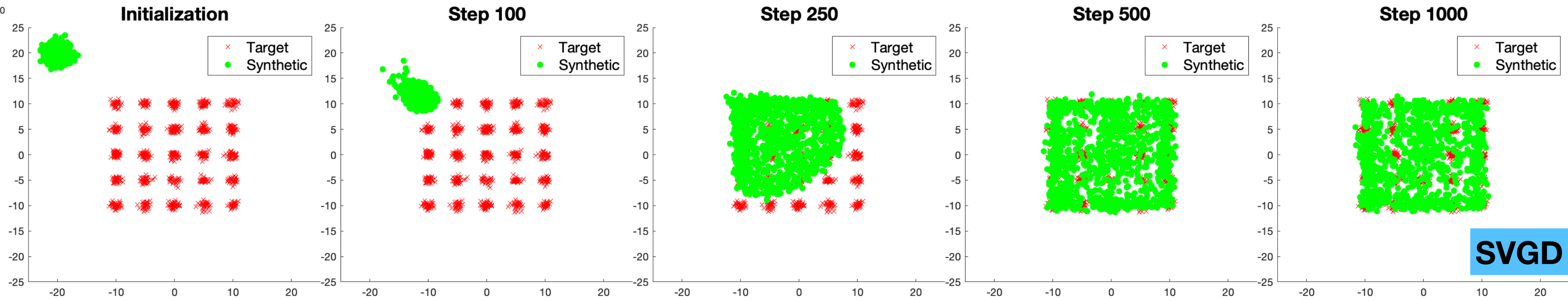
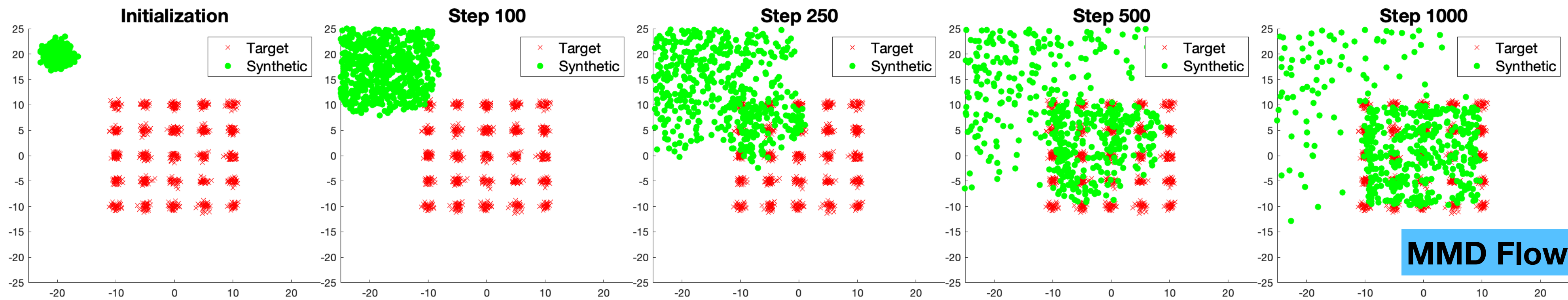
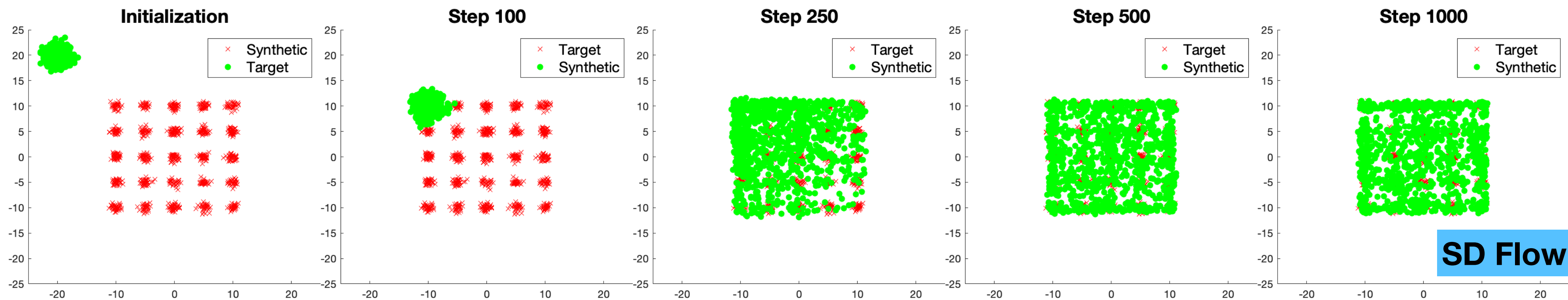
ADAGRAD	BATCH	CONST	ANNEAL
Y	Y	N	N



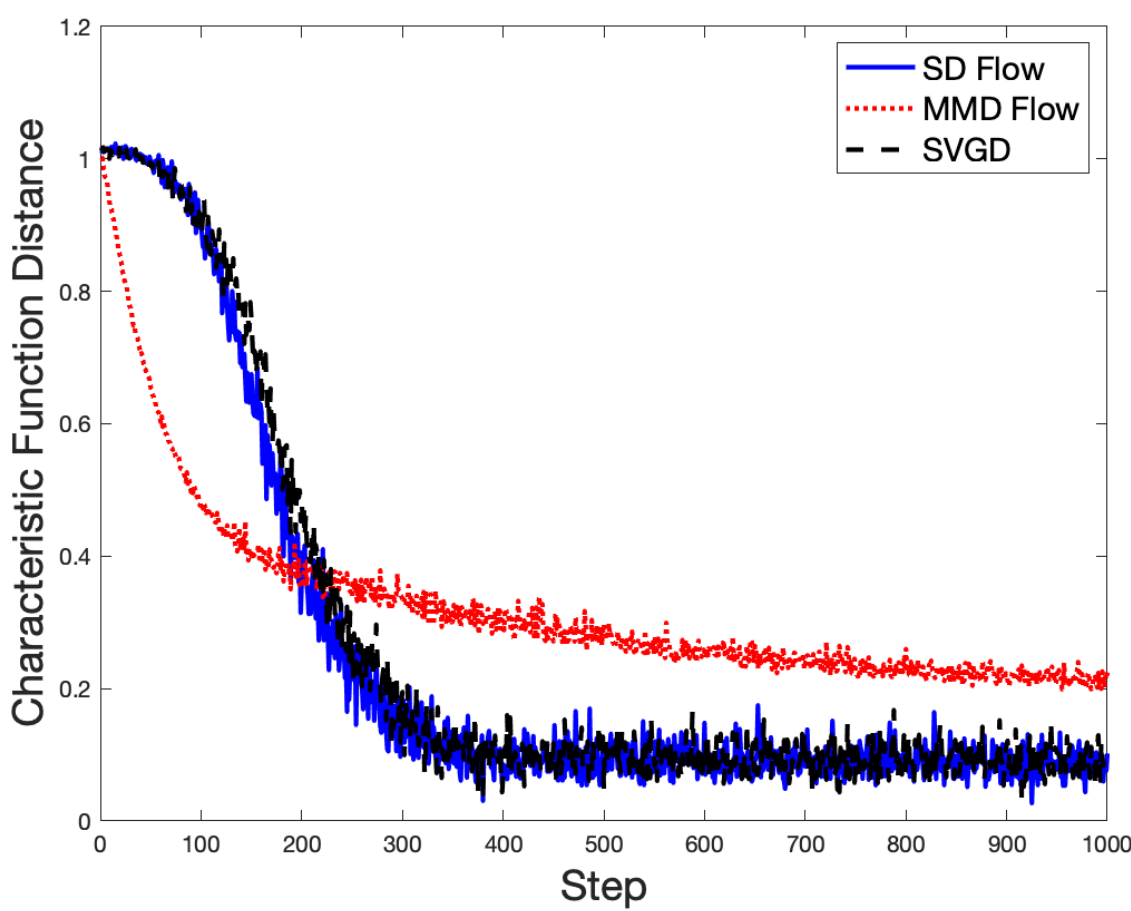
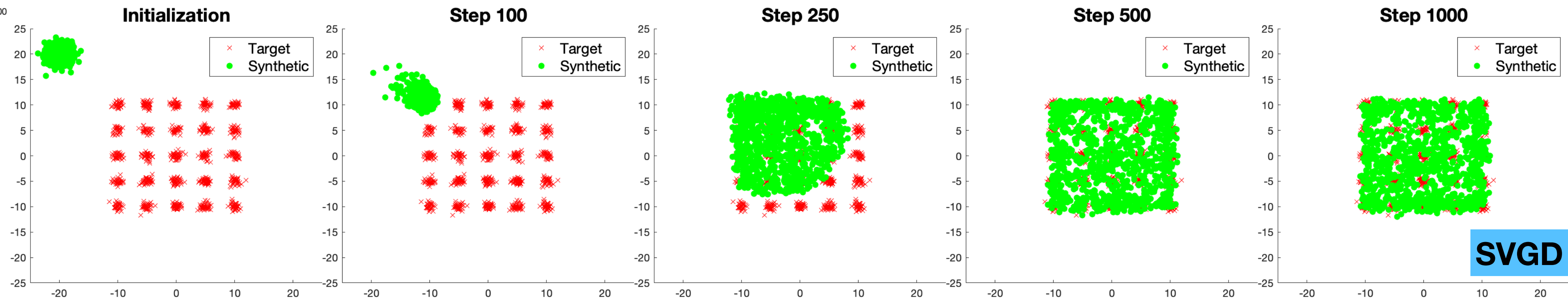
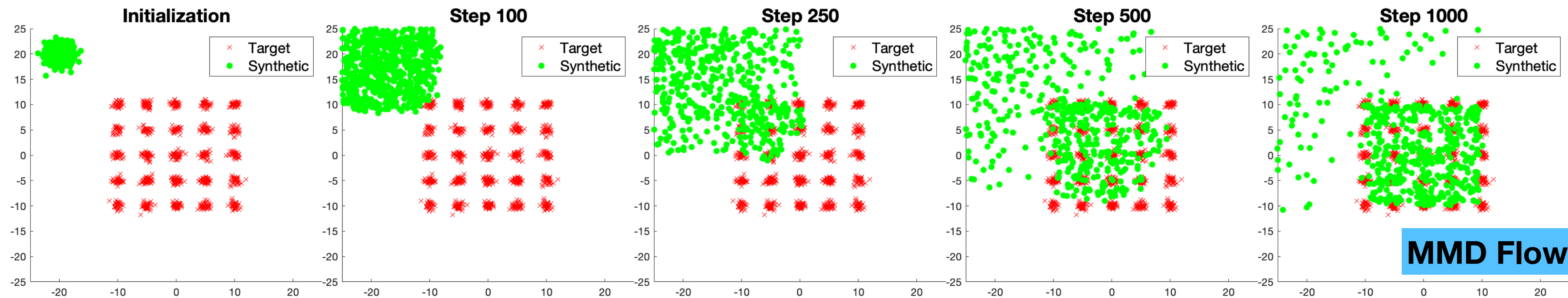
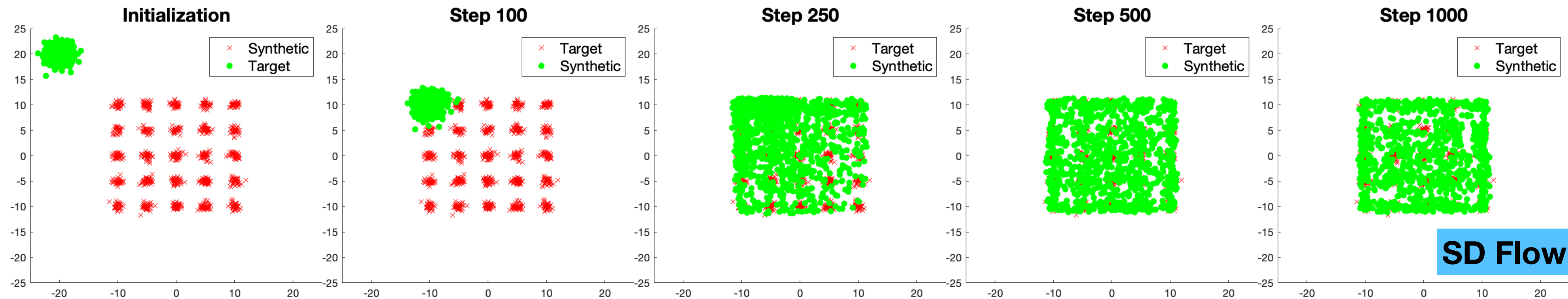
ADAGRAD	BATCH	CONST	ANNEAL
Y	Y	N	Y



ADAGRAD	BATCH	CONST	ANNEAL
Y	Y	Y	N

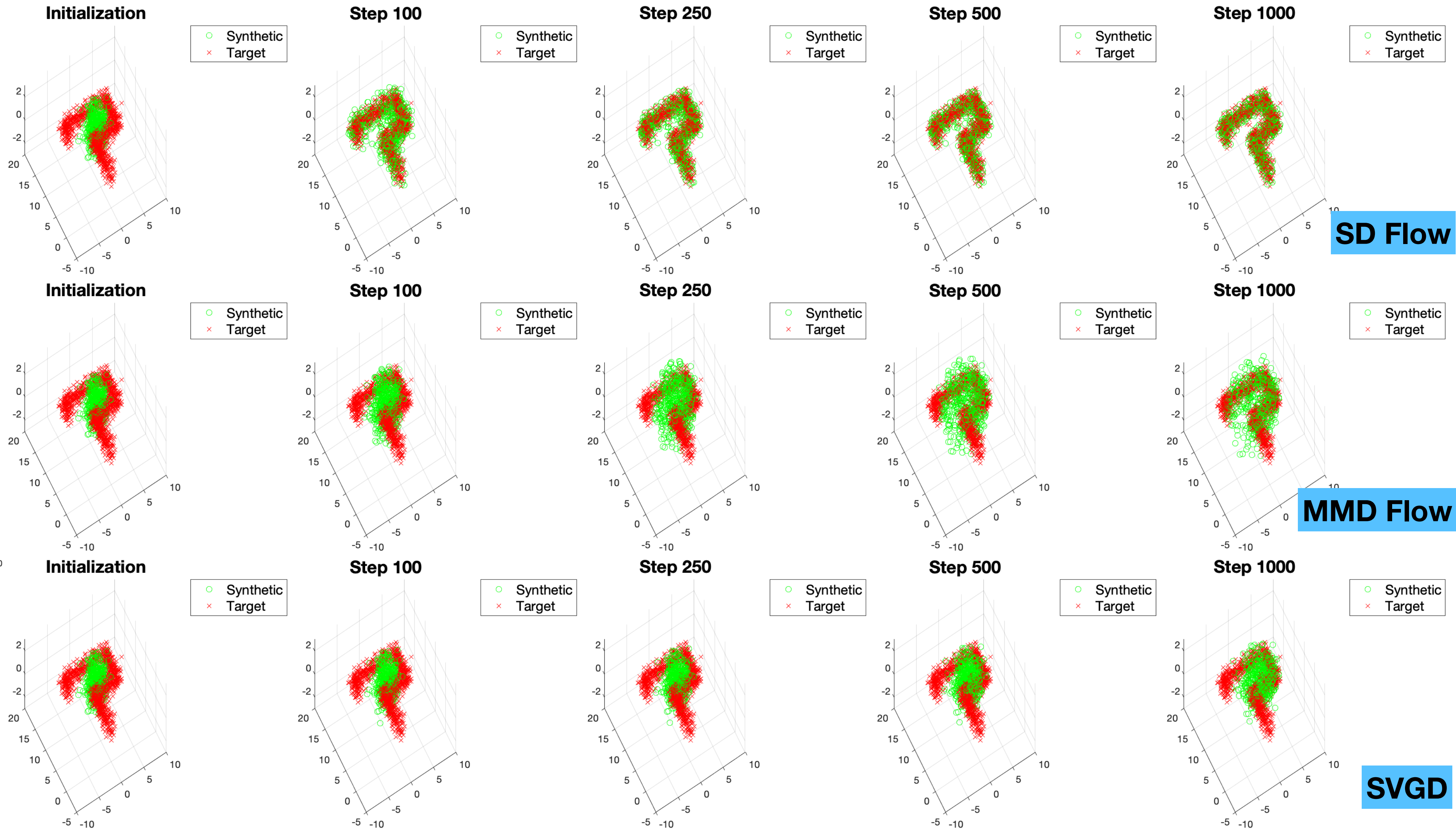
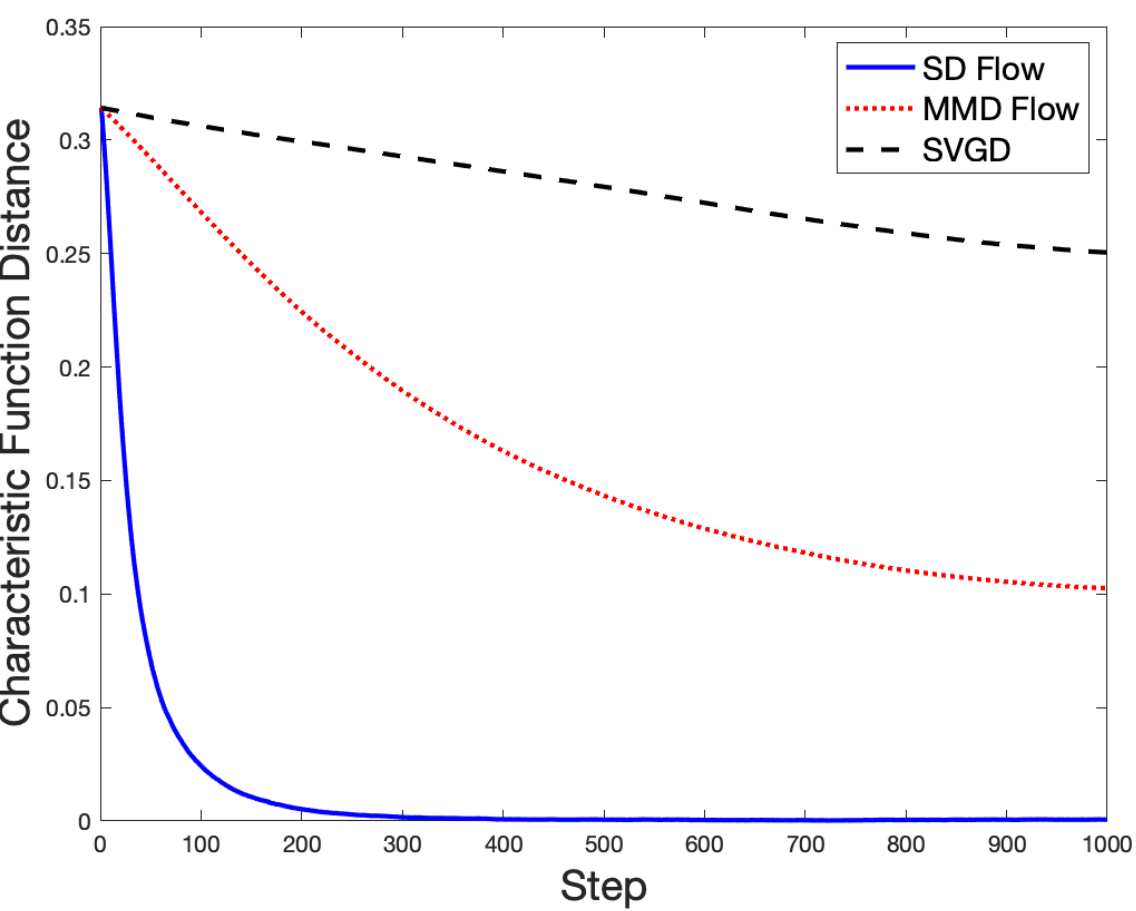


ADAGRAD	BATCH	CONST	ANNEAL
Y	Y	Y	Y

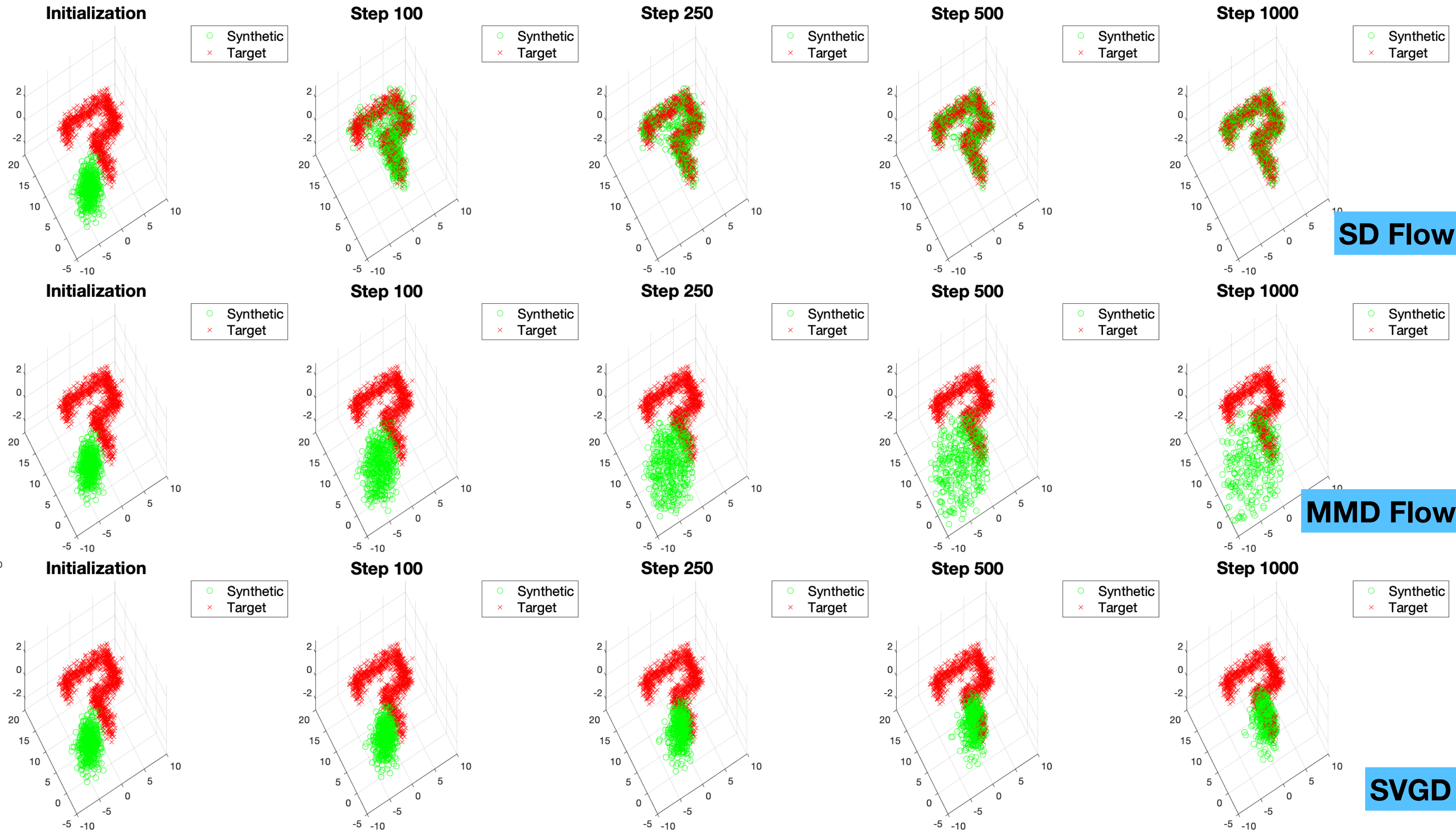
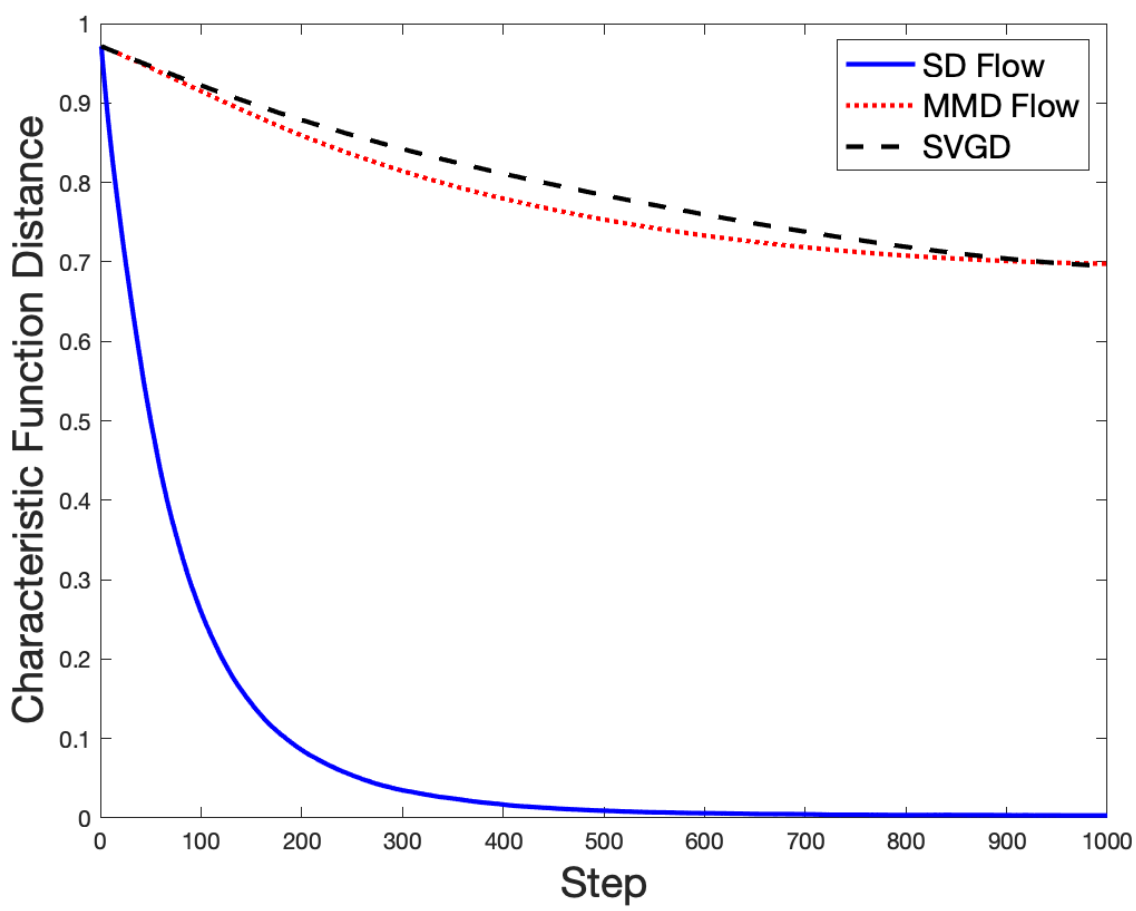




ADAGRAD	BATCH	CONST	ANNEAL	OFFSET
N	N	N	N	N



ADAGRAD	BATCH	CONST	ANNEAL	OFFSET
N	N	N	N	Y

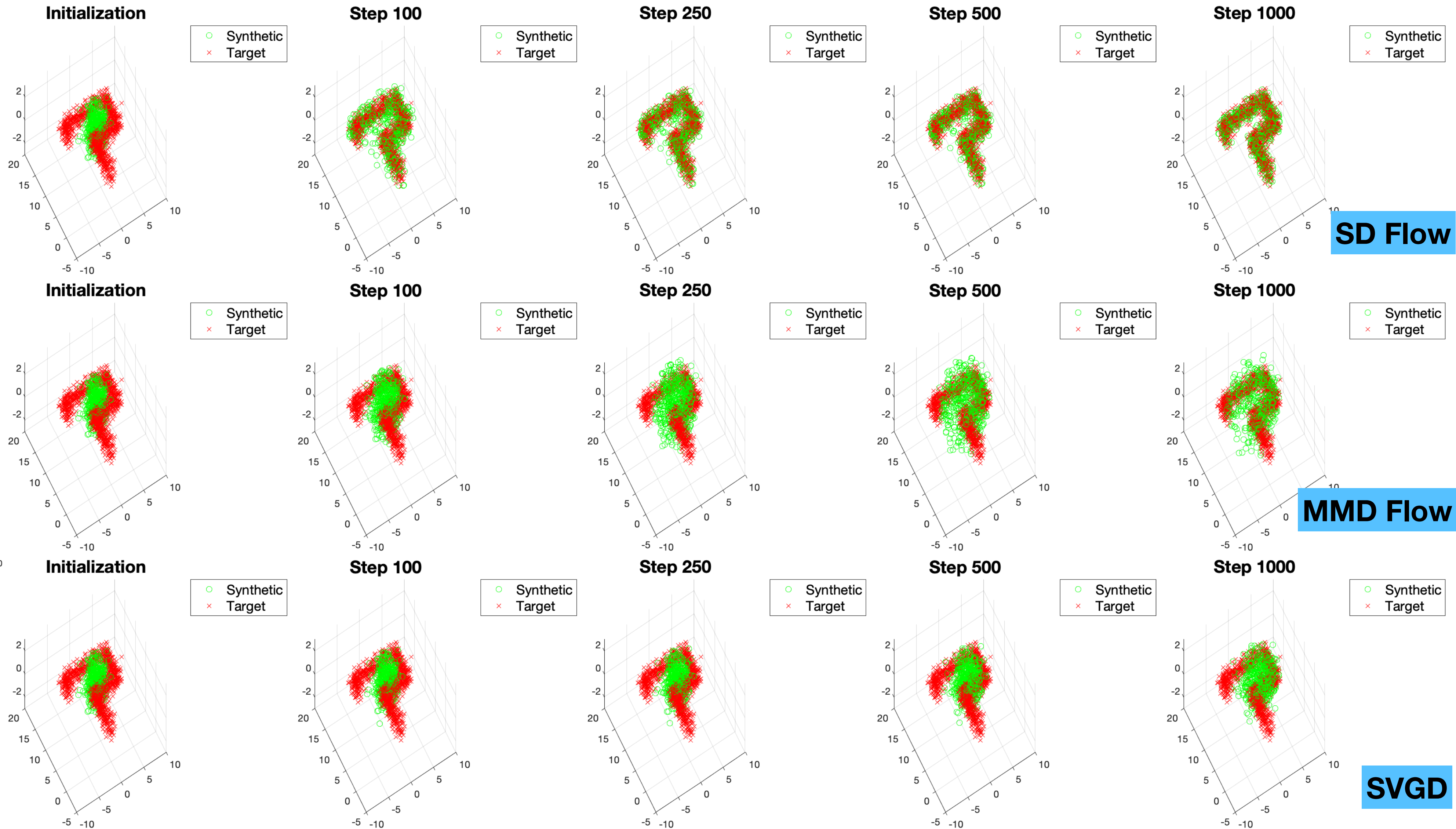
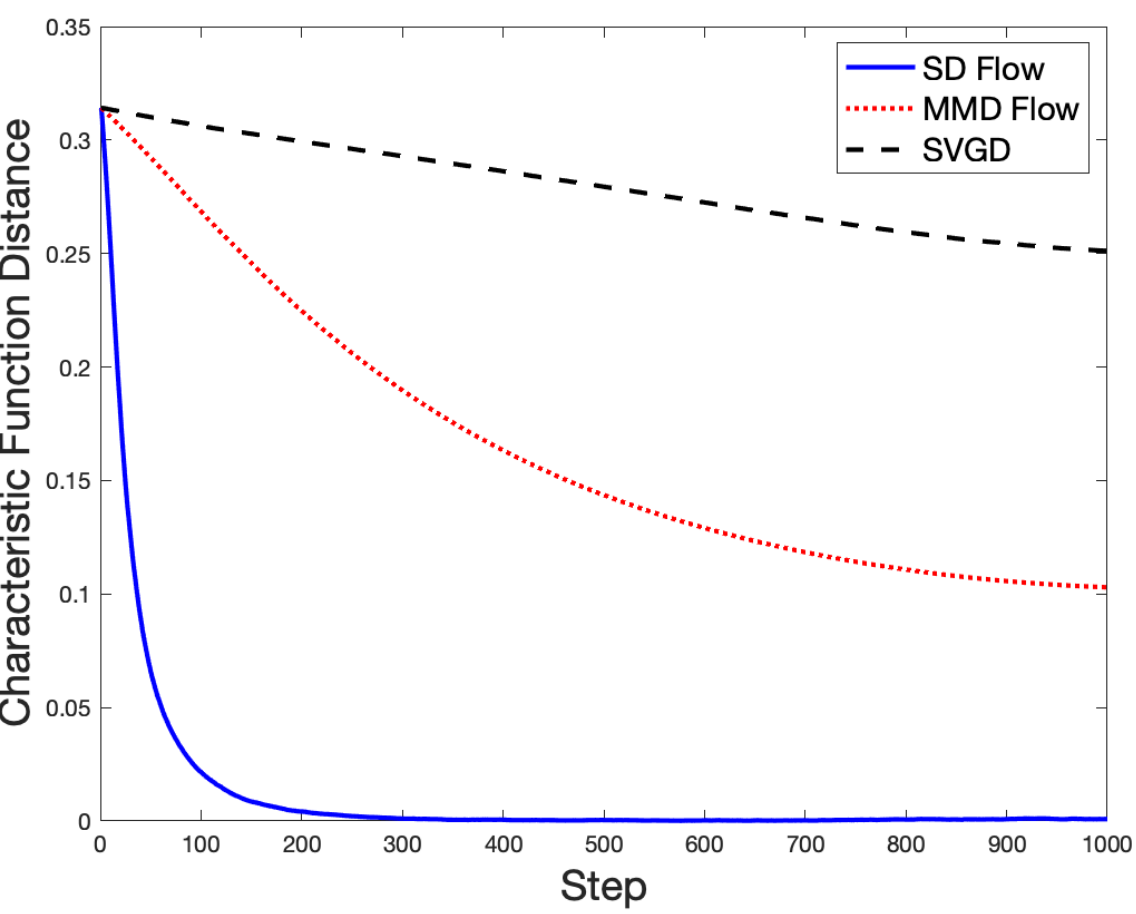


**SD Flow**

**MMD Flow**

**SVGD**

ADAGRAD	BATCH	CONST	ANNEAL	OFFSET
N	N	N	Y	N

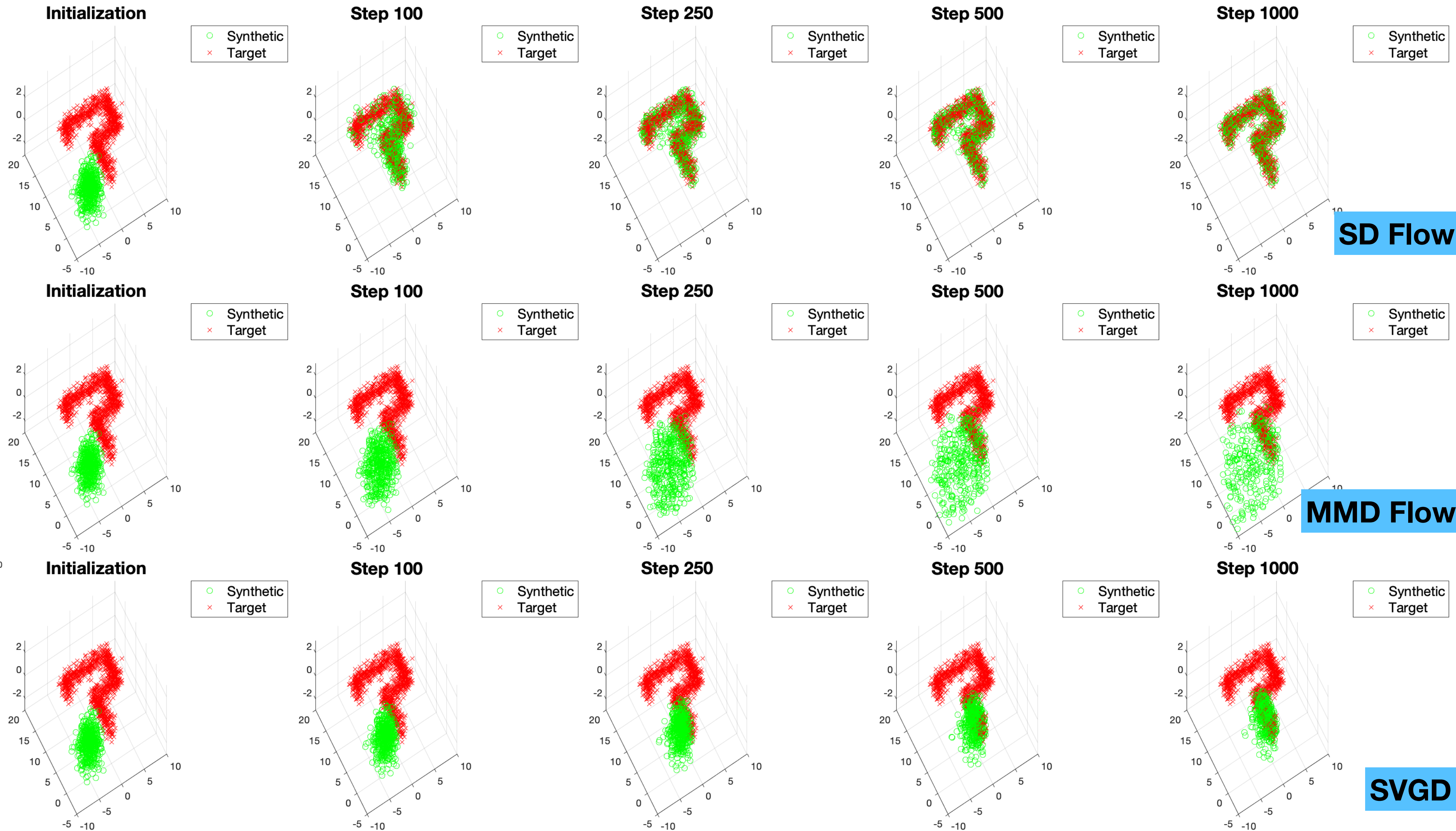
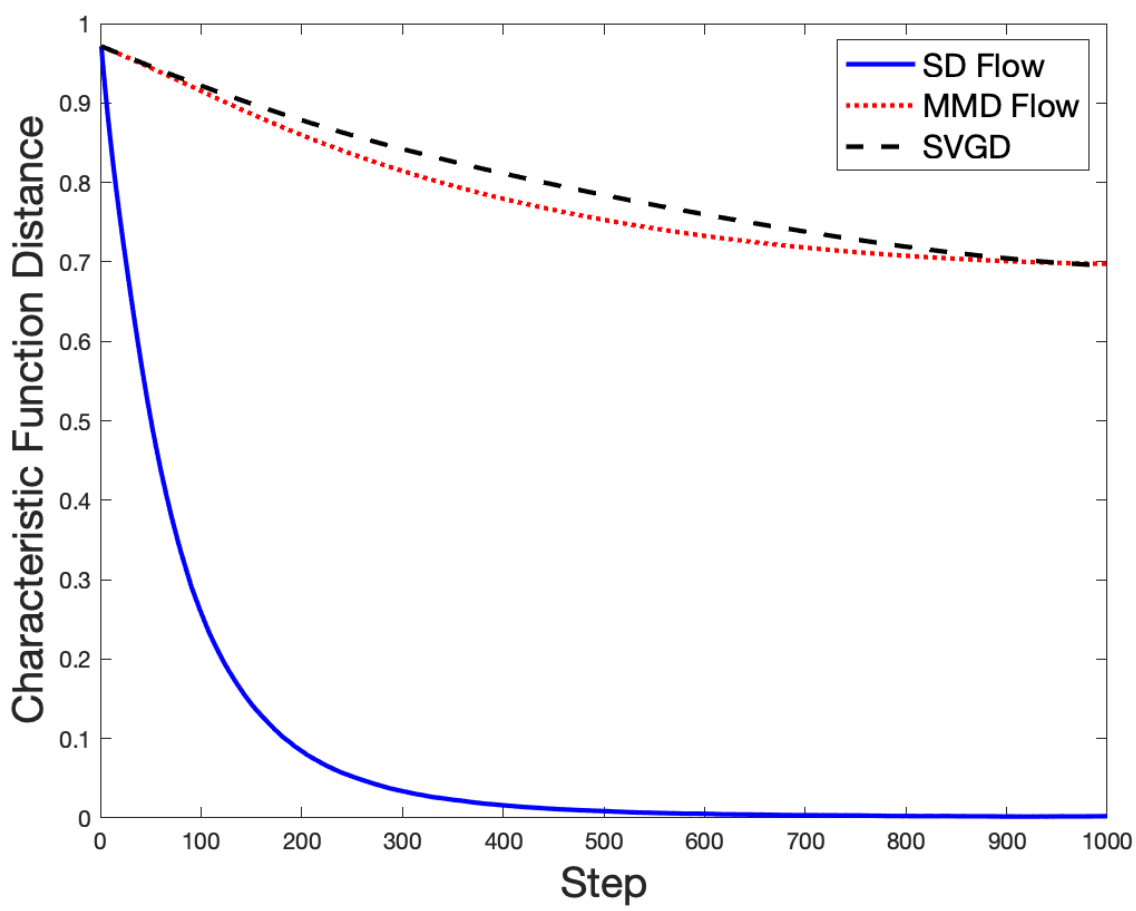


**SD Flow**

**MMD Flow**

**SVGD**

ADAGRAD	BATCH	CONST	ANNEAL	OFFSET
N	N	N	Y	Y

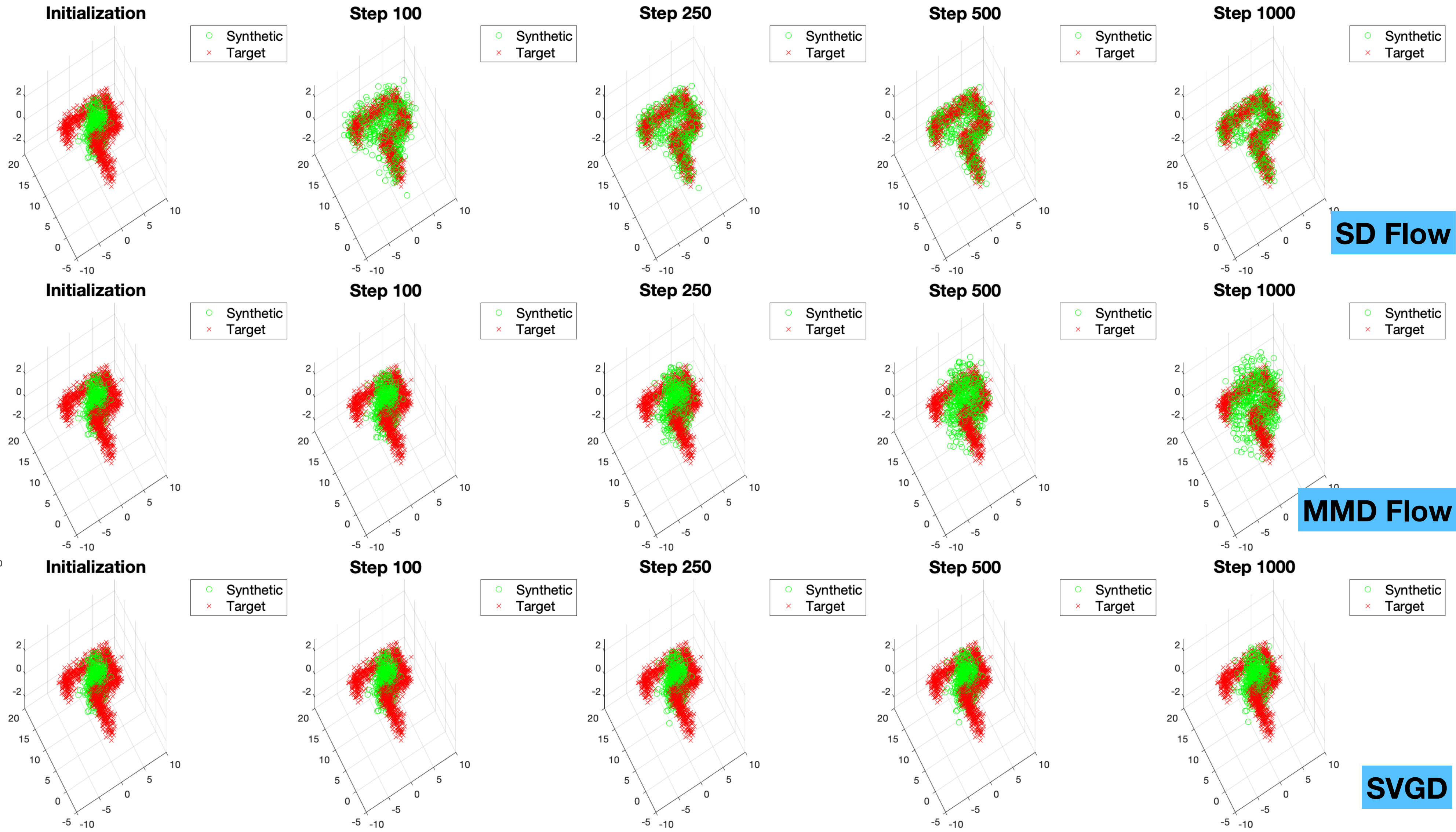
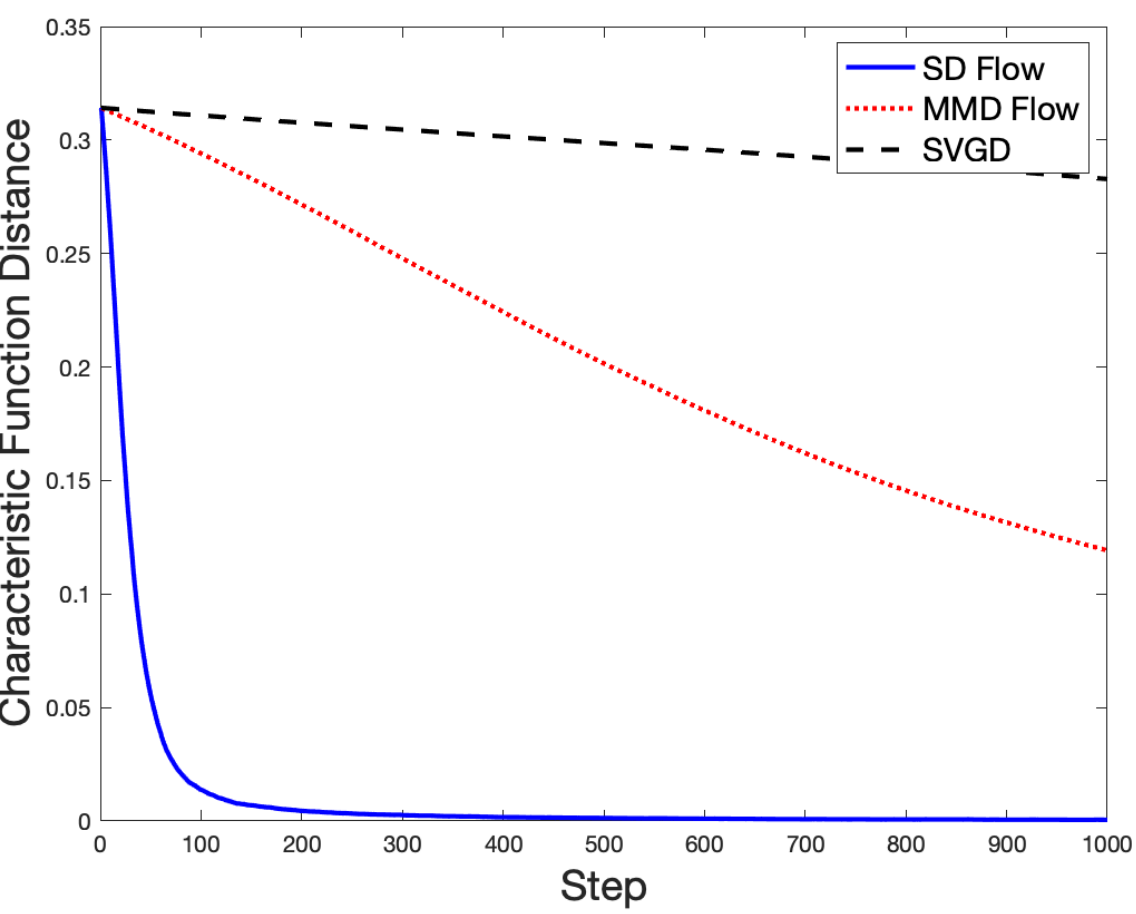


**SD Flow**

**MMD Flow**

**SVGD**

ADAGRAD	BATCH	CONST	ANNEAL	OFFSET
N	N	Y	N	N

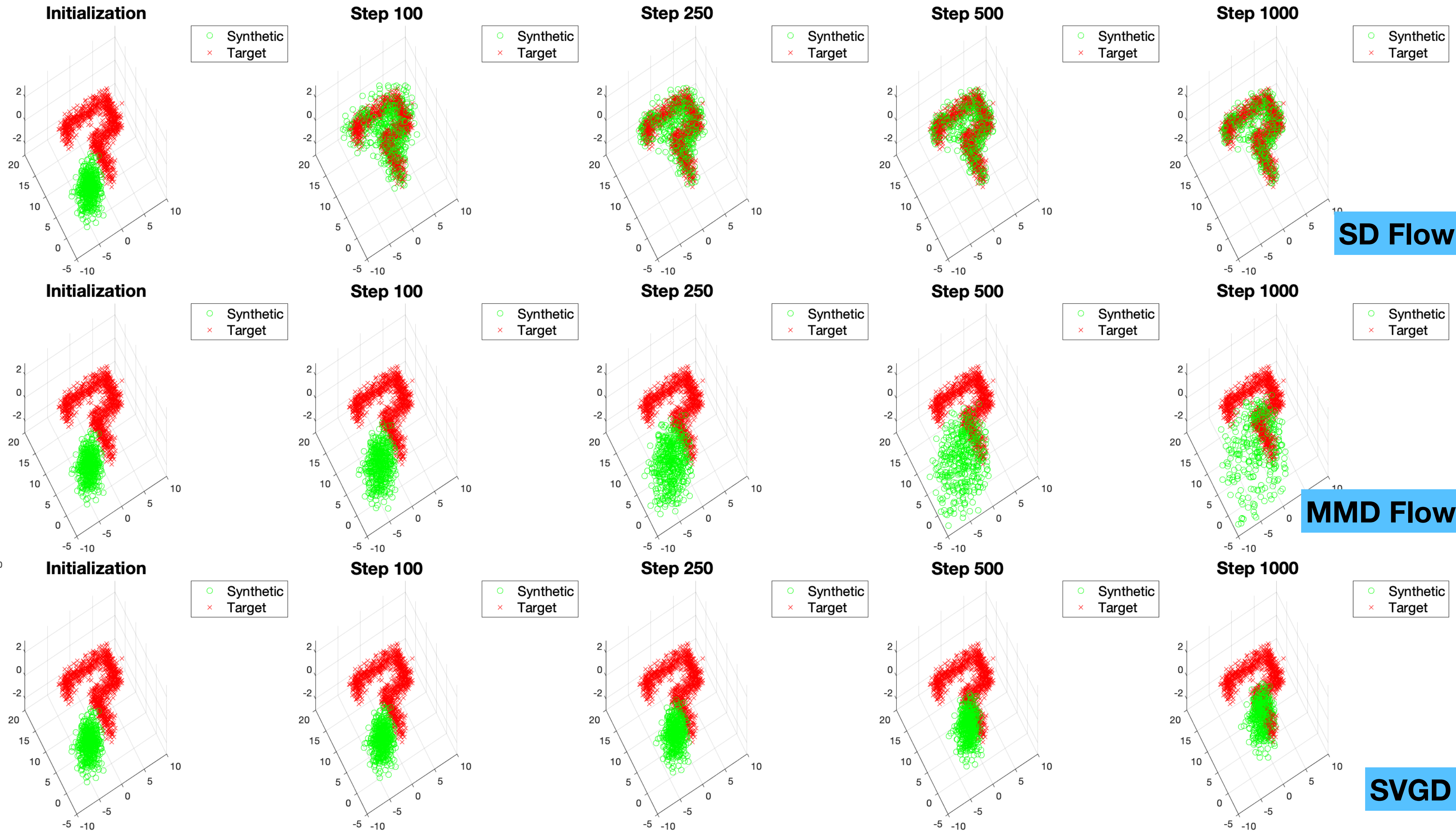
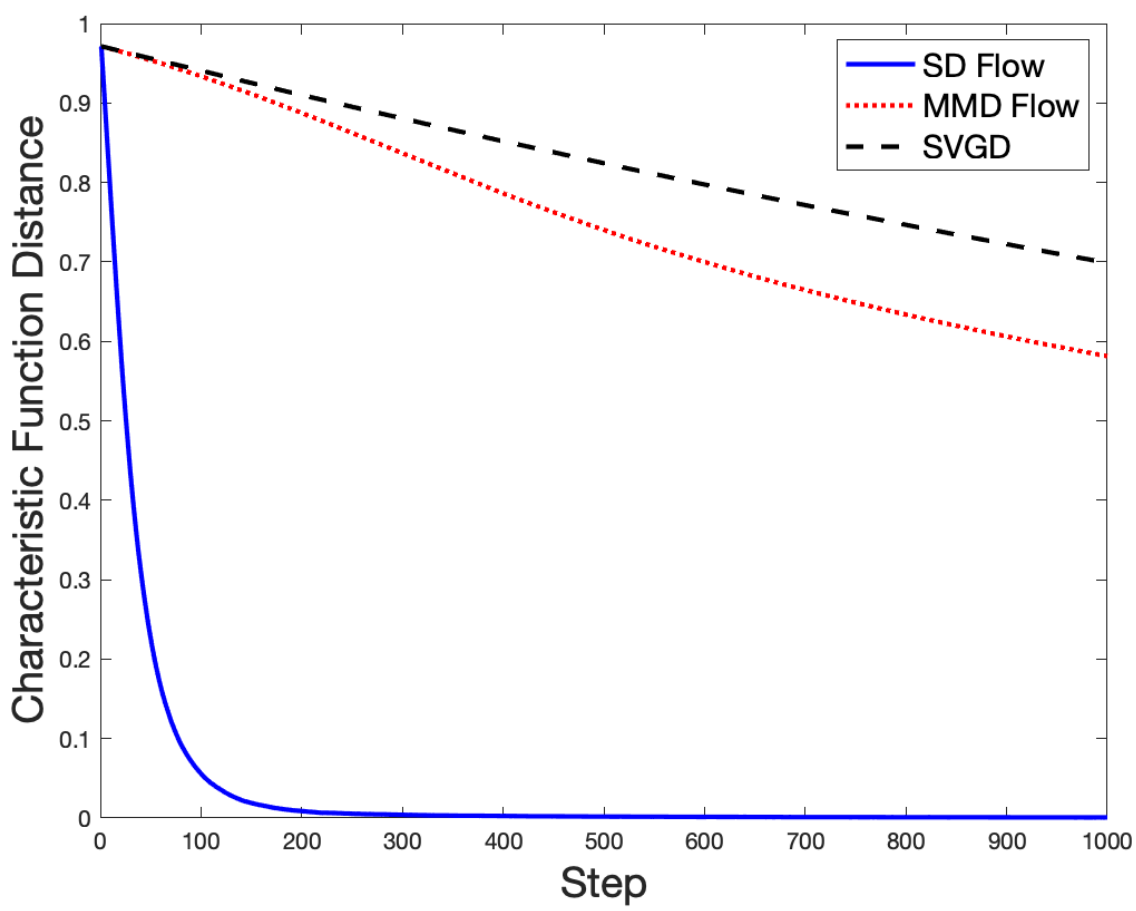


**SD Flow**

**MMD Flow**

**SVG D**

ADAGRAD	BATCH	CONST	ANNEAL	OFFSET
N	N	Y	N	Y

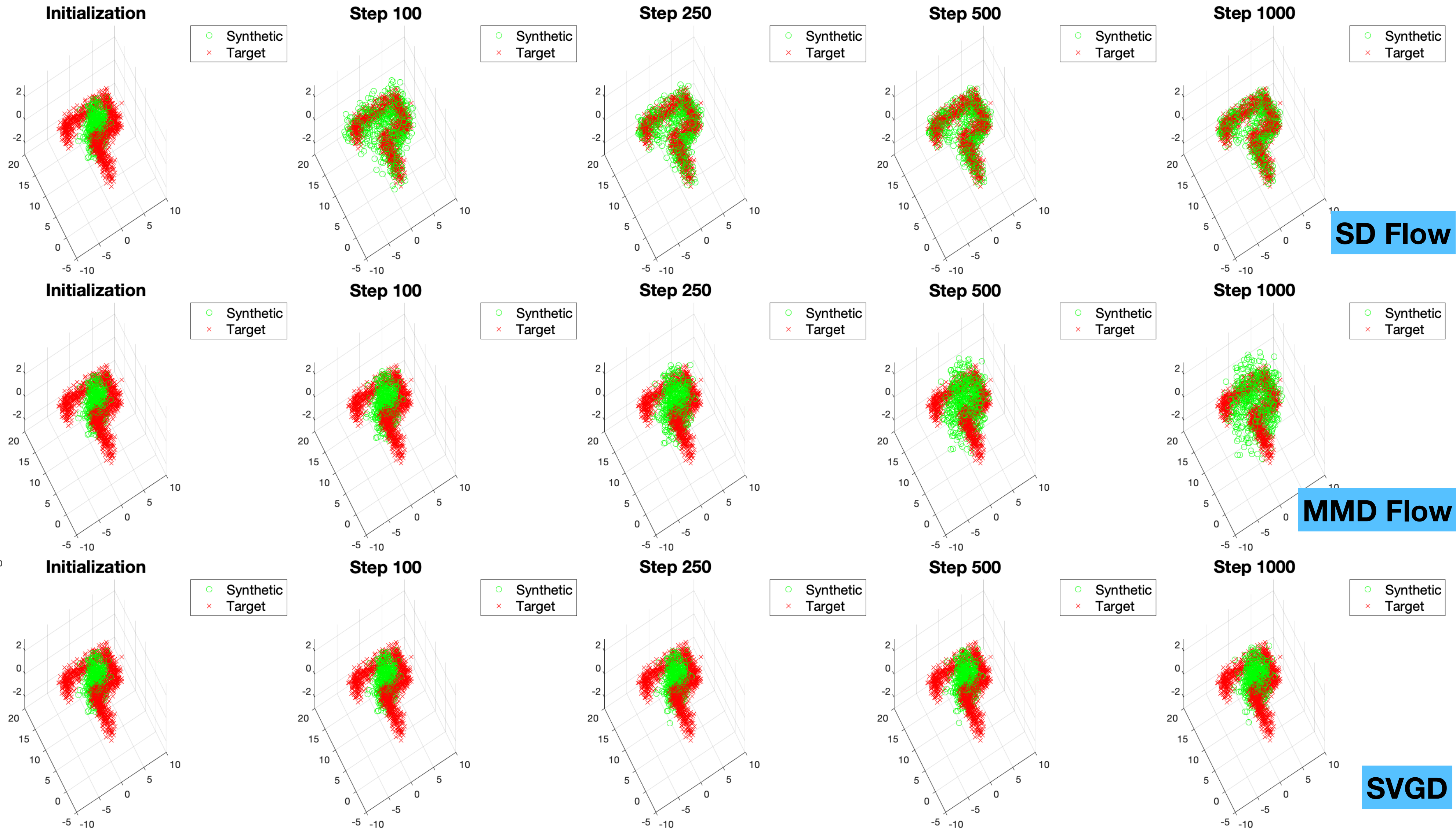
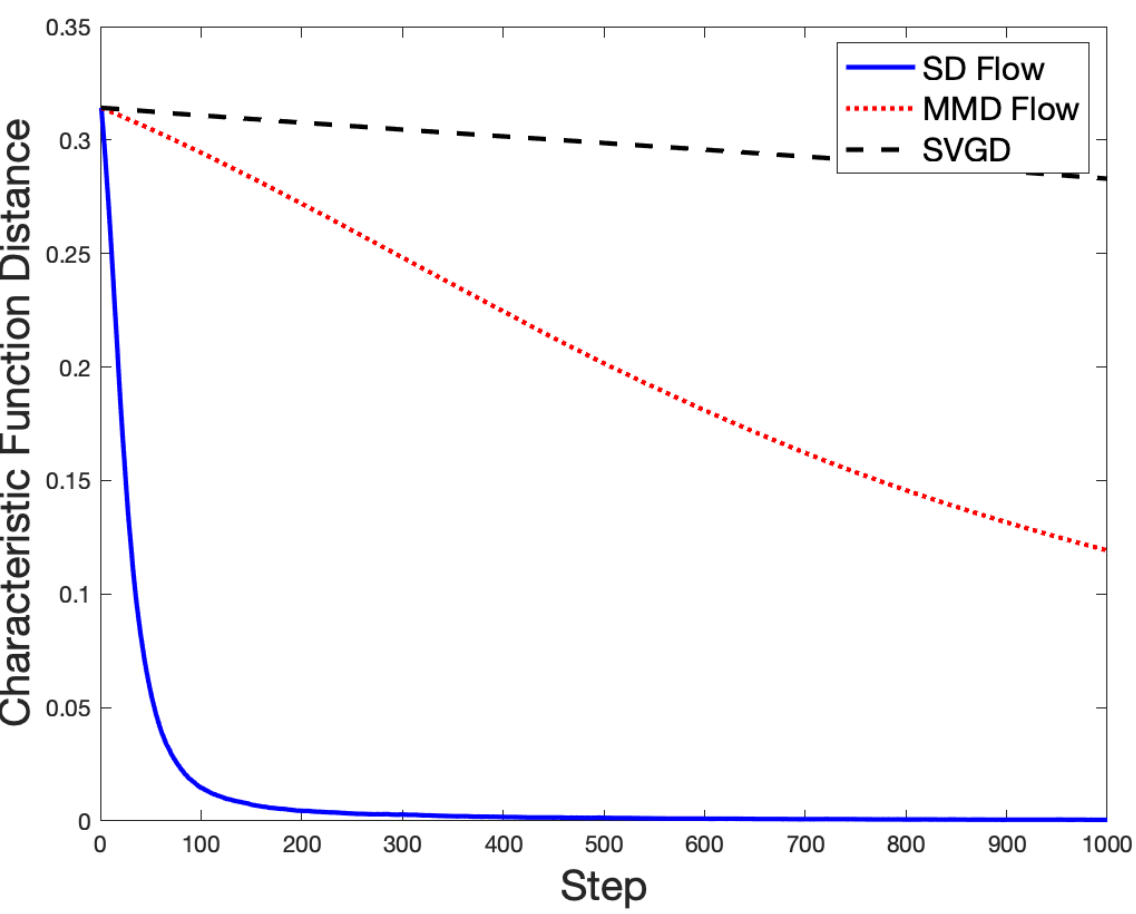


**SD Flow**

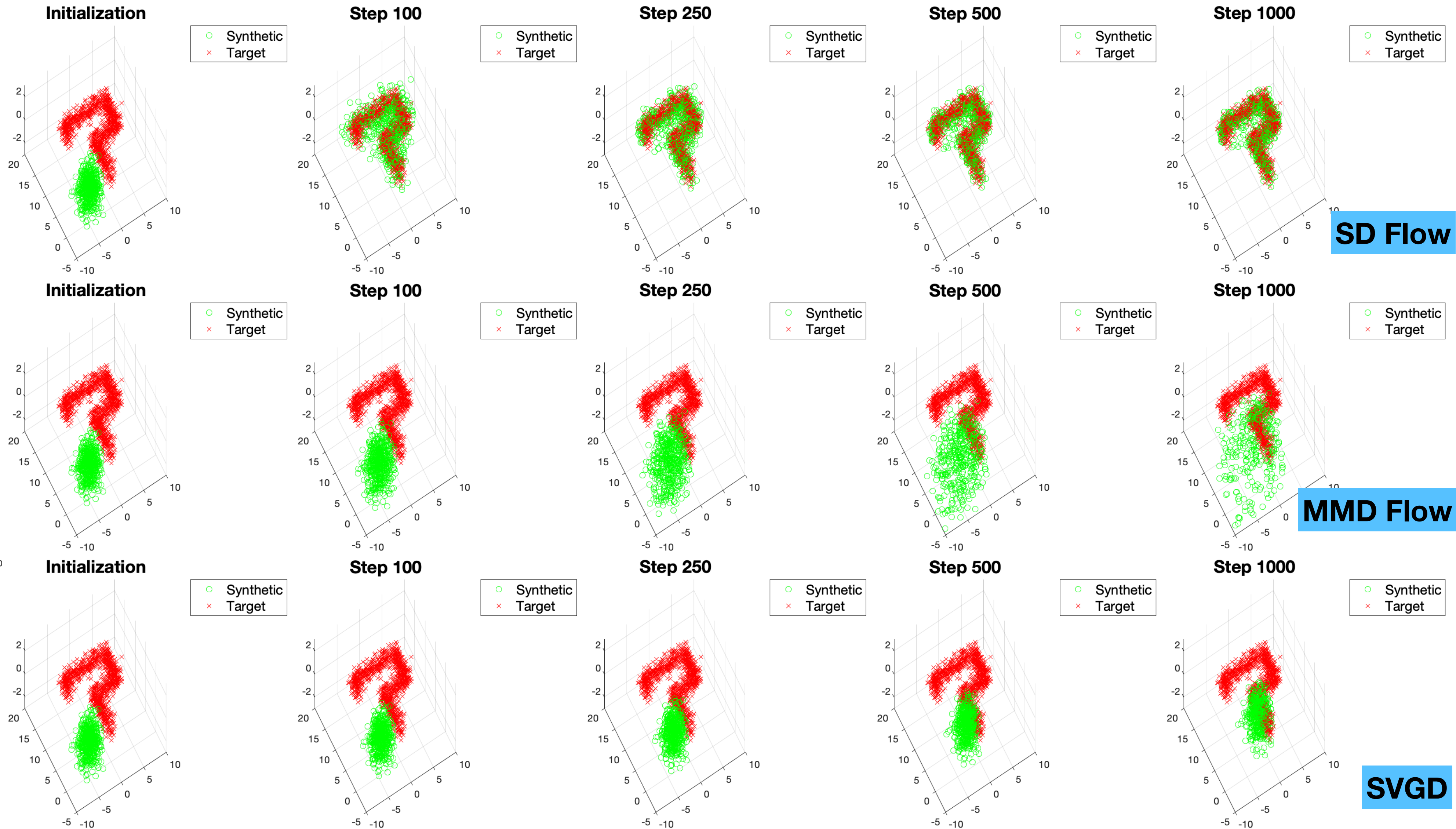
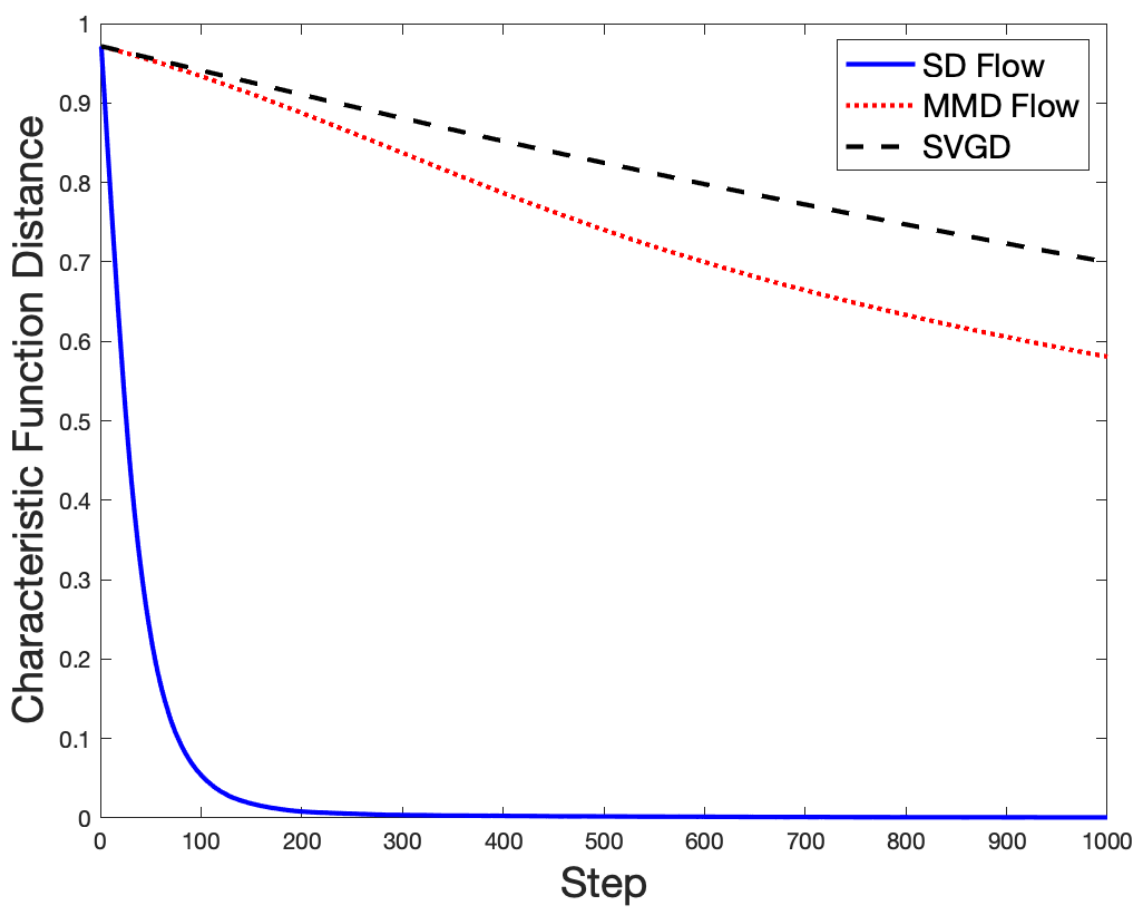
**MMD Flow**

**SVGD**

ADAGRAD	BATCH	CONST	ANNEAL	OFFSET
N	N	Y	Y	N



ADAGRAD	BATCH	CONST	ANNEAL	OFFSET
N	N	Y	Y	Y



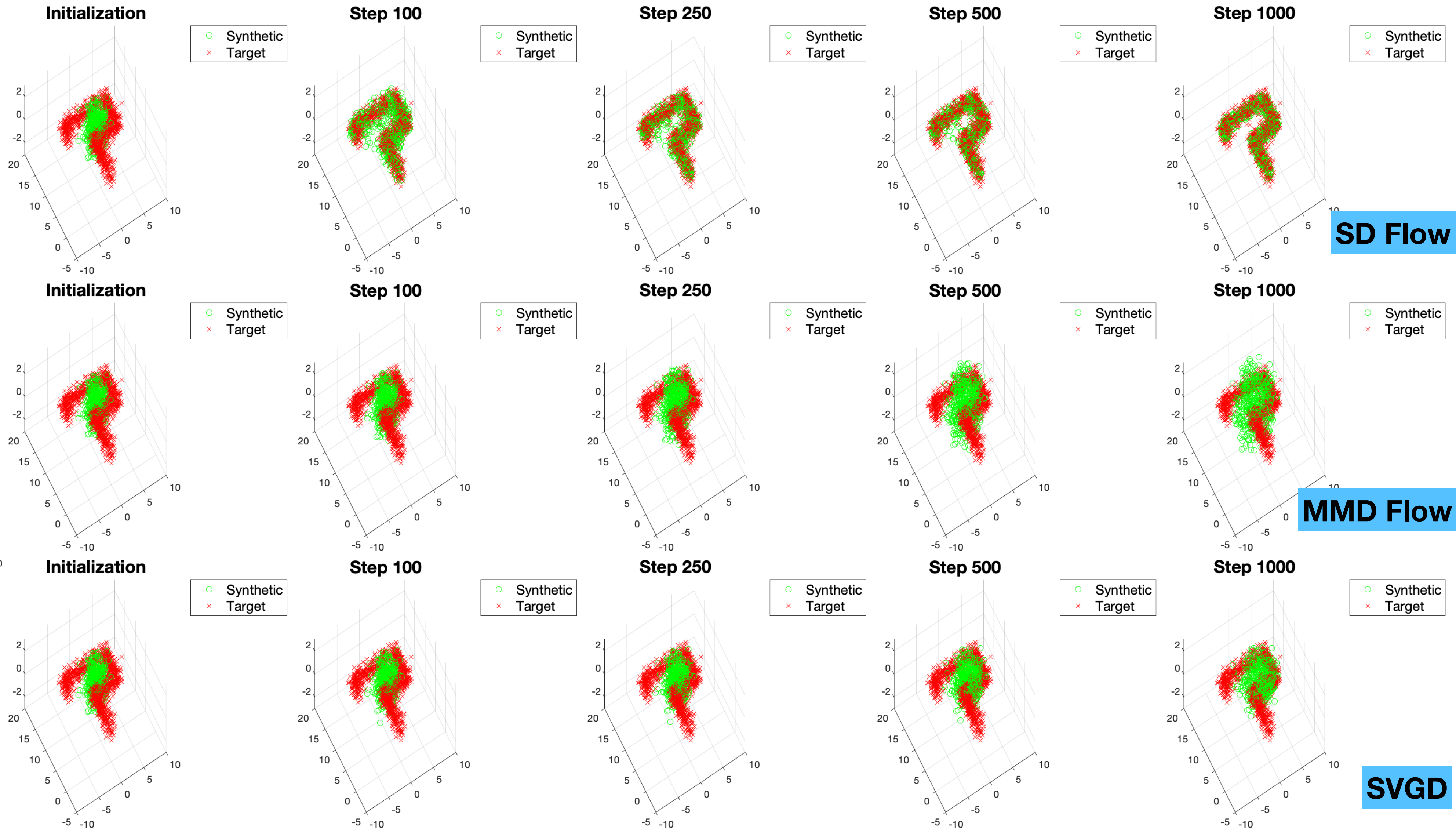
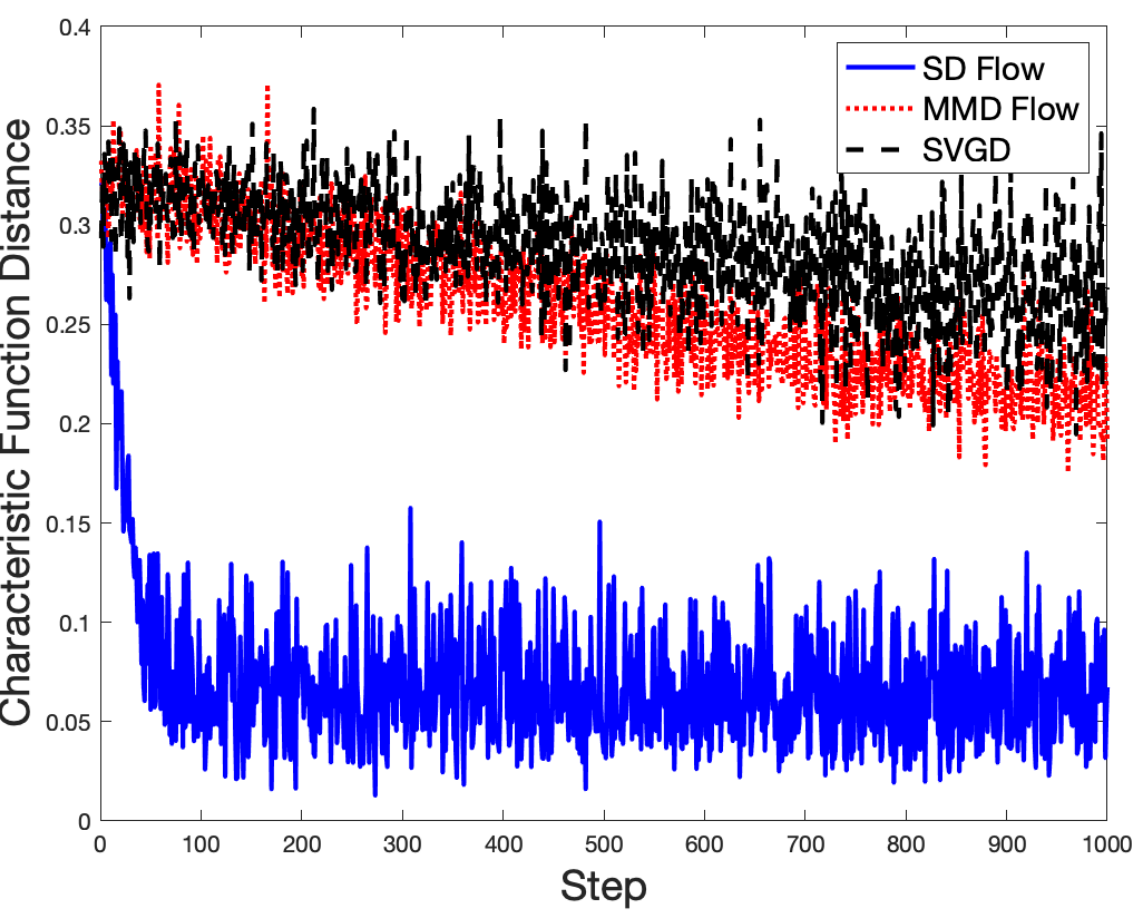
**SD Flow**

**MMD Flow**

**SVGD**



ADAGRAD	BATCH	CONST	ANNEAL	OFFSET
N	Y	N	N	N

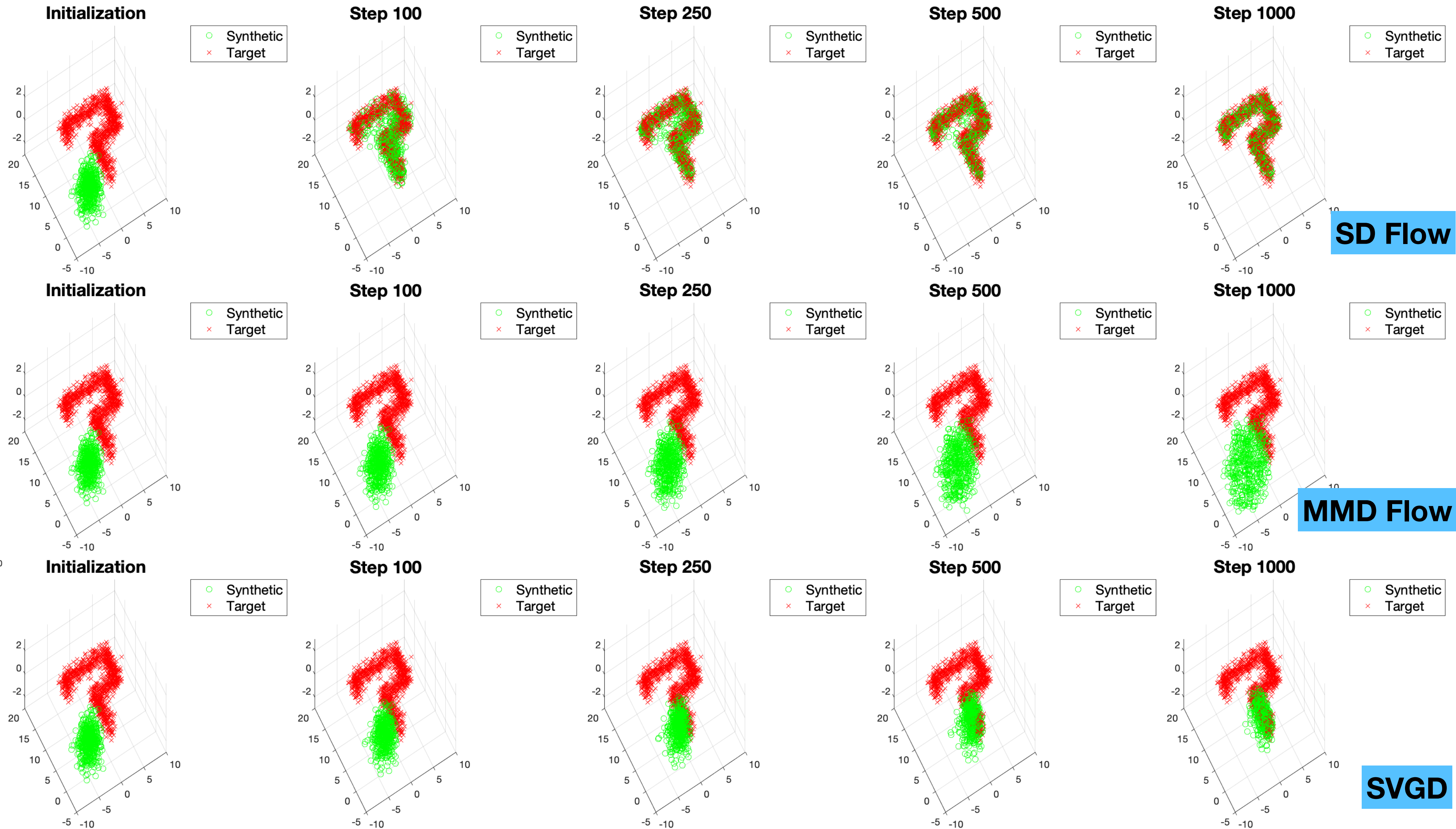
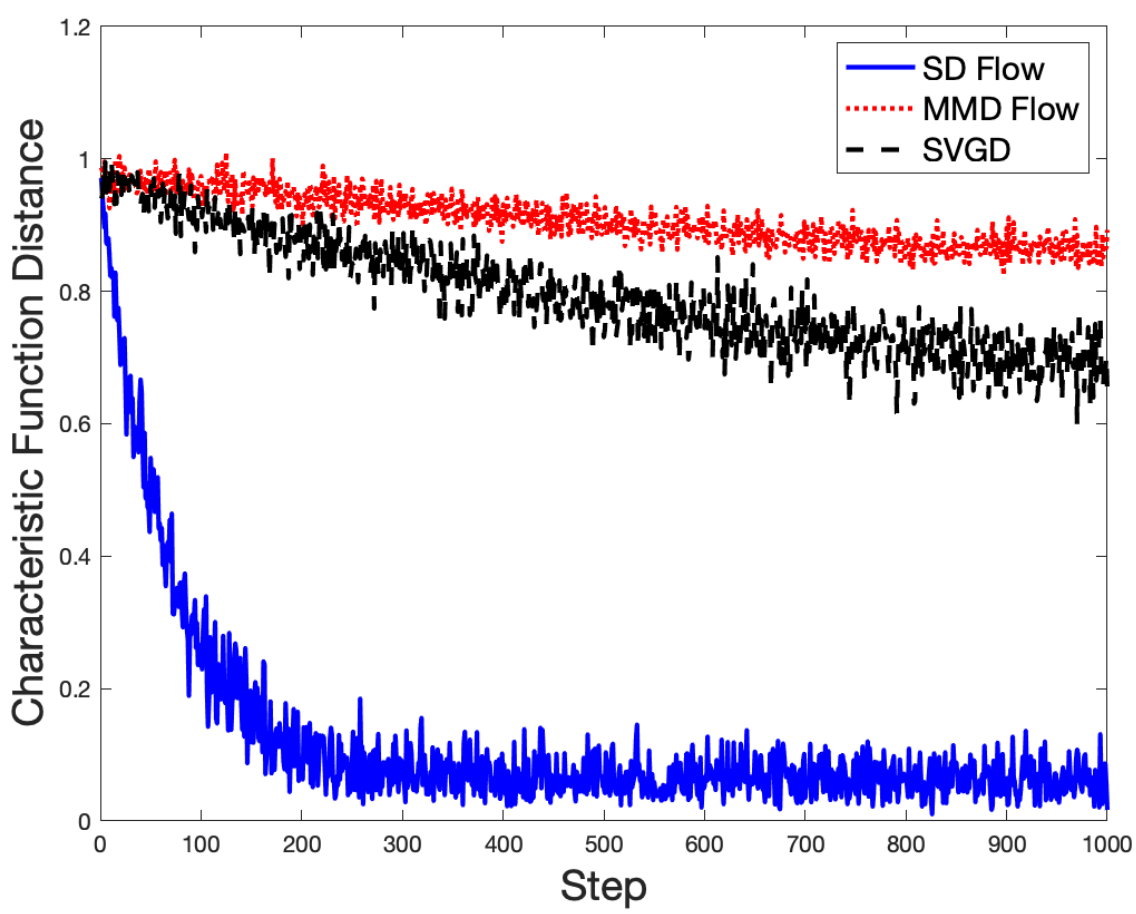


**SD Flow**

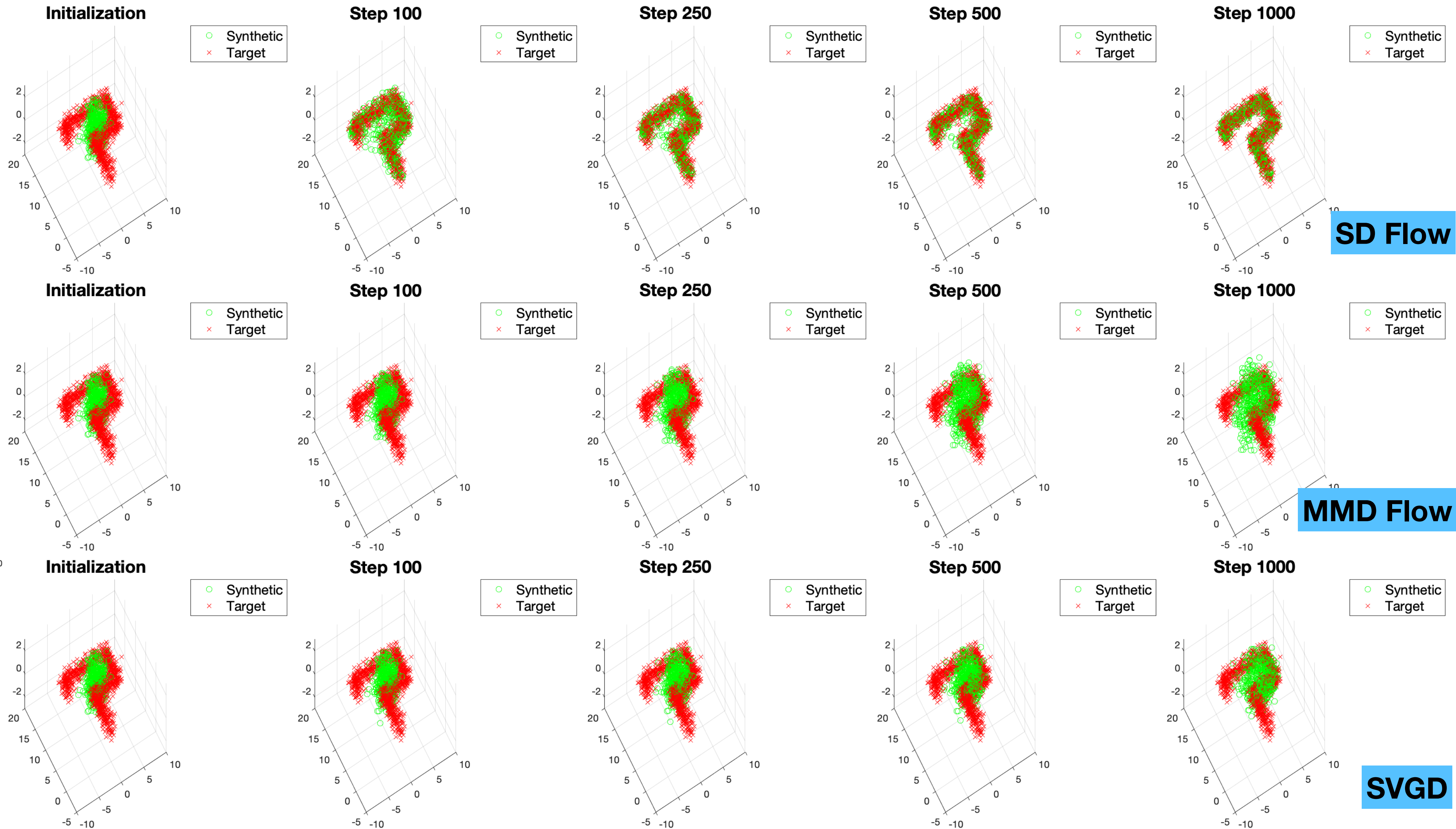
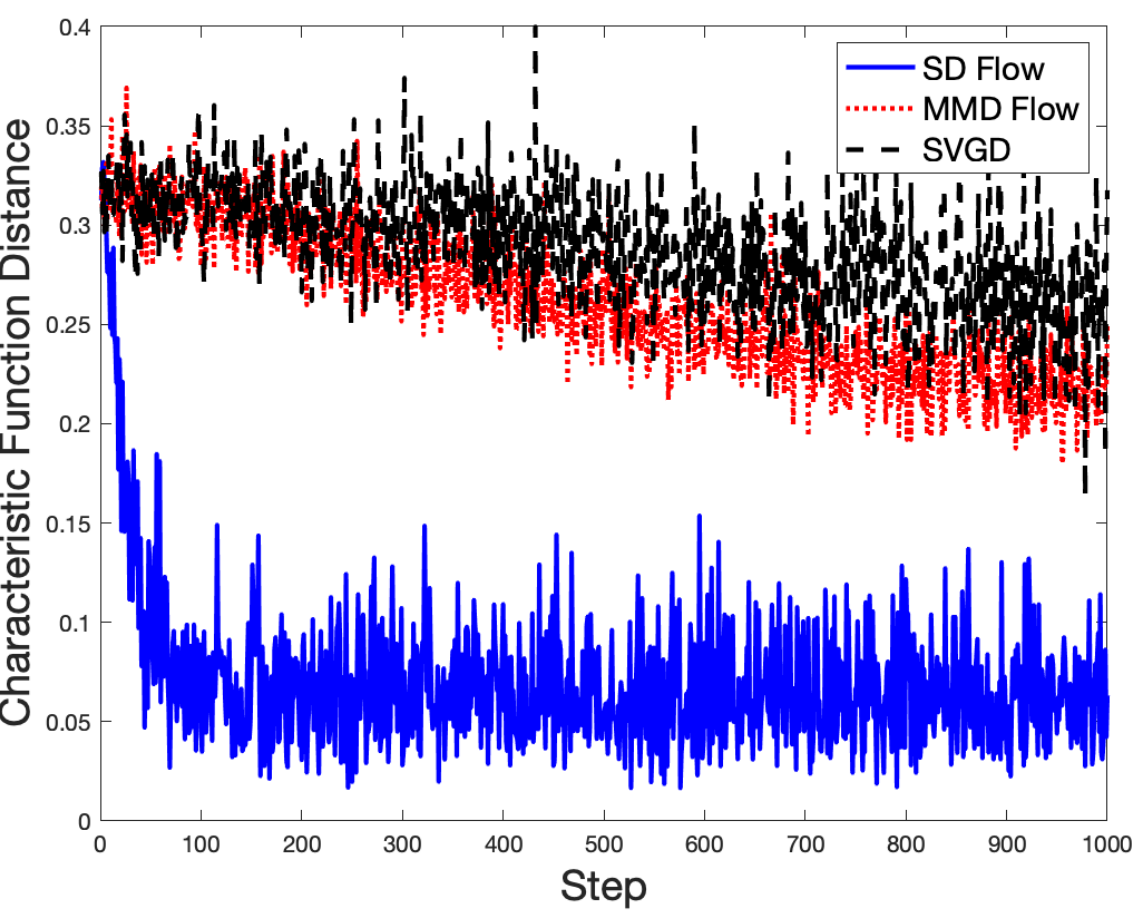
**MMD Flow**

**SVGD**

ADAGRAD	BATCH	CONST	ANNEAL	OFFSET
N	Y	N	N	Y



ADAGRAD	BATCH	CONST	ANNEAL	OFFSET
N	Y	N	Y	N

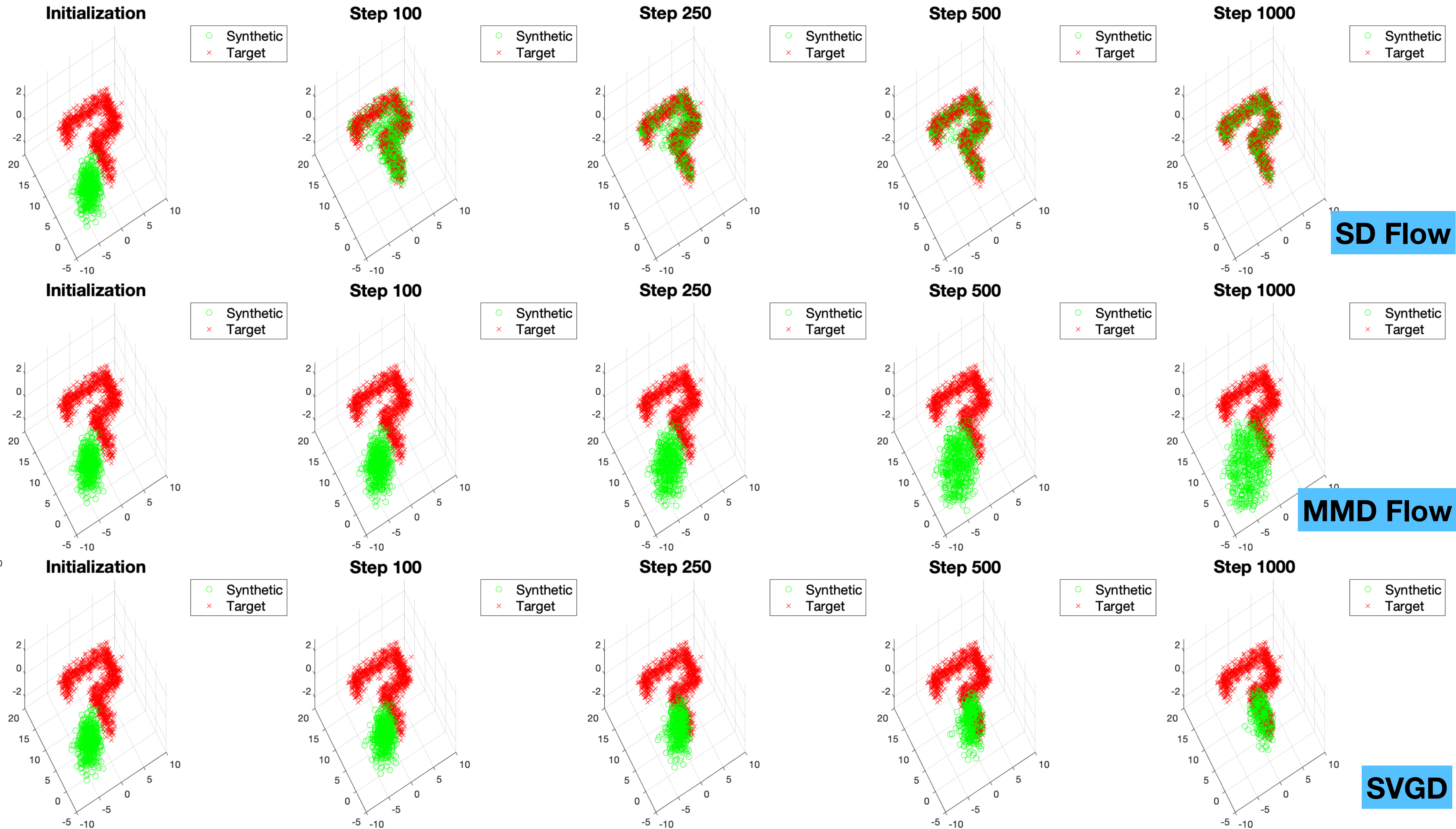
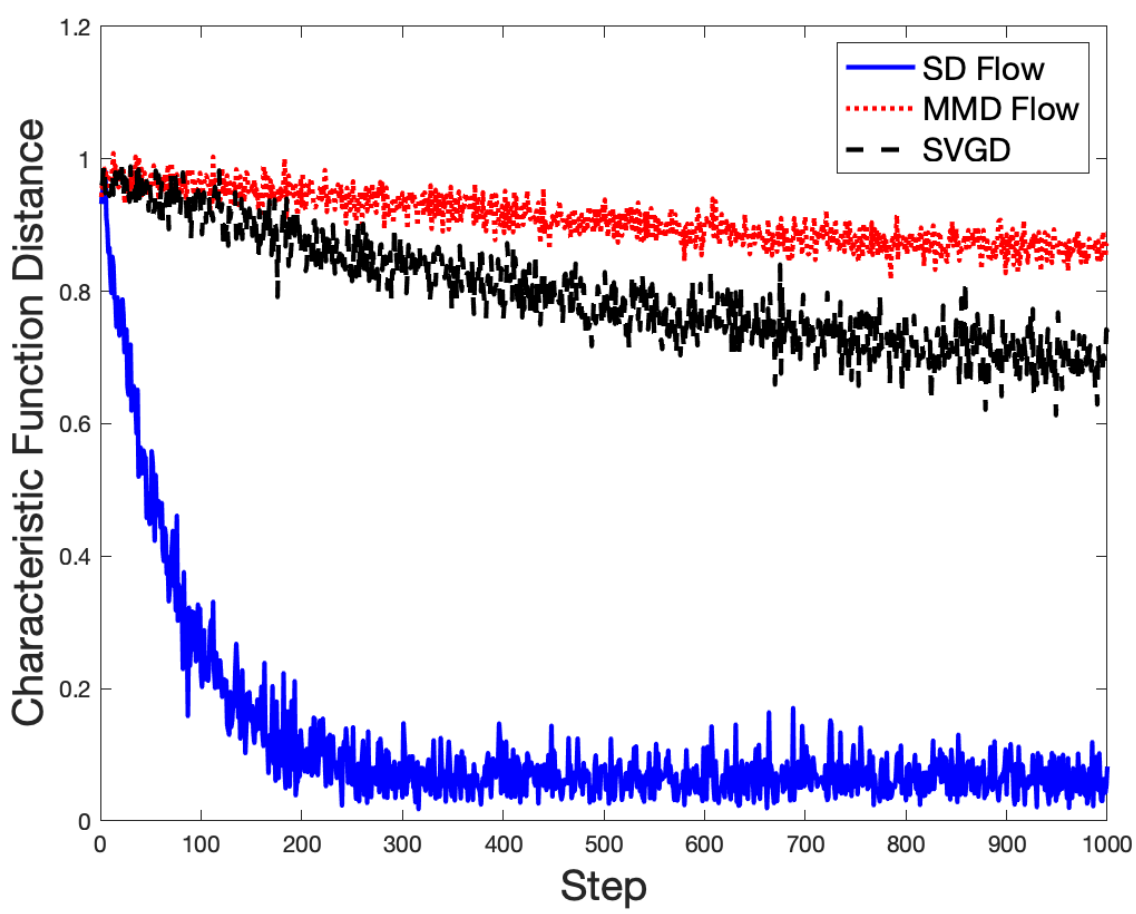


**SD Flow**

**MMD Flow**

**SVGD**

ADAGRAD	BATCH	CONST	ANNEAL	OFFSET
N	Y	N	Y	Y

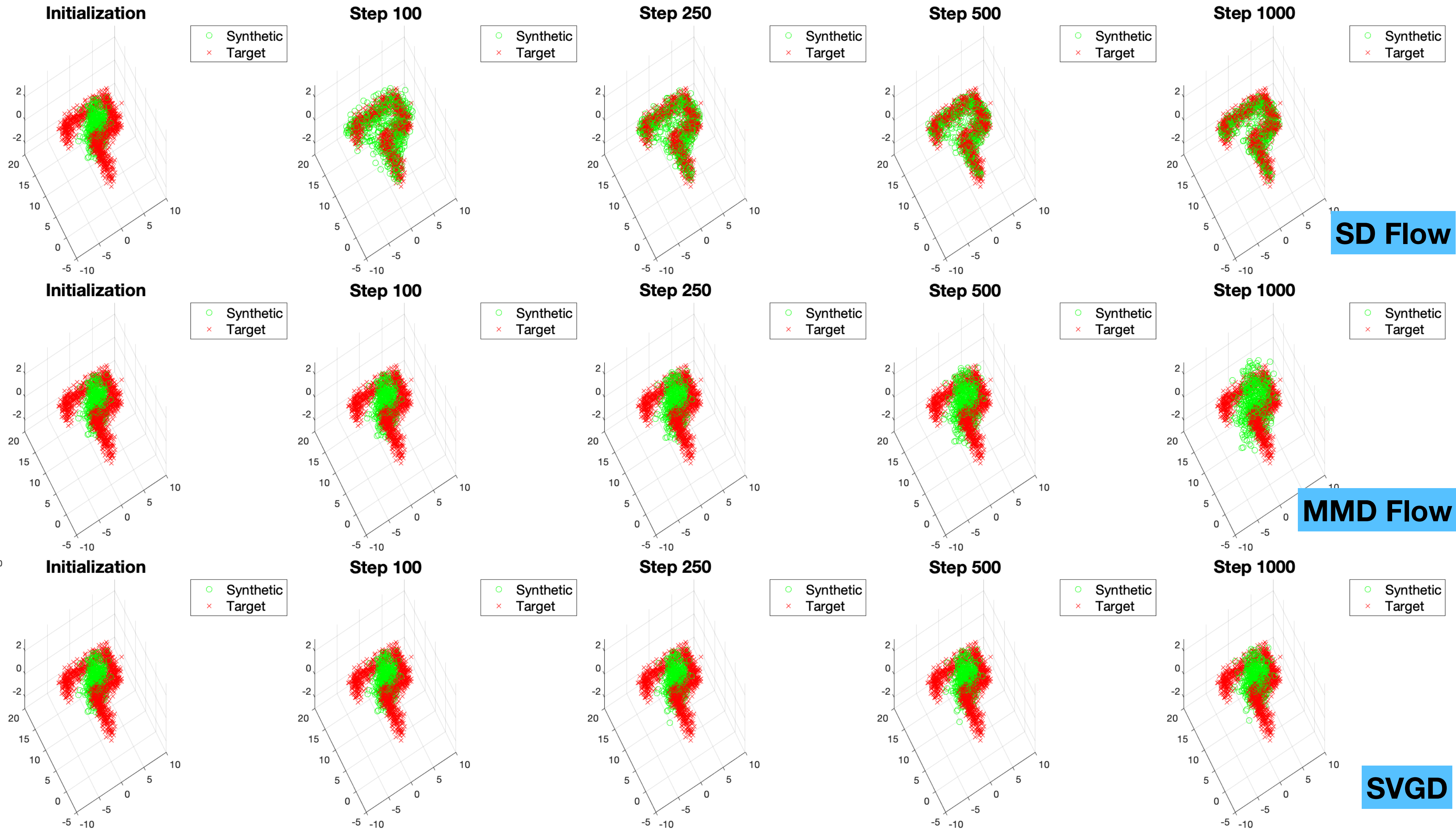
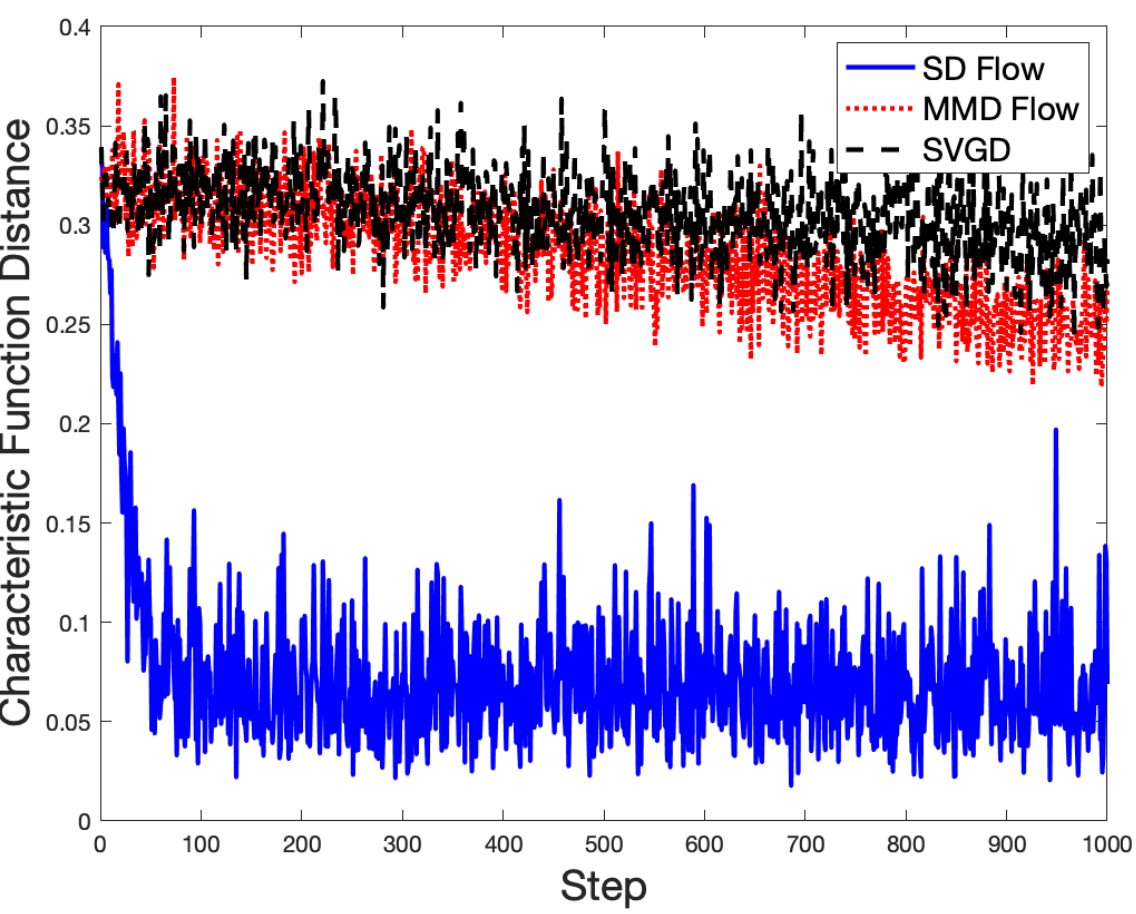


**SD Flow**

**MMD Flow**

**SVGD**

ADAGRAD	BATCH	CONST	ANNEAL	OFFSET
N	Y	Y	N	N

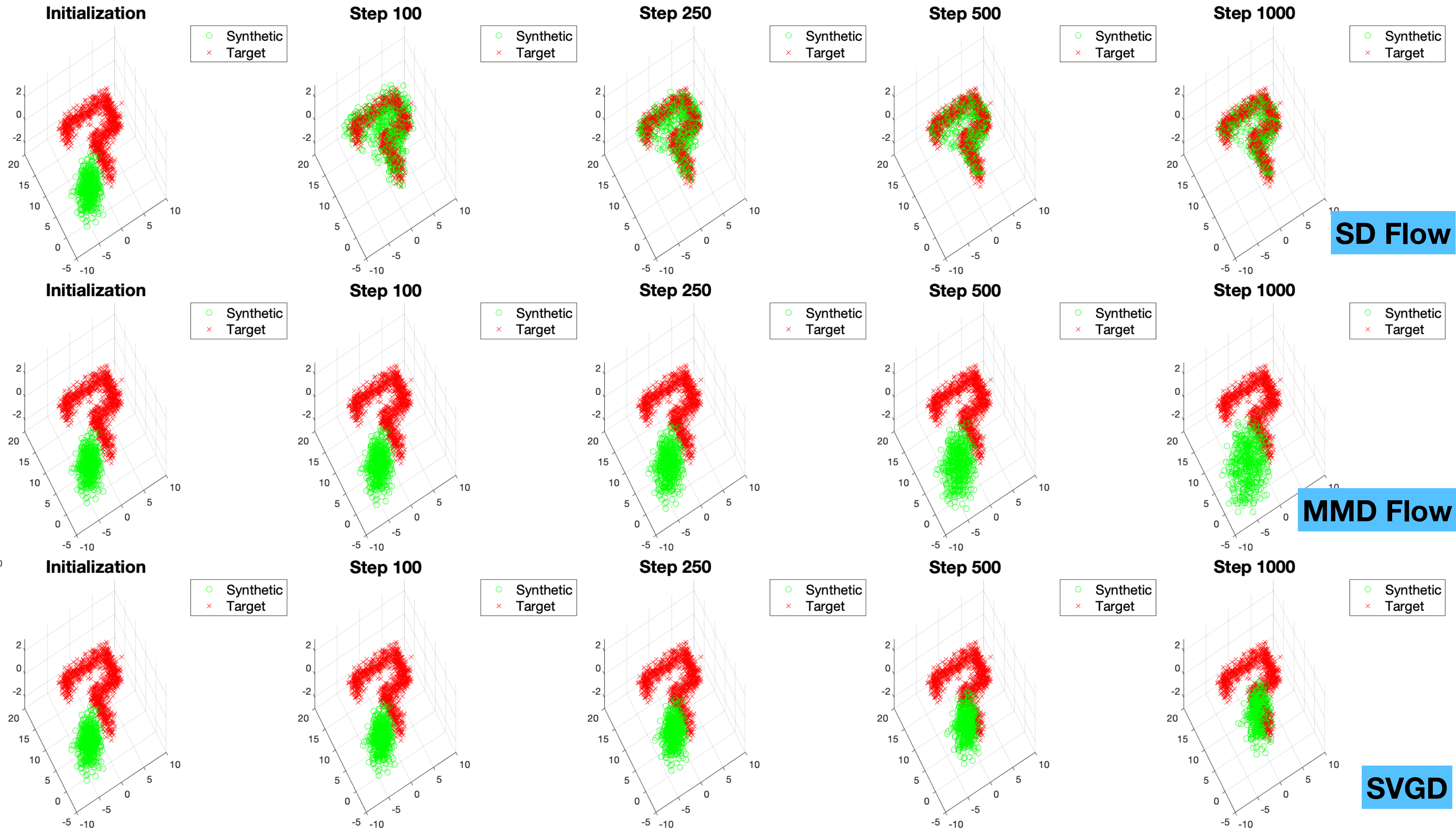
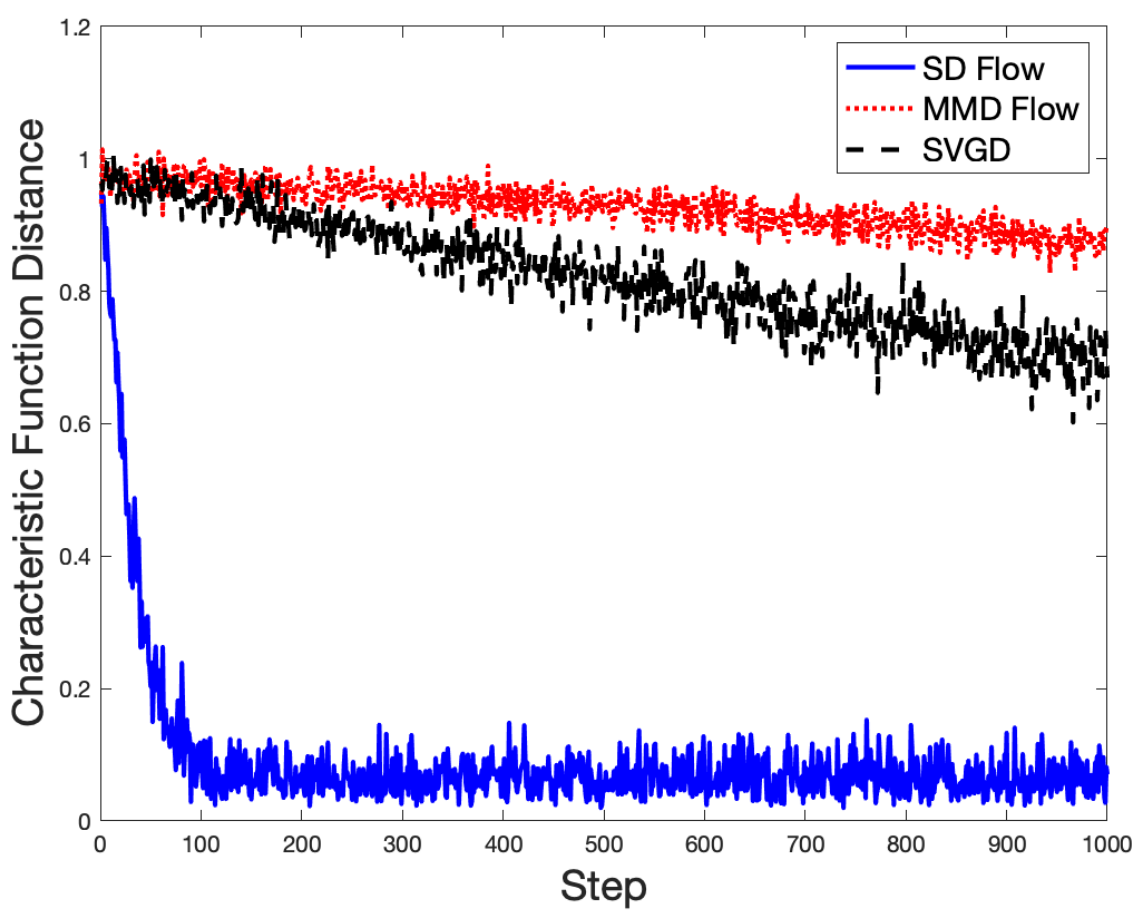


**SD Flow**

**MMD Flow**

**SVGD**

ADAGRAD	BATCH	CONST	ANNEAL	OFFSET
N	Y	Y	N	Y

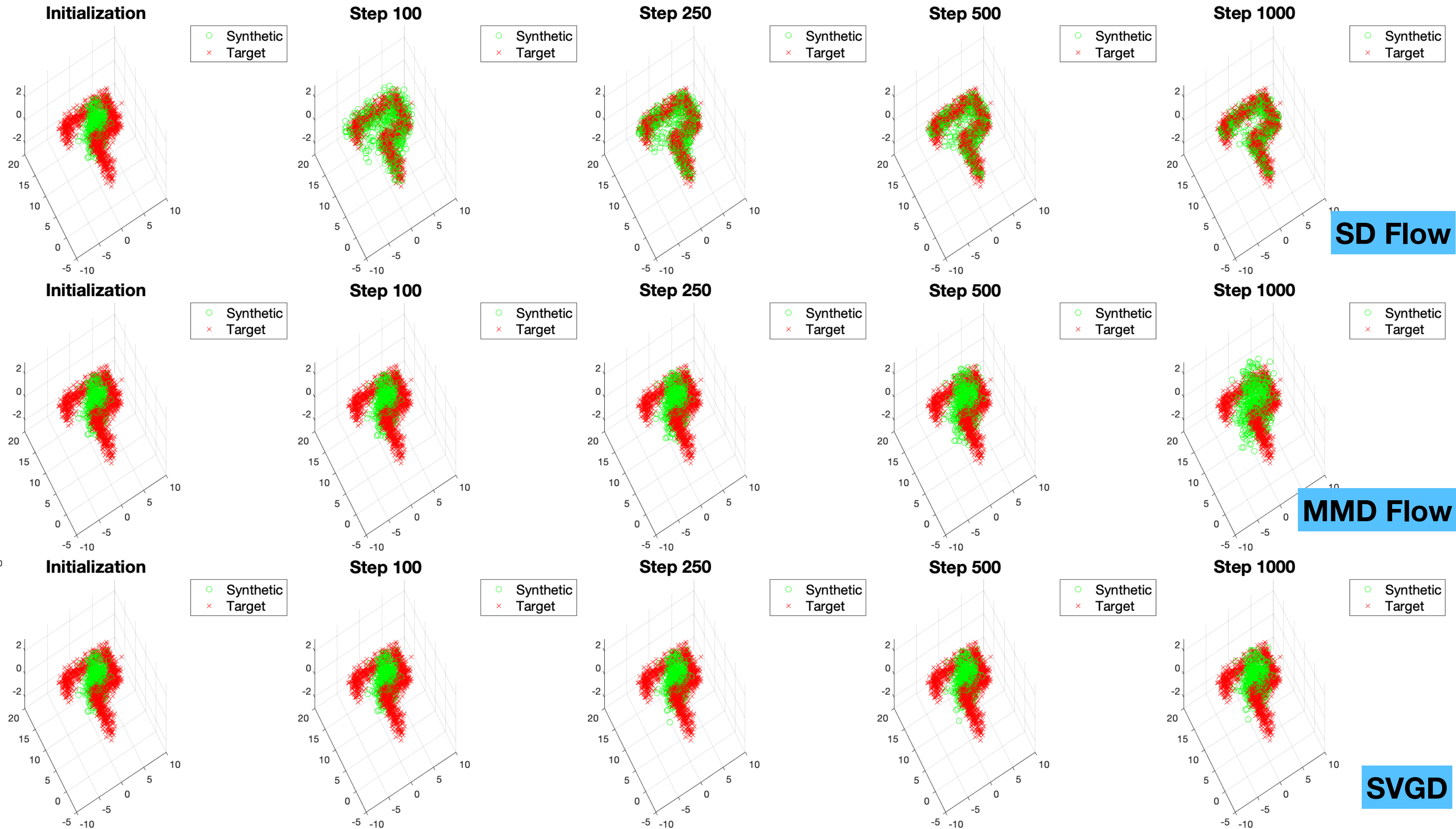
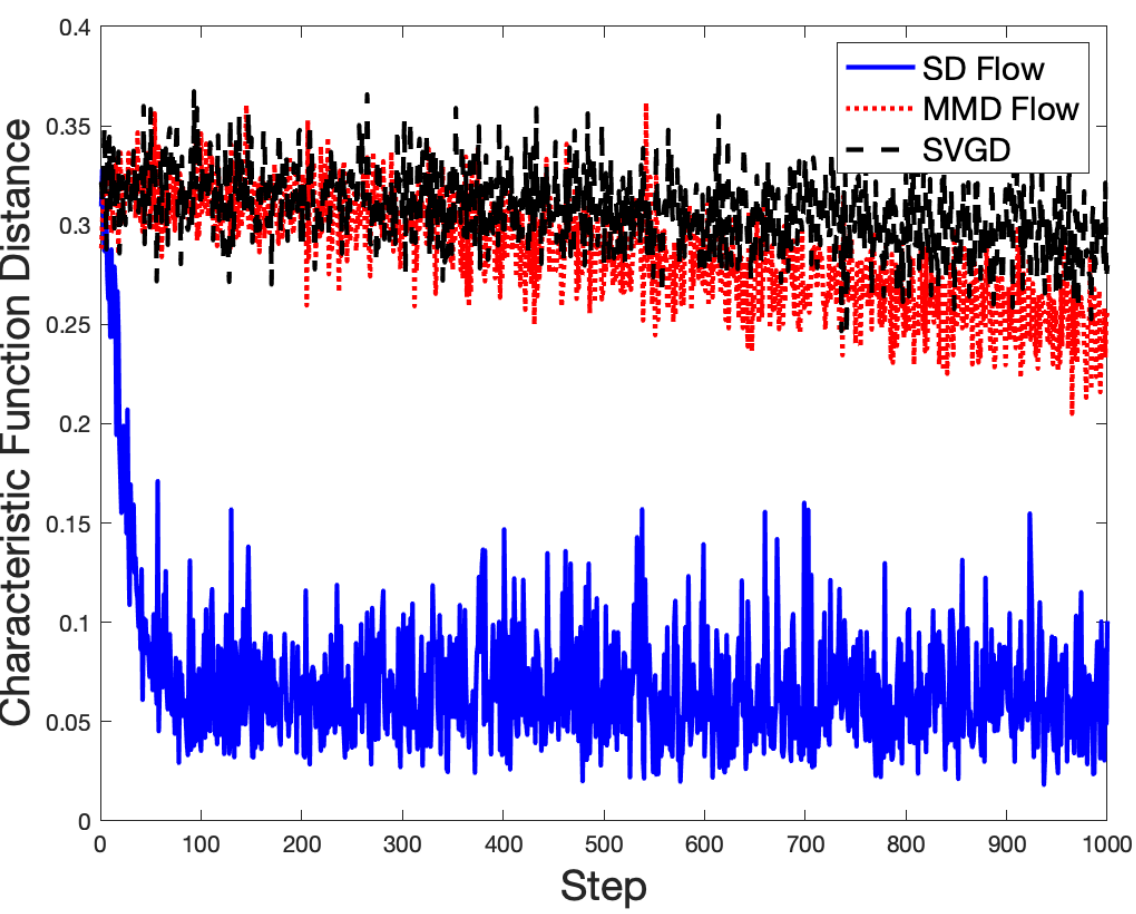


**SD Flow**

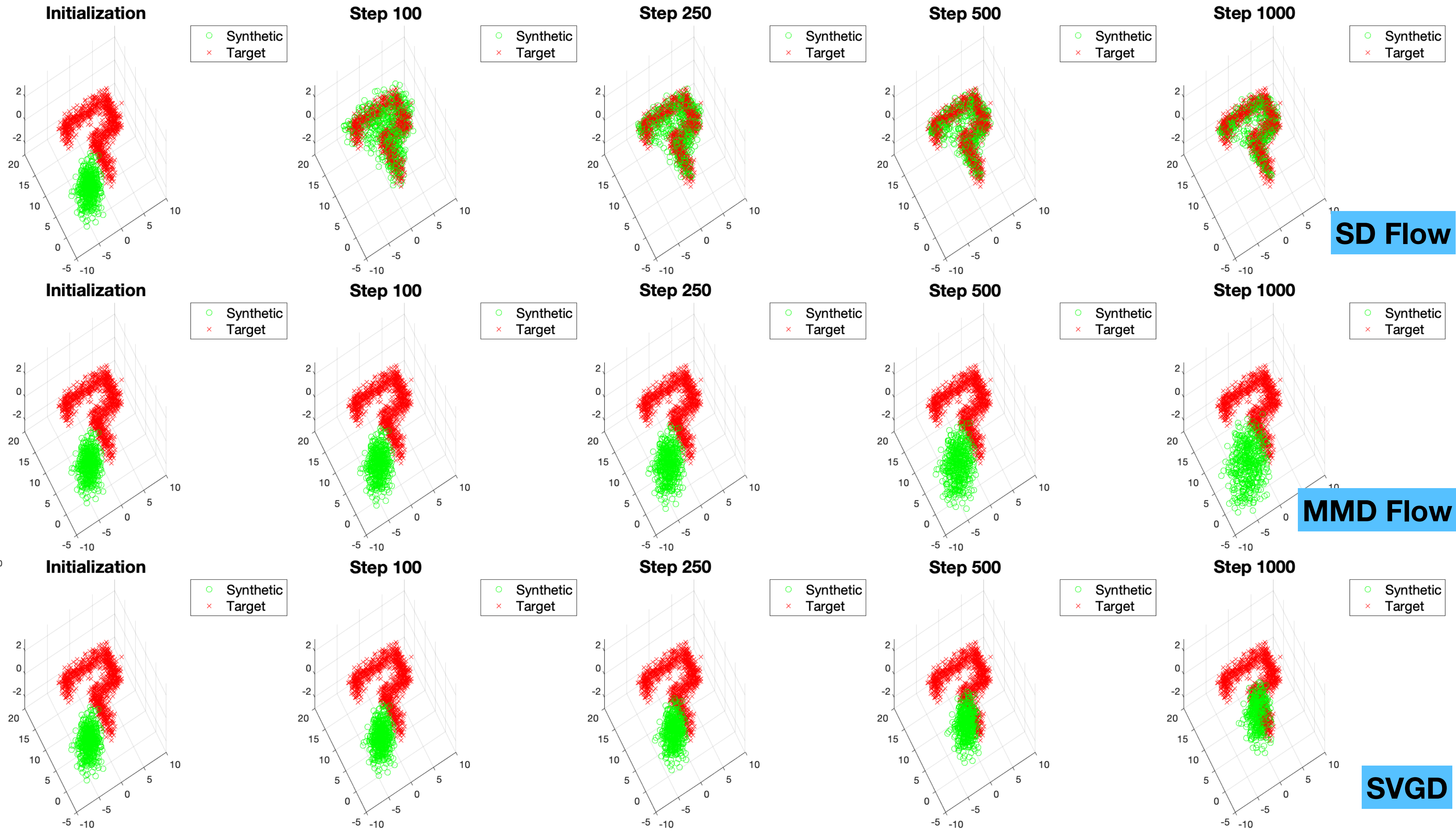
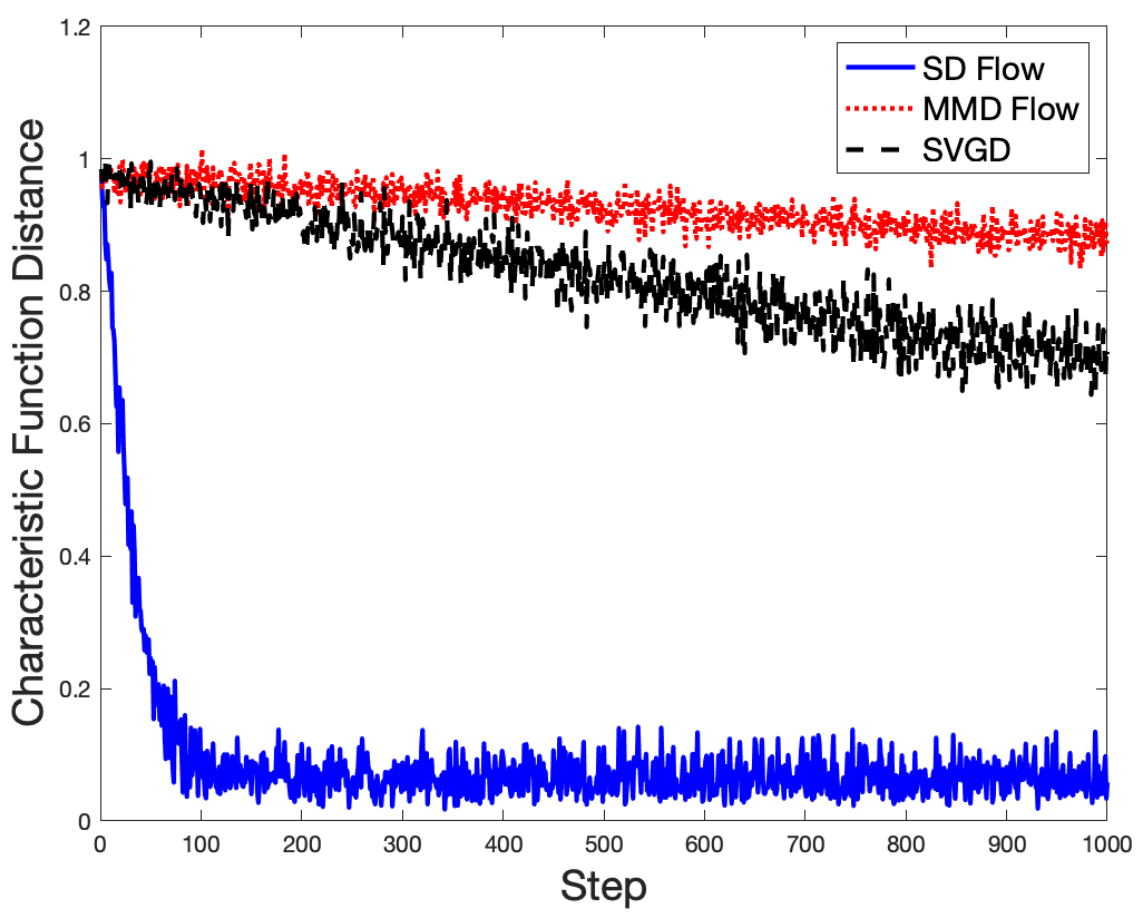
**MMD Flow**

**SVGD**

ADAGRAD	BATCH	CONST	ANNEAL	OFFSET
N	Y	Y	Y	N



ADAGRAD	BATCH	CONST	ANNEAL	OFFSET
N	Y	Y	Y	Y



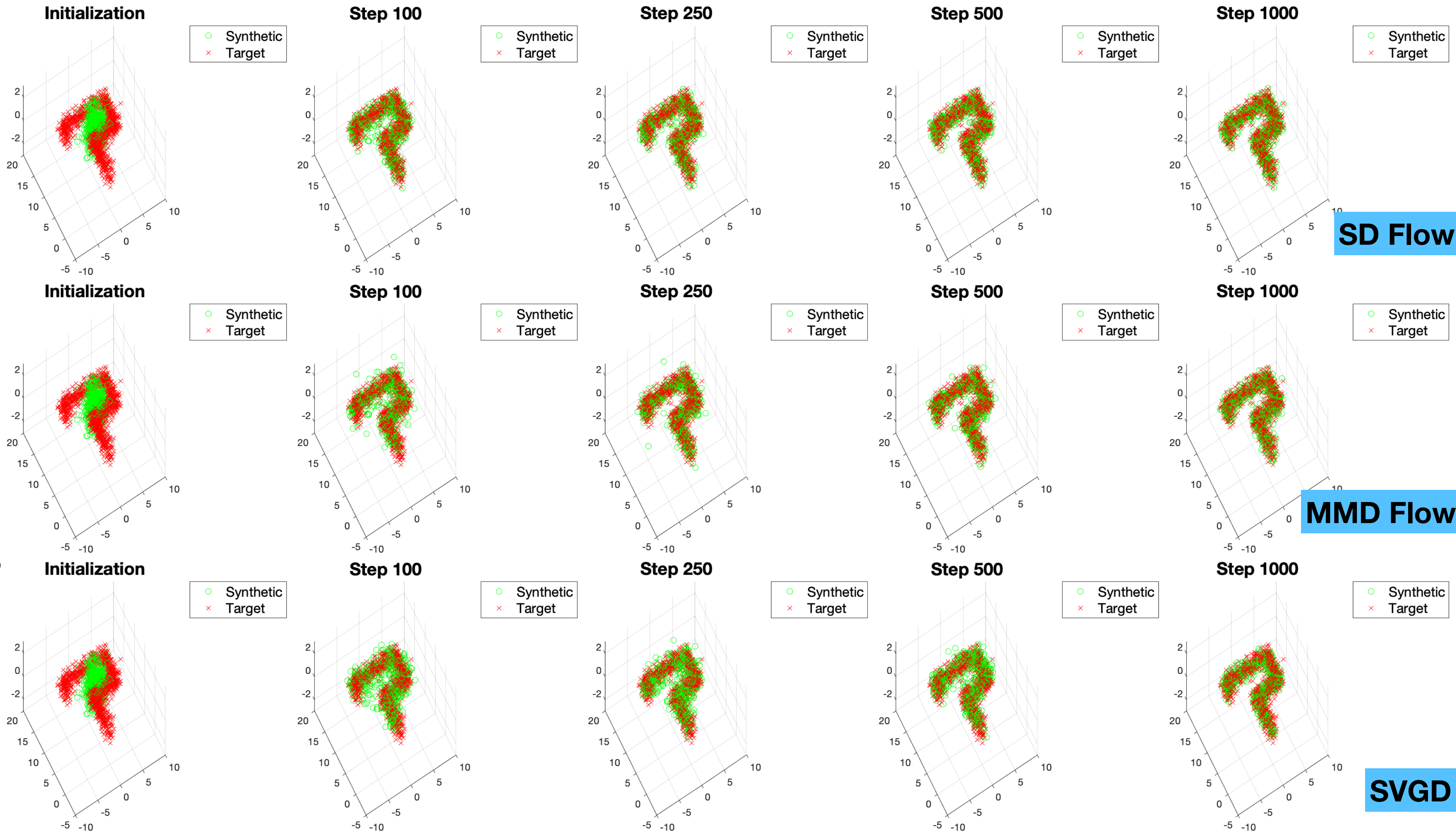
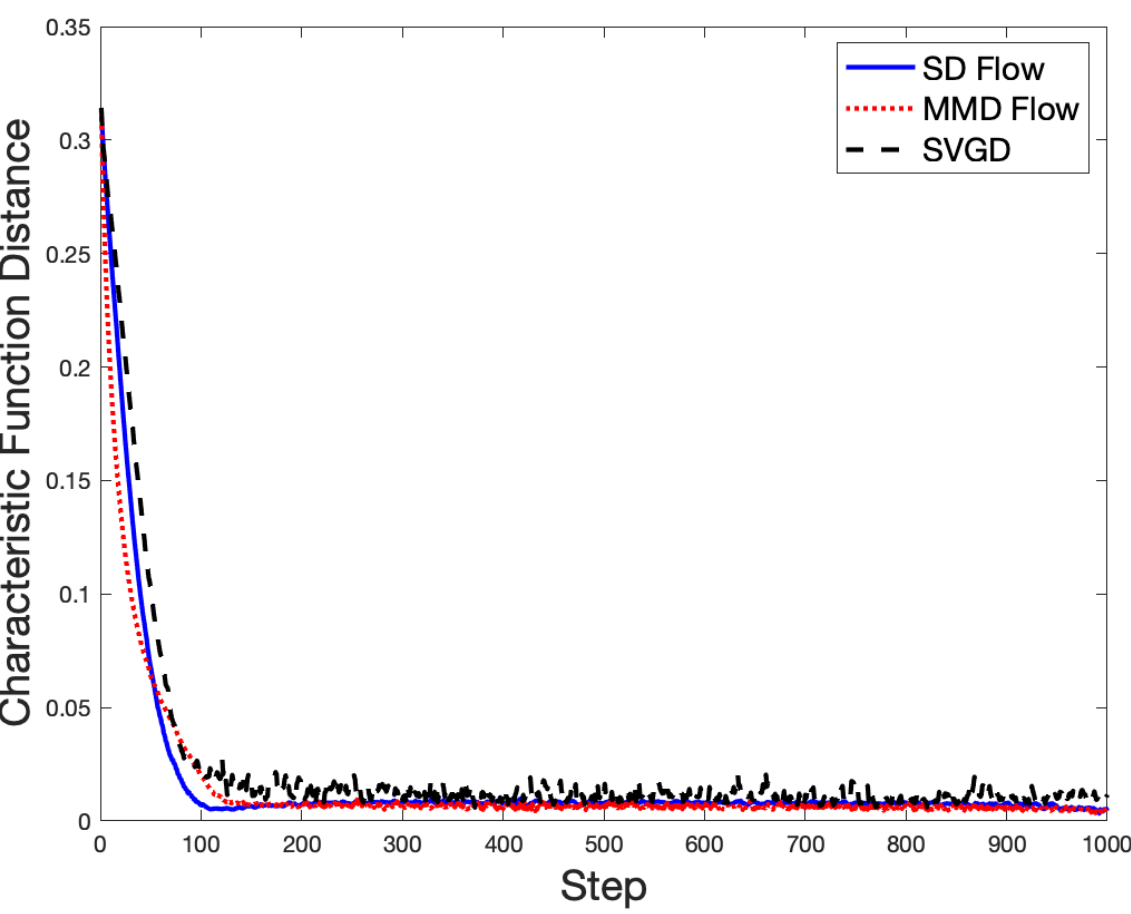
**SD Flow**

**MMD Flow**

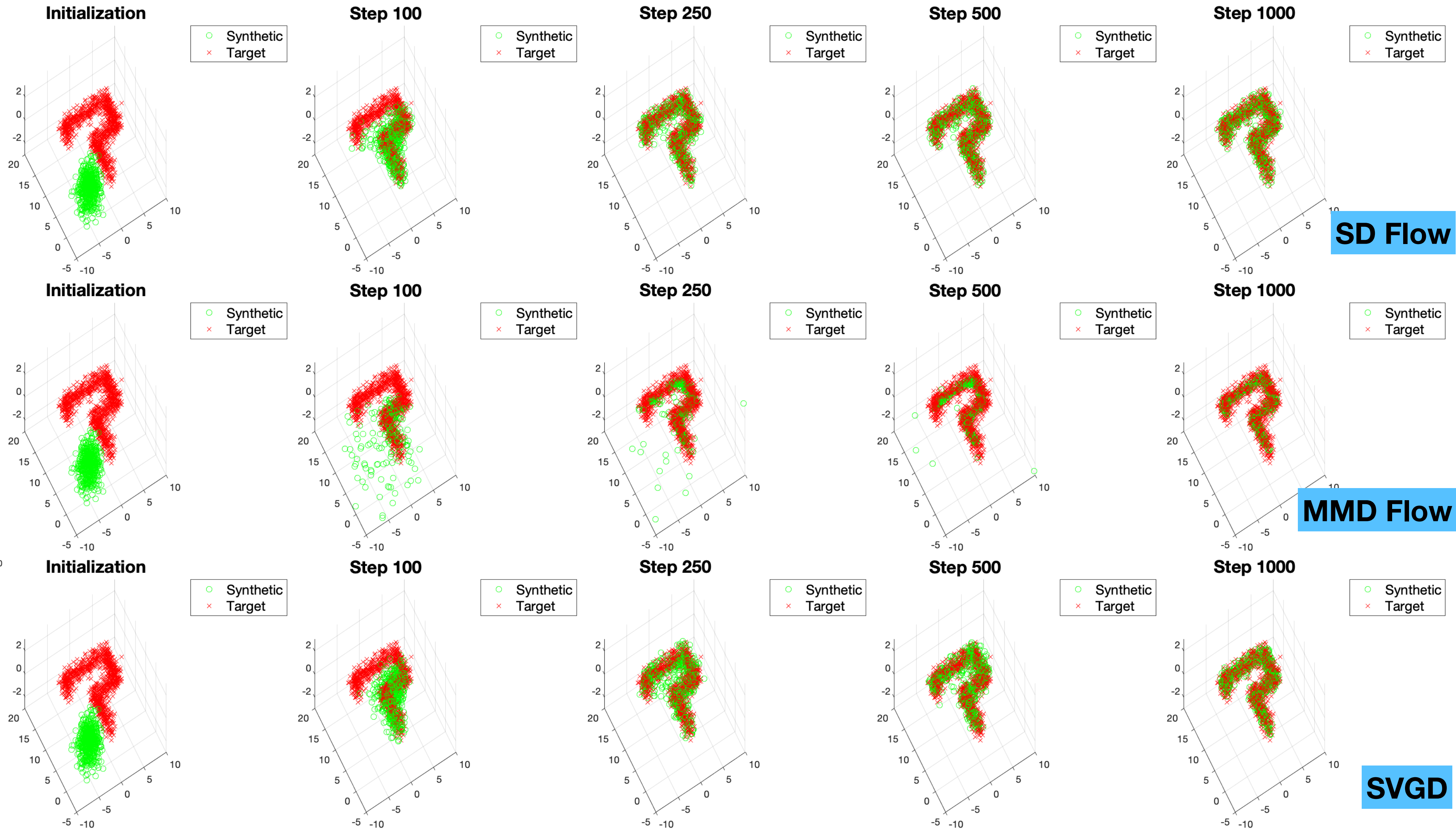
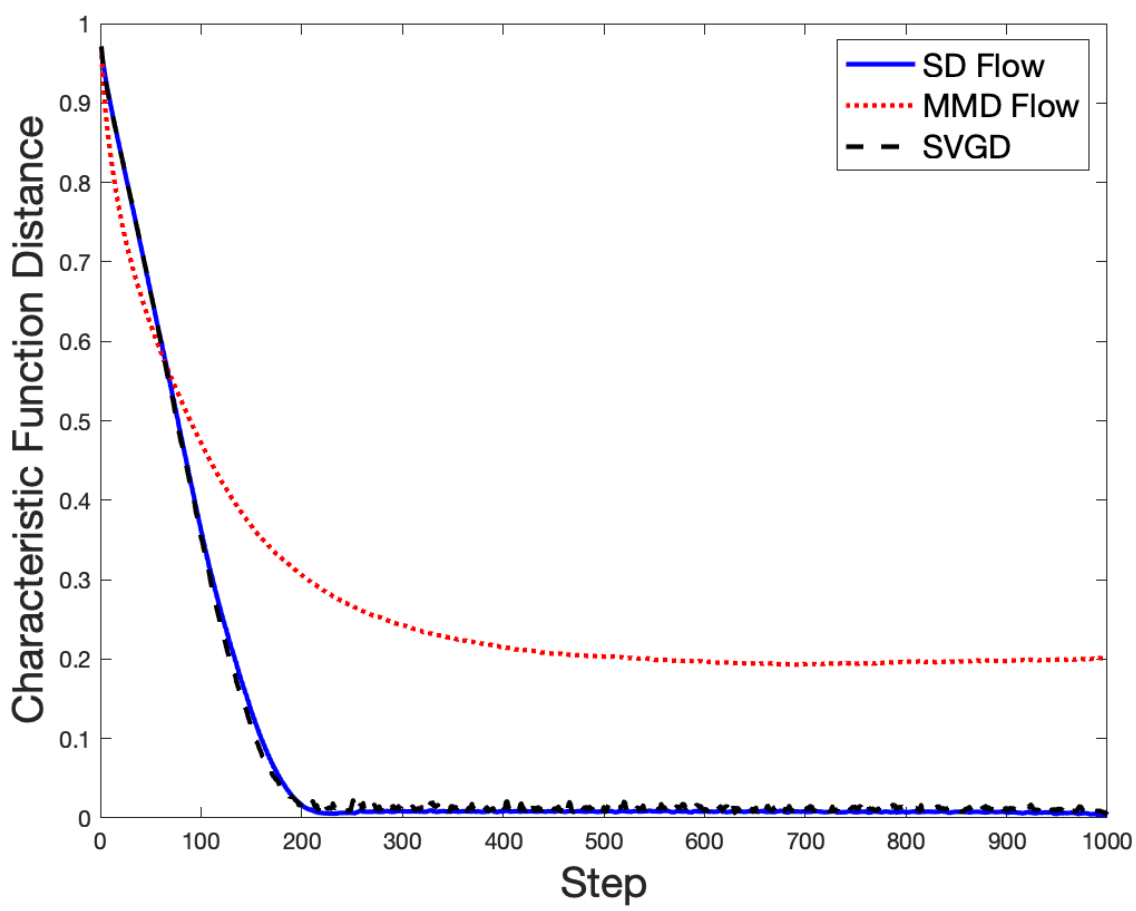
**SVGD**



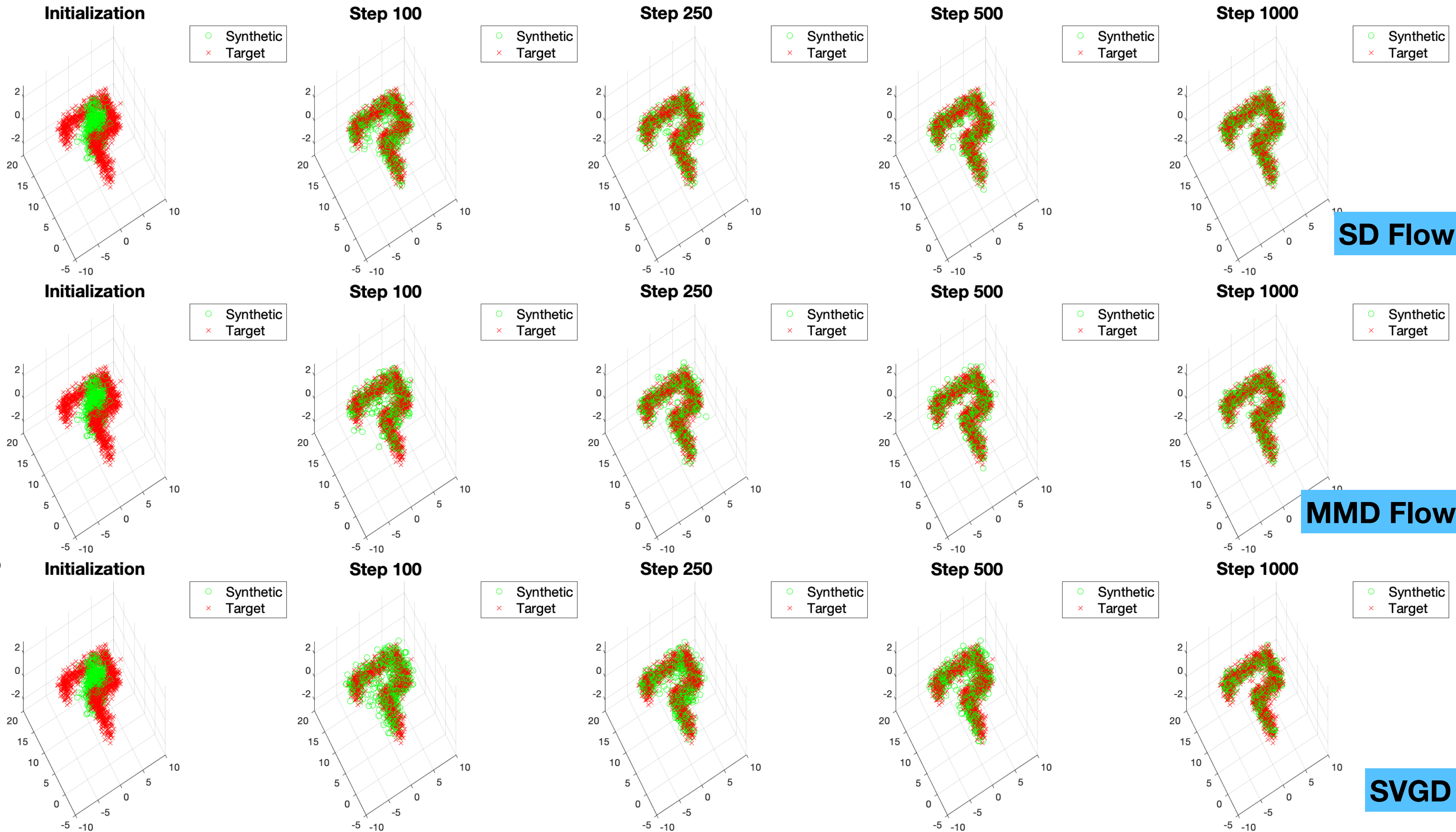
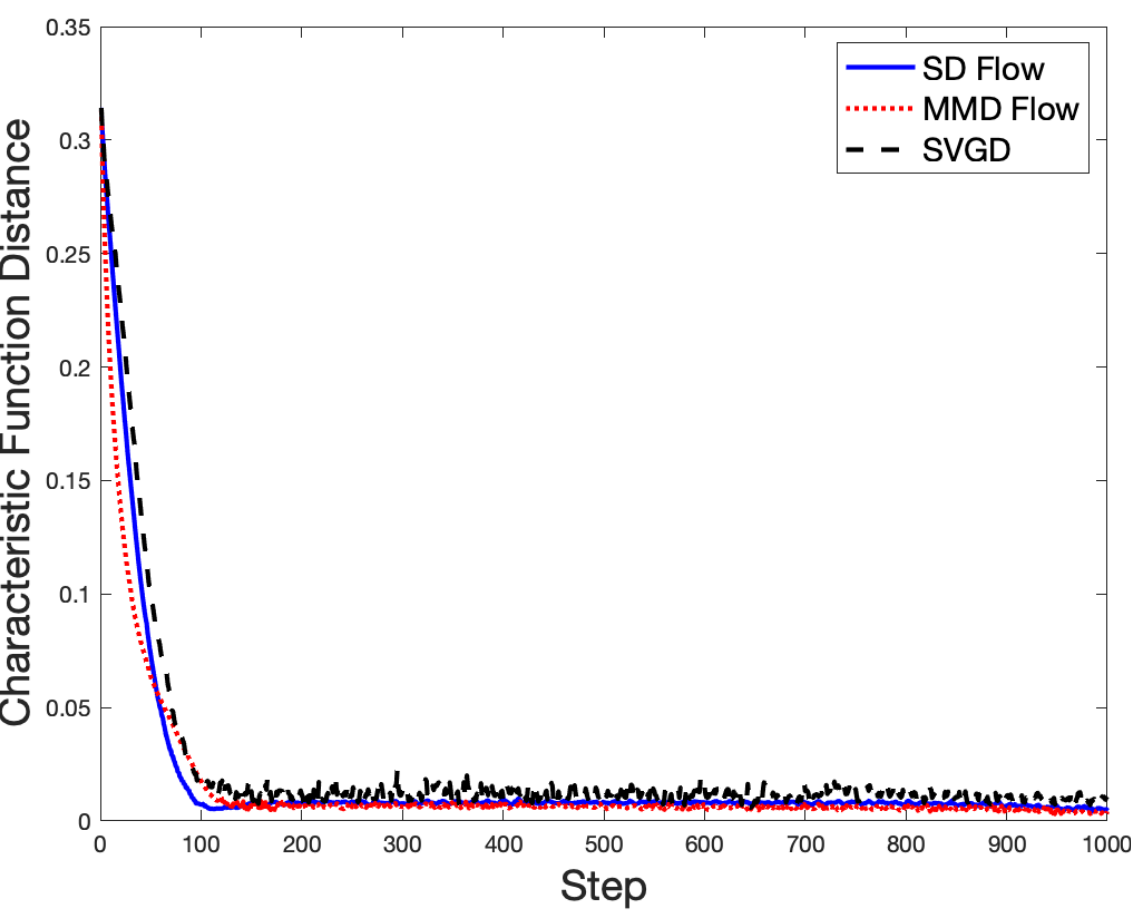
ADAGRAD	BATCH	CONST	ANNEAL	OFFSET
Y	N	N	N	N



ADAGRAD	BATCH	CONST	ANNEAL	OFFSET
Y	N	N	N	Y



ADAGRAD	BATCH	CONST	ANNEAL	OFFSET
Y	N	N	Y	N

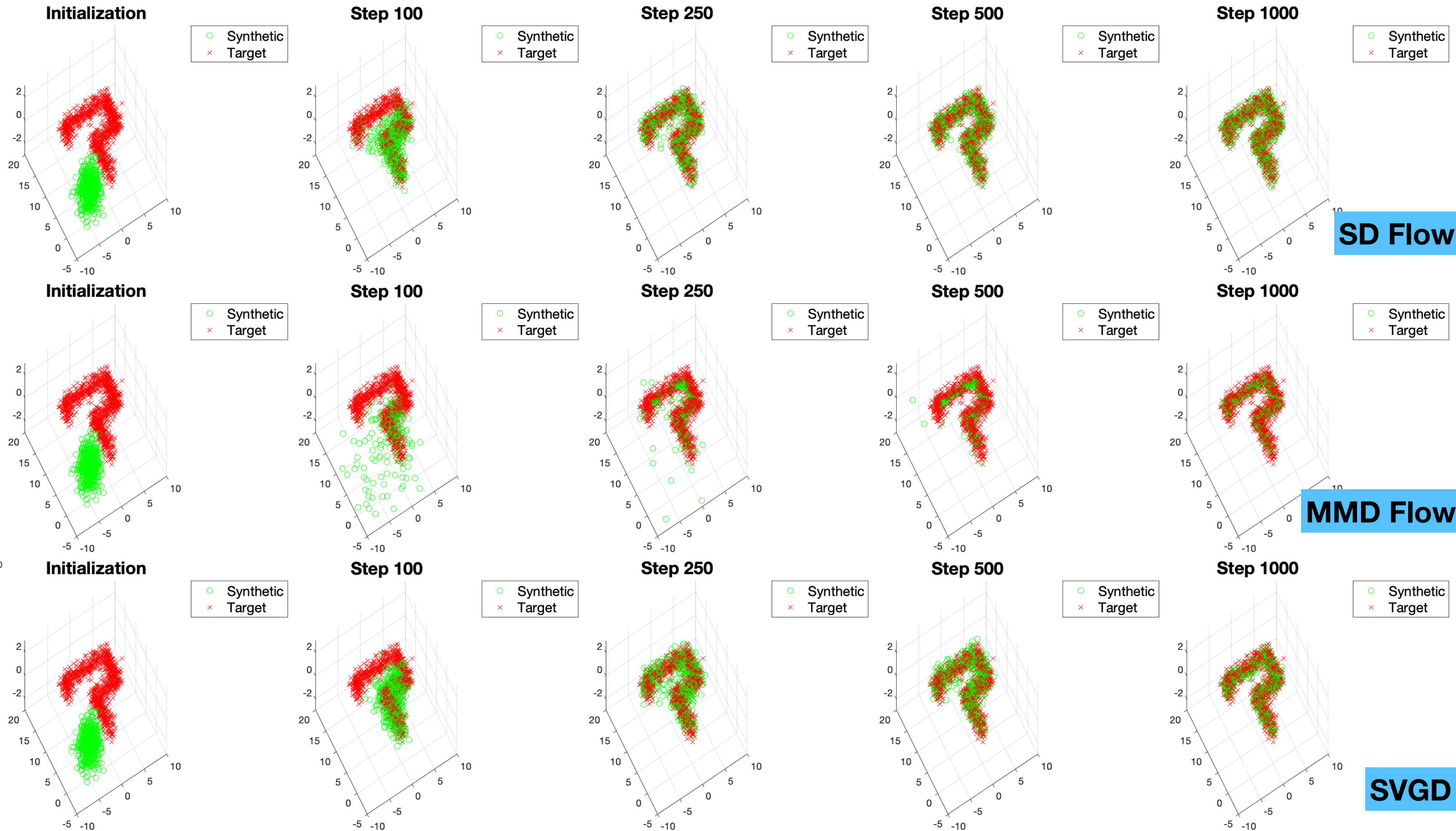
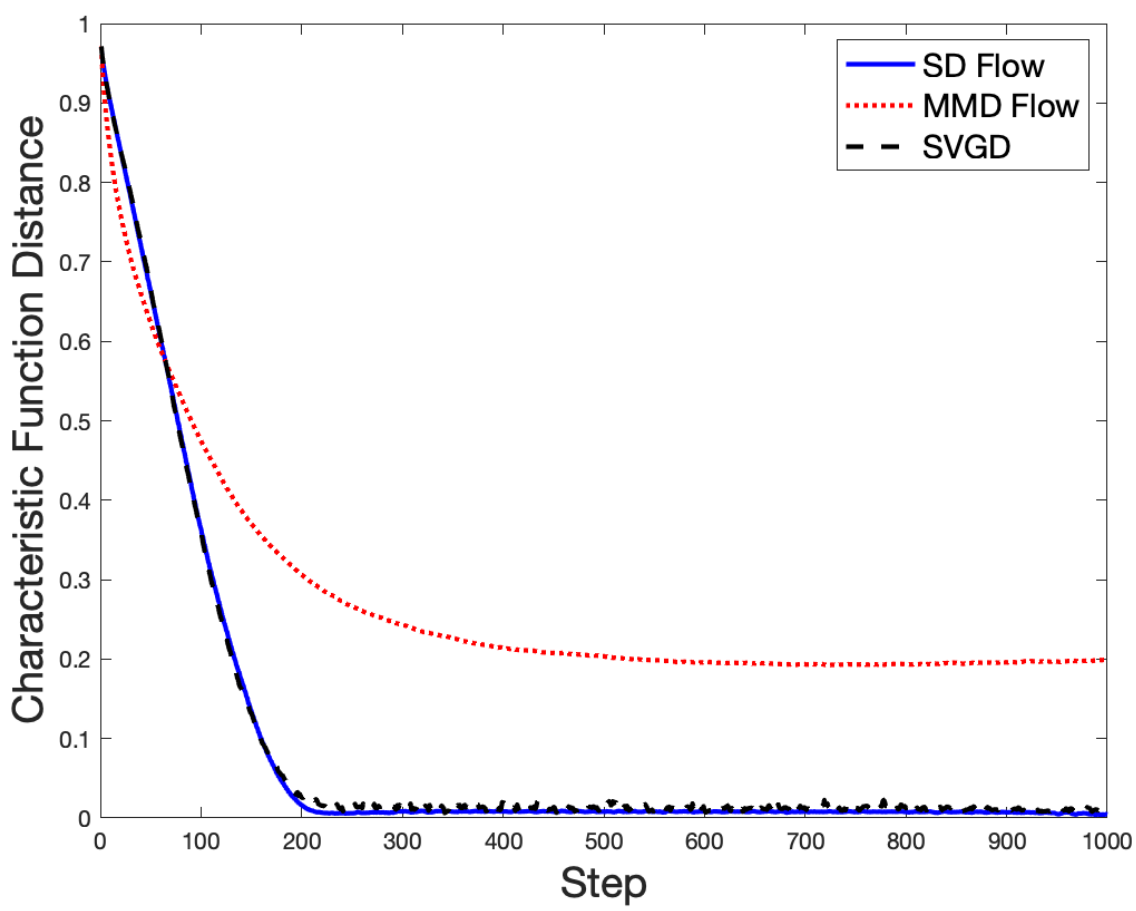


**SD Flow**

**MMD Flow**

**SVGD**

ADAGRAD	BATCH	CONST	ANNEAL	OFFSET
Y	N	N	Y	Y

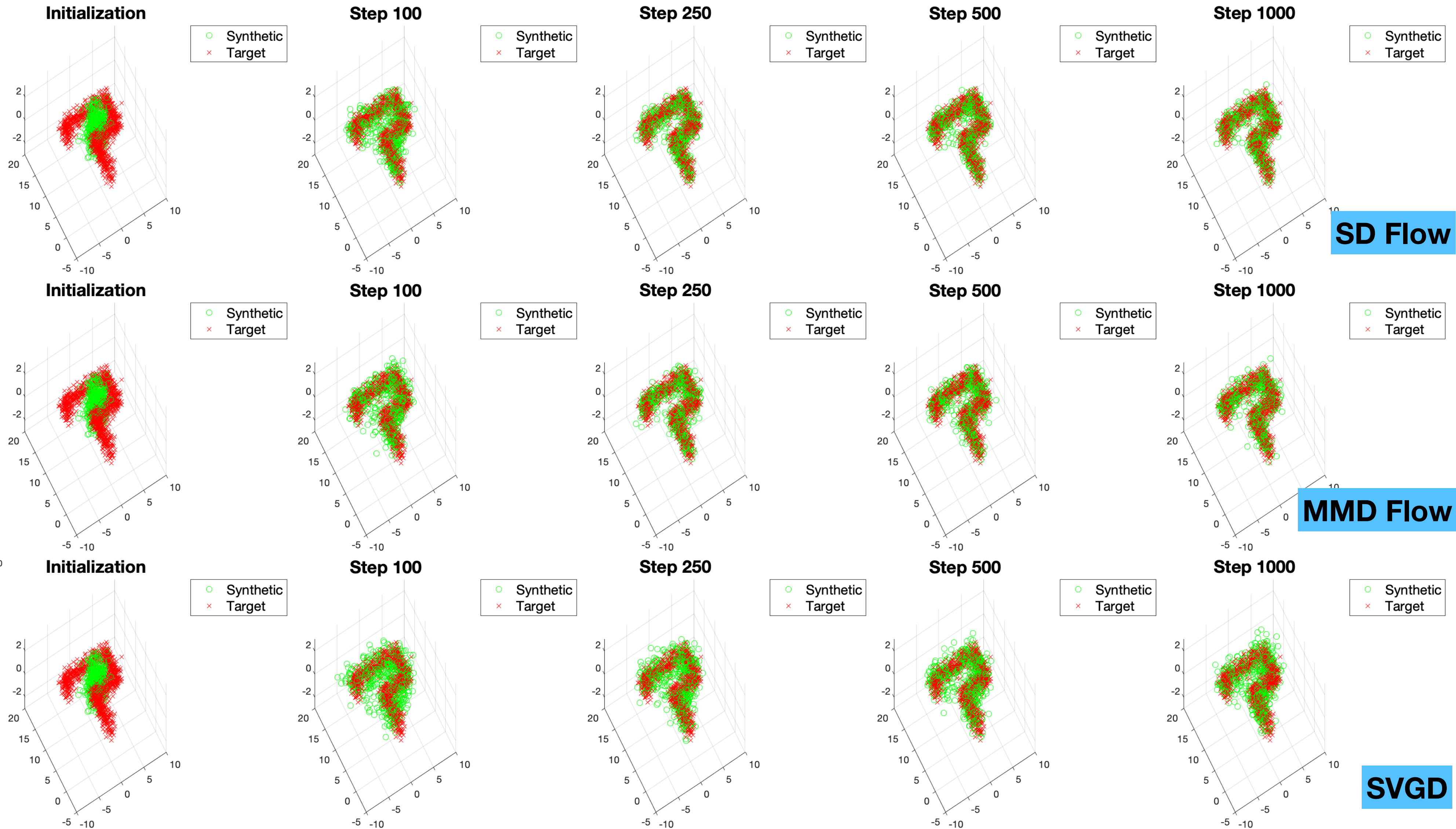
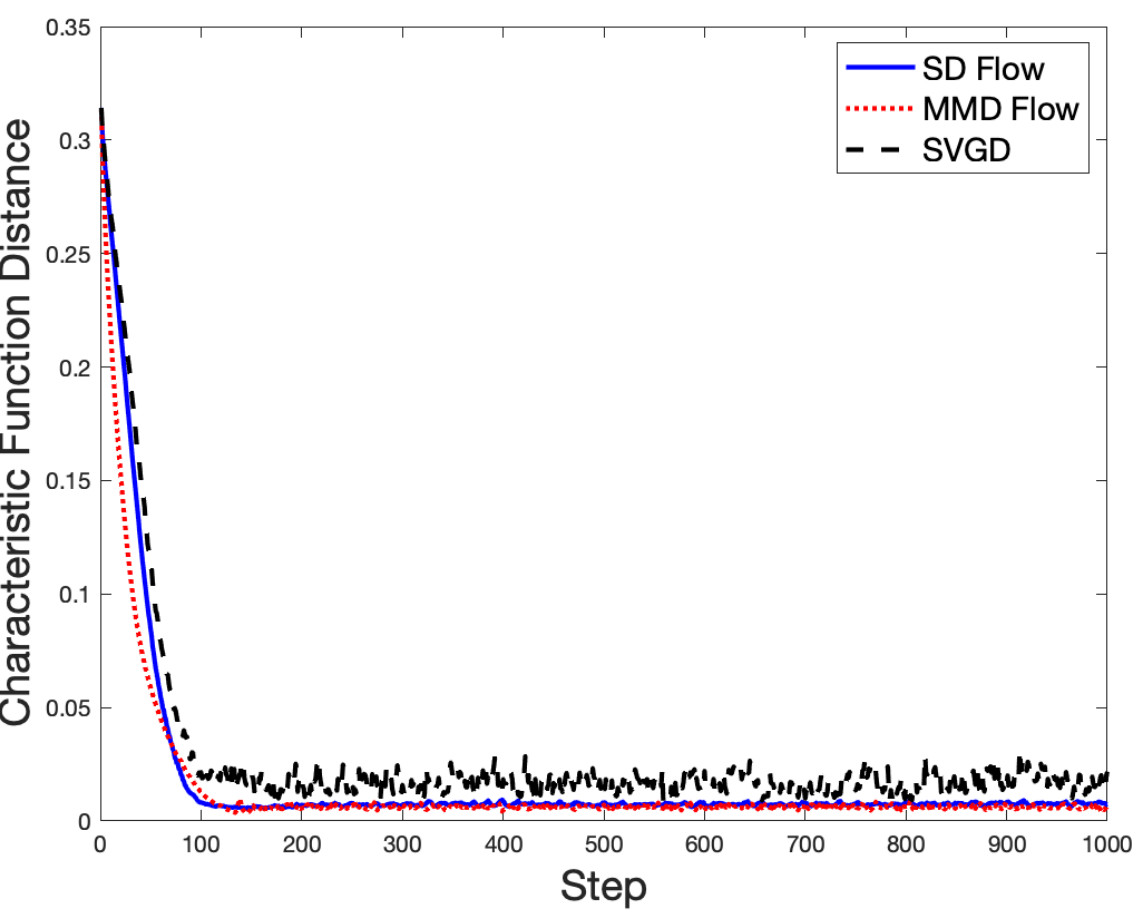


**SD Flow**

**MMD Flow**

**SVG D**

ADAGRAD	BATCH	CONST	ANNEAL	OFFSET
Y	N	Y	N	N

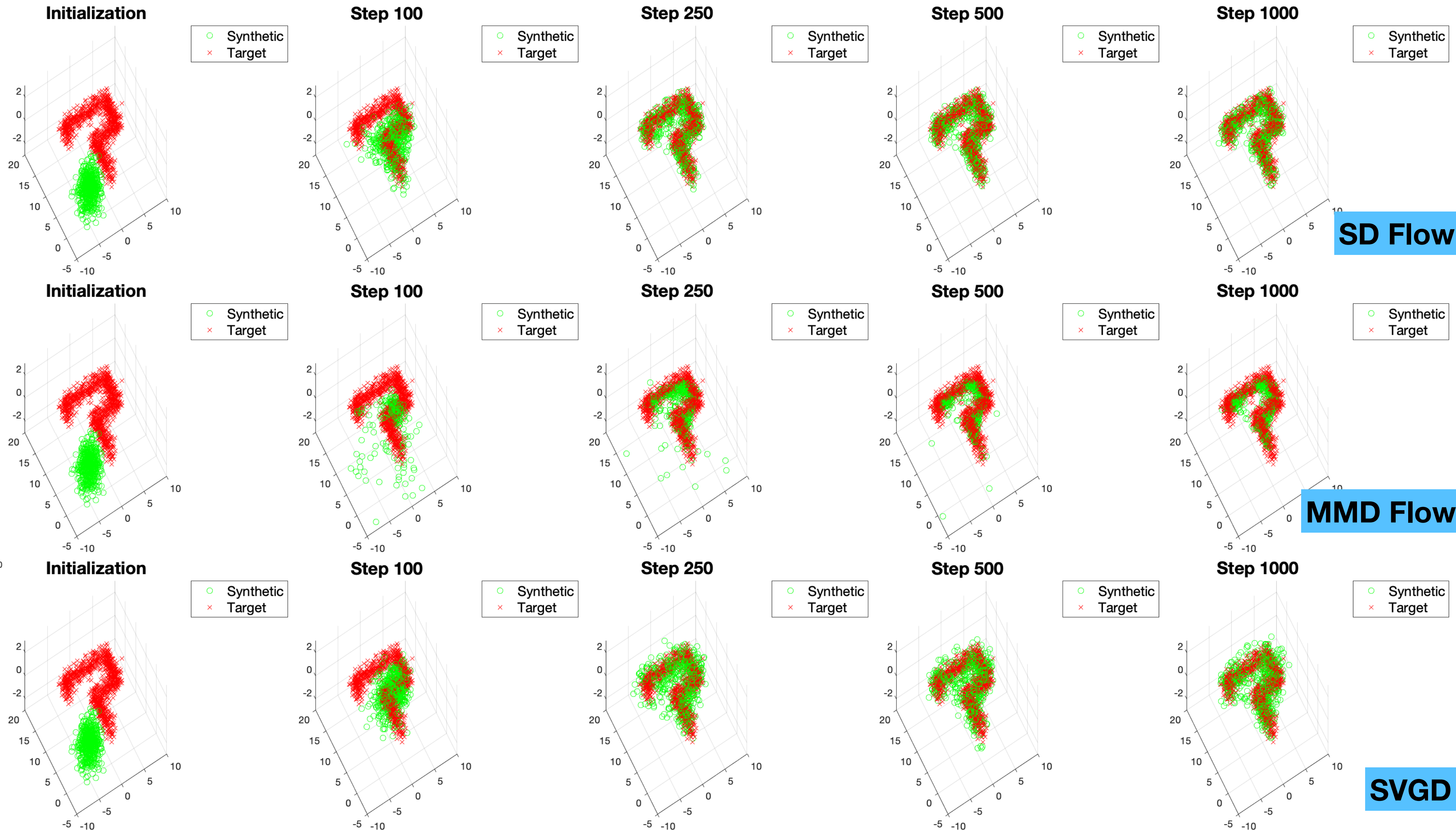
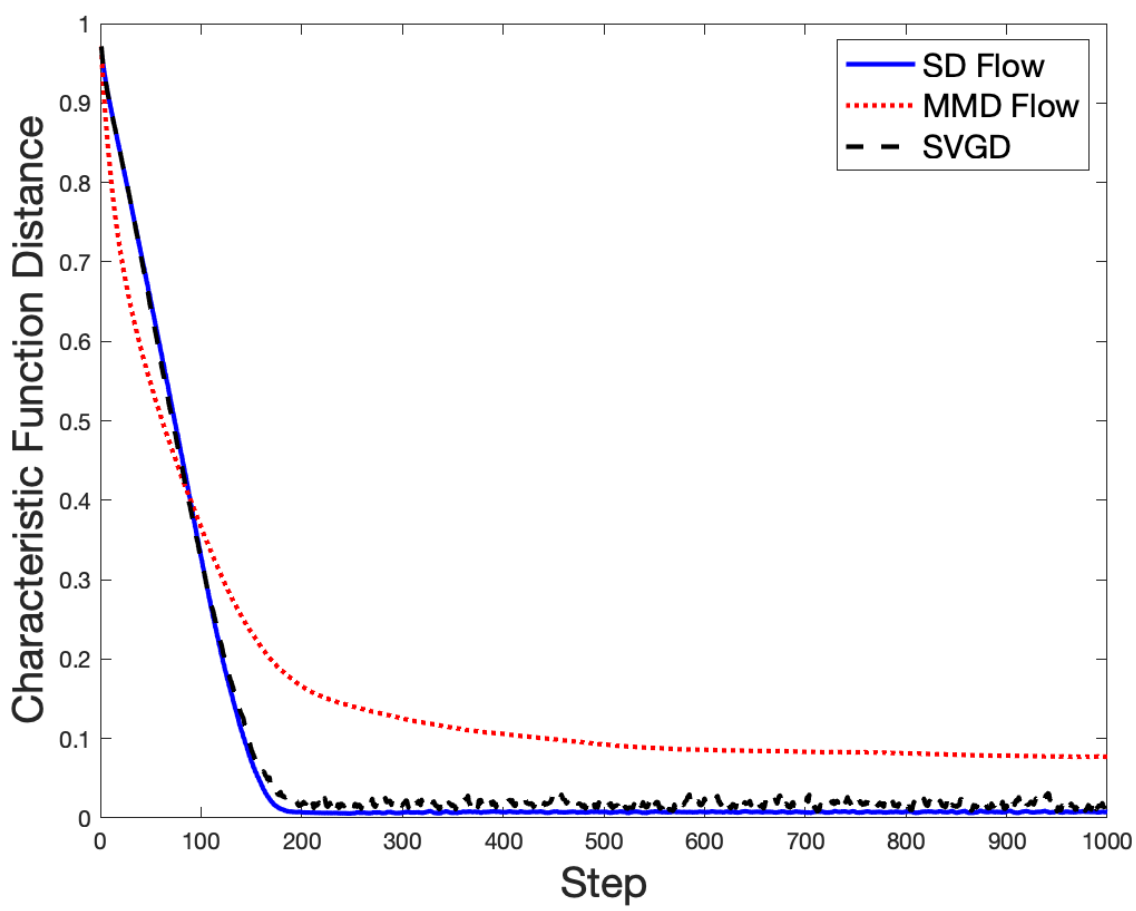


**SD Flow**

**MMD Flow**

**SVGD**

ADAGRAD	BATCH	CONST	ANNEAL	OFFSET
Y	N	Y	N	Y

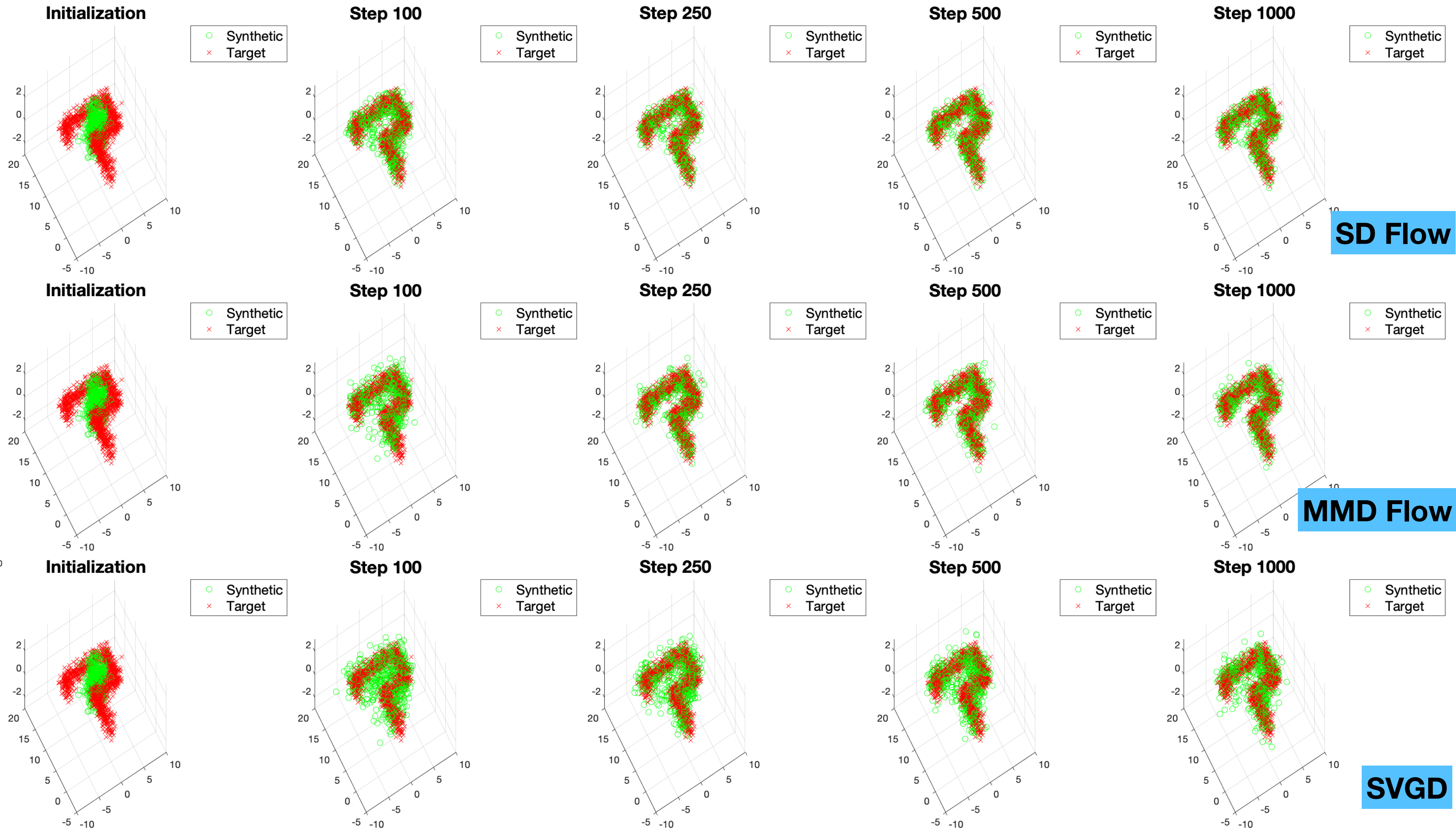
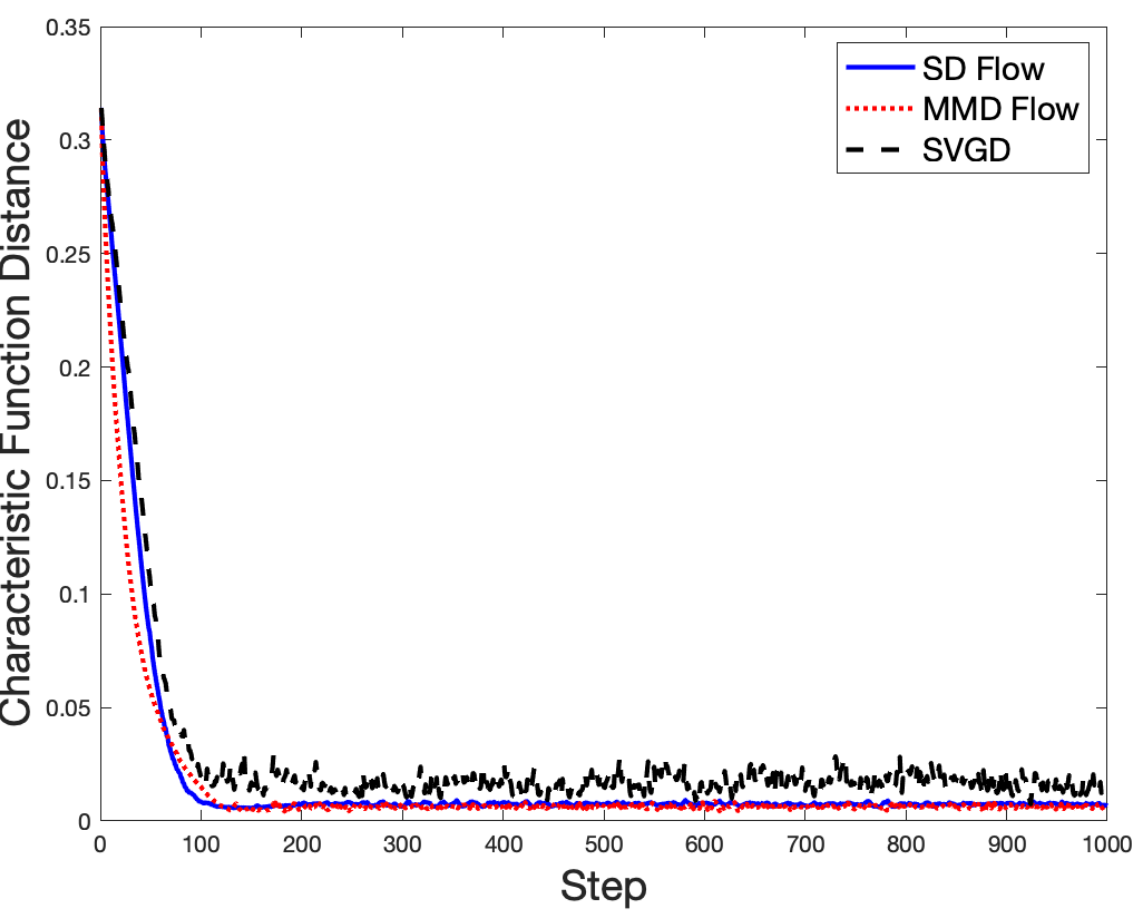


**SD Flow**

**MMD Flow**

**SVG D**

ADAGRAD	BATCH	CONST	ANNEAL	OFFSET
Y	N	Y	Y	N

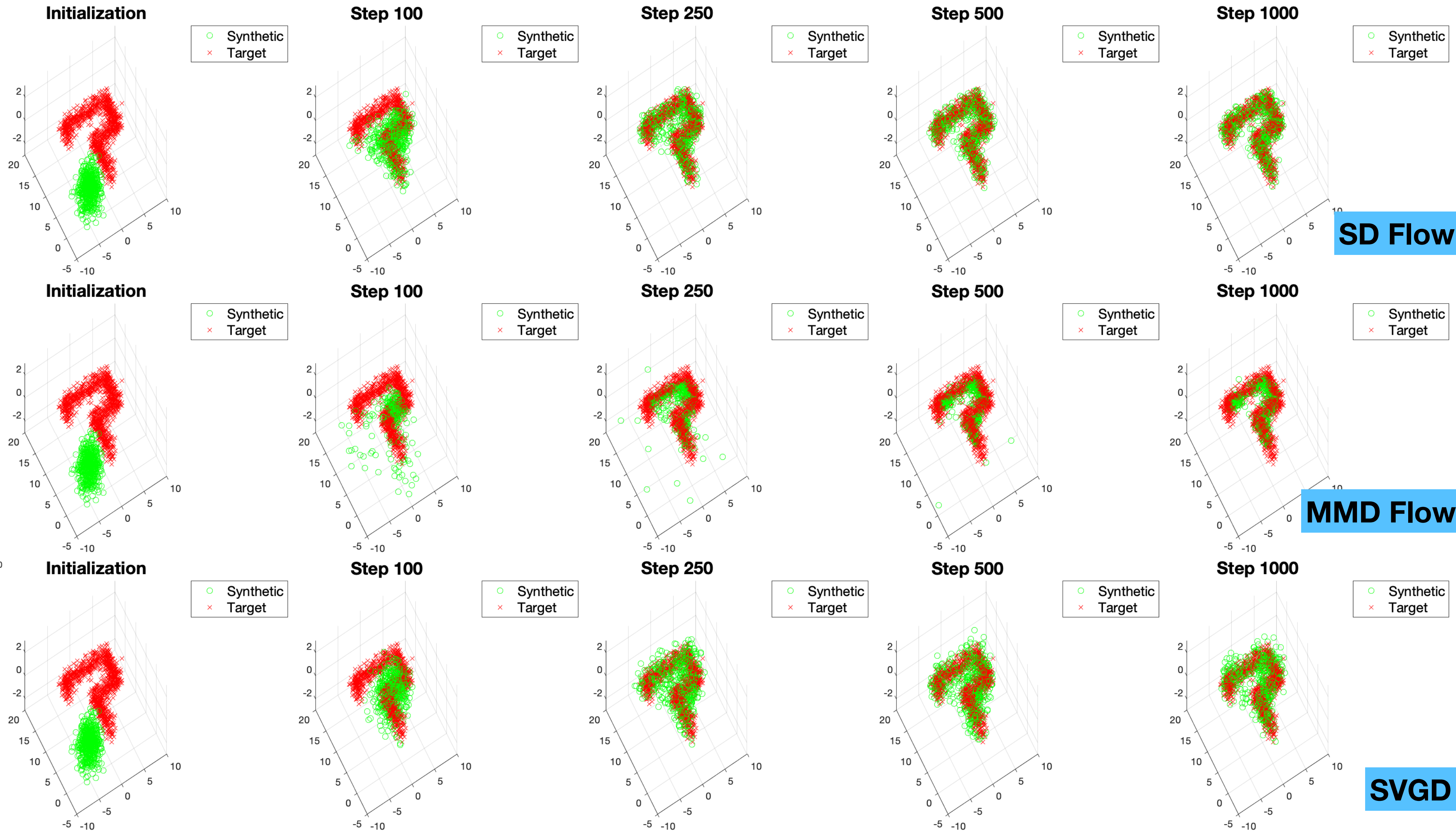
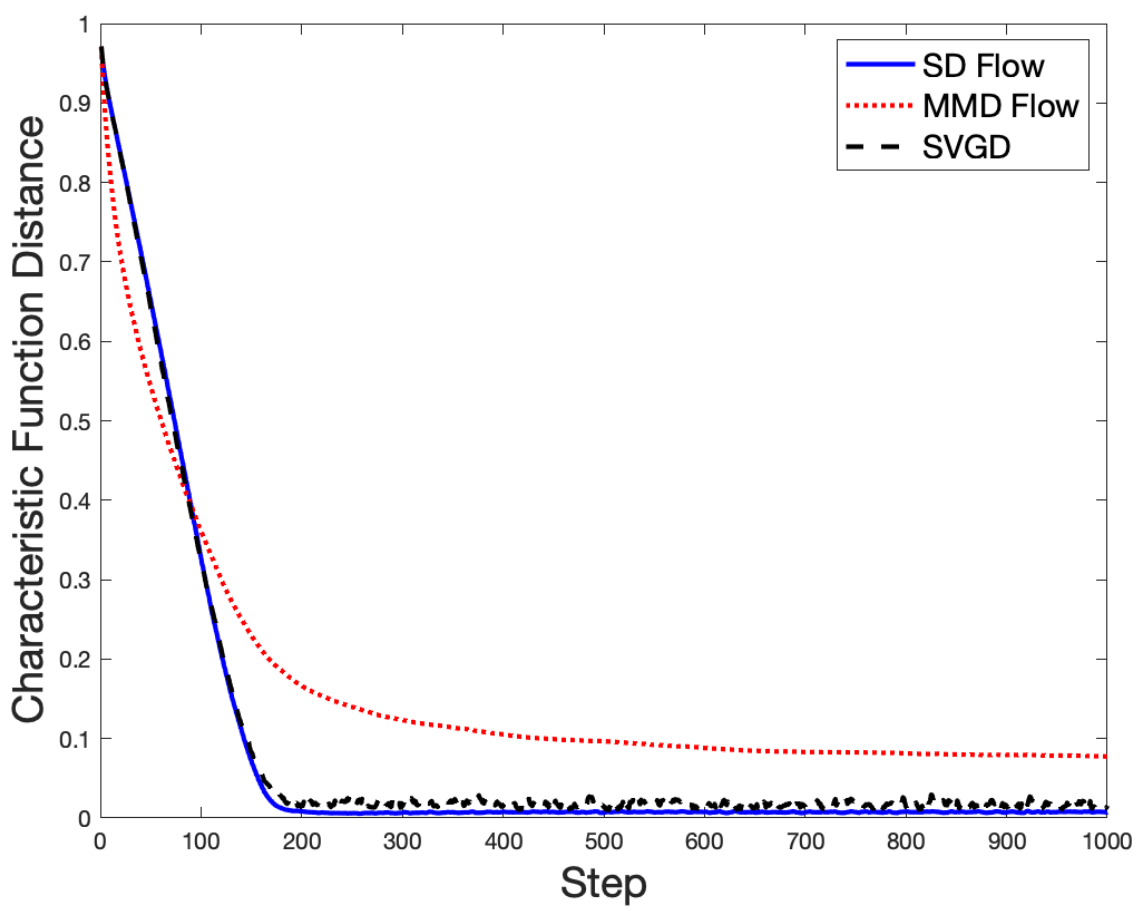


**SD Flow**

**MMD Flow**

**SVGD**

ADAGRAD	BATCH	CONST	ANNEAL	OFFSET
Y	N	Y	Y	Y



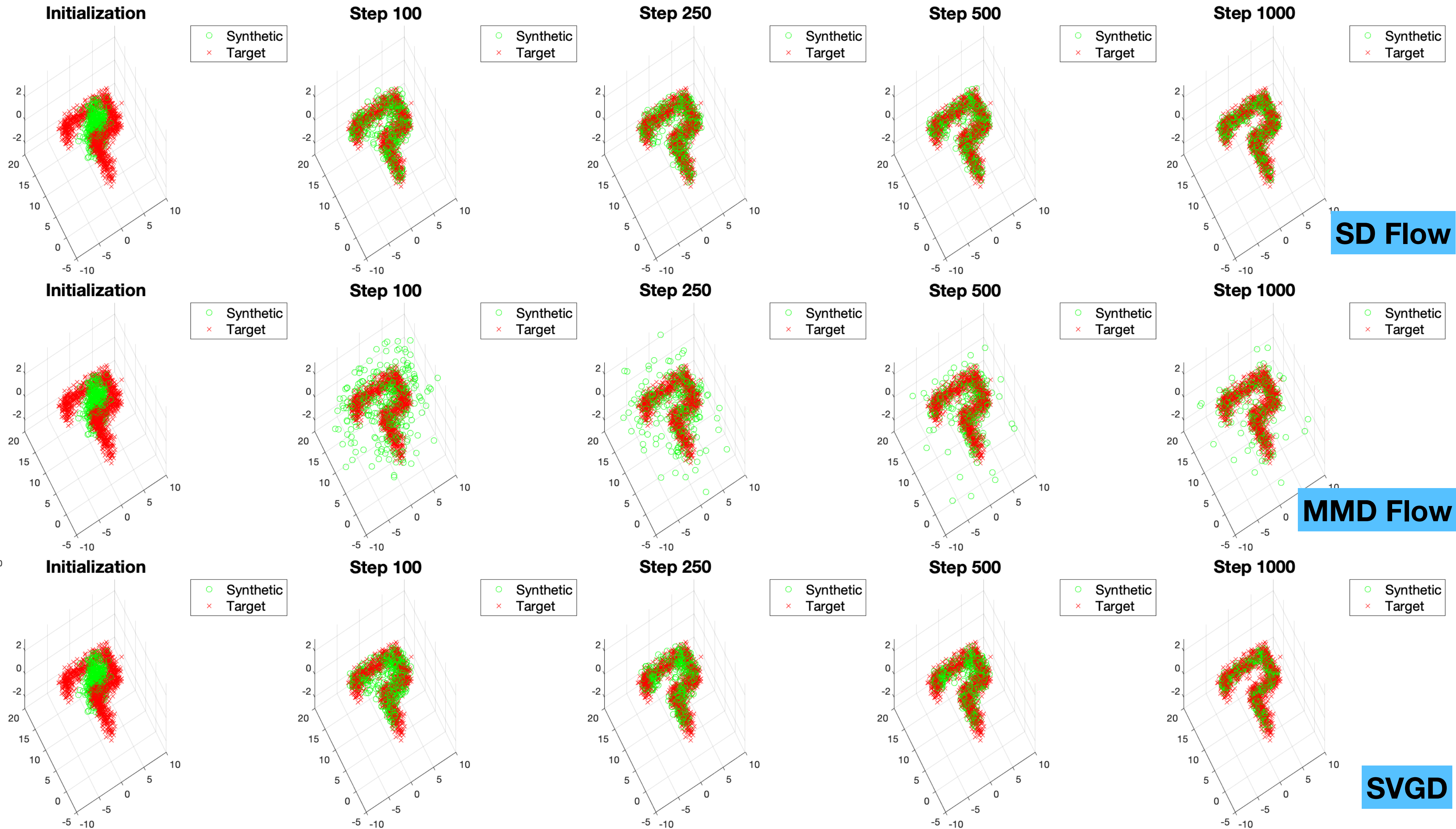
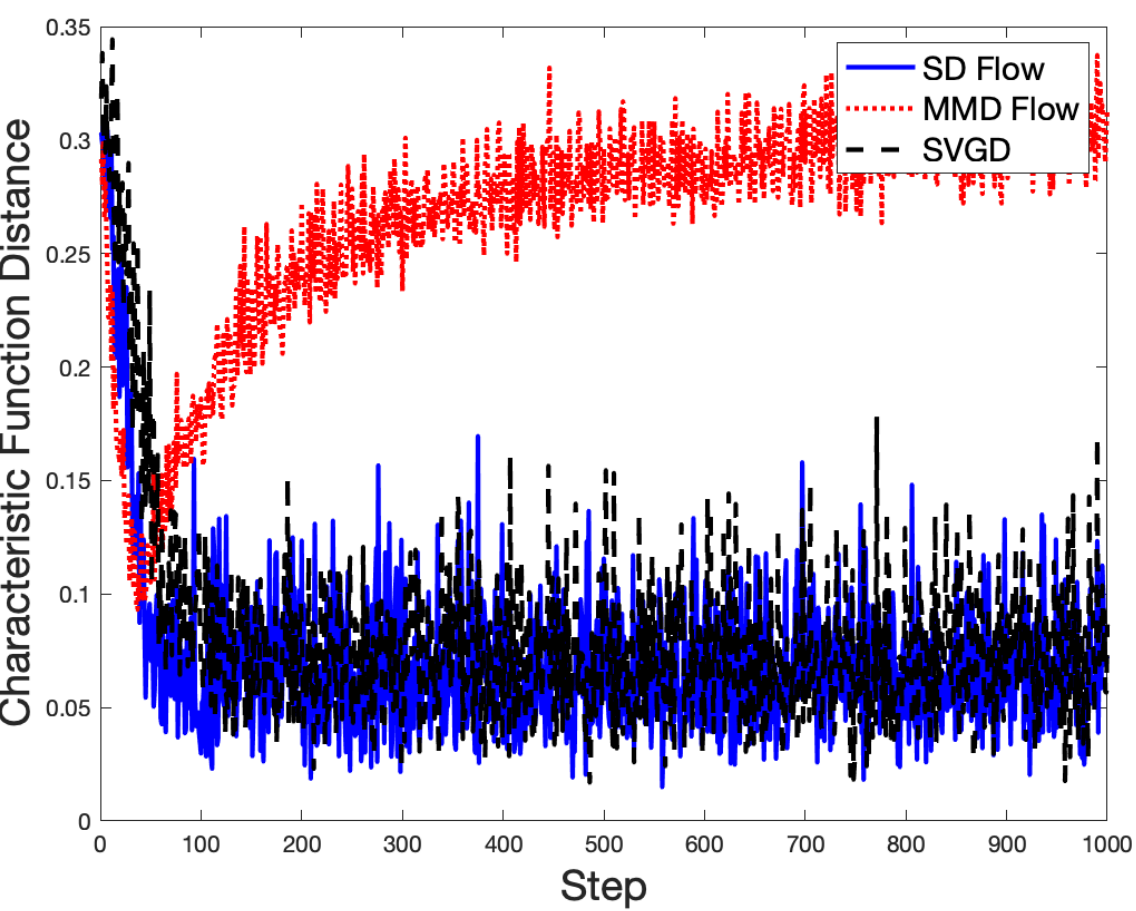
**SD Flow**

**MMD Flow**

**SVGD**



ADAGRAD	BATCH	CONST	ANNEAL	OFFSET
Y	Y	N	N	N

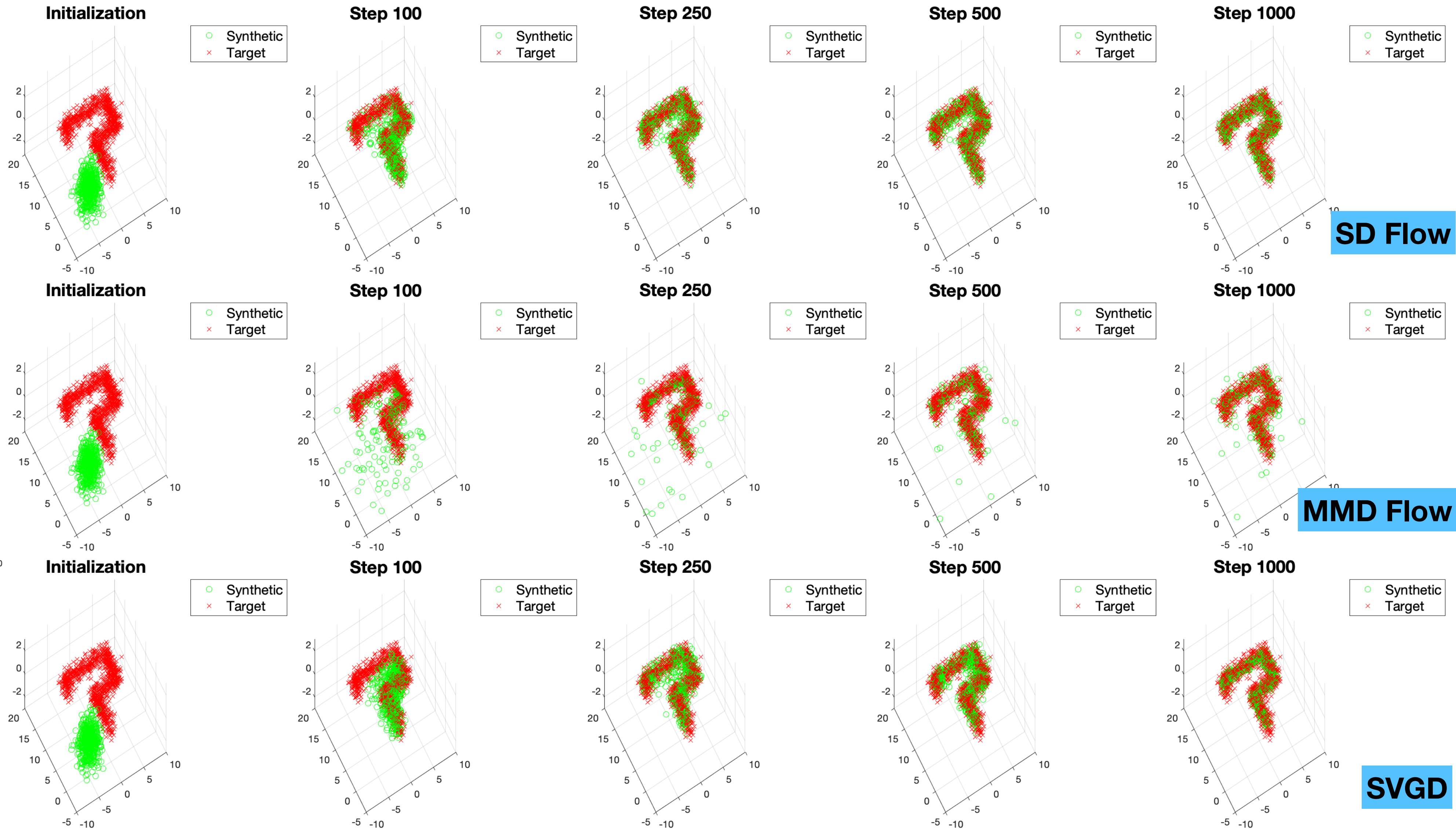
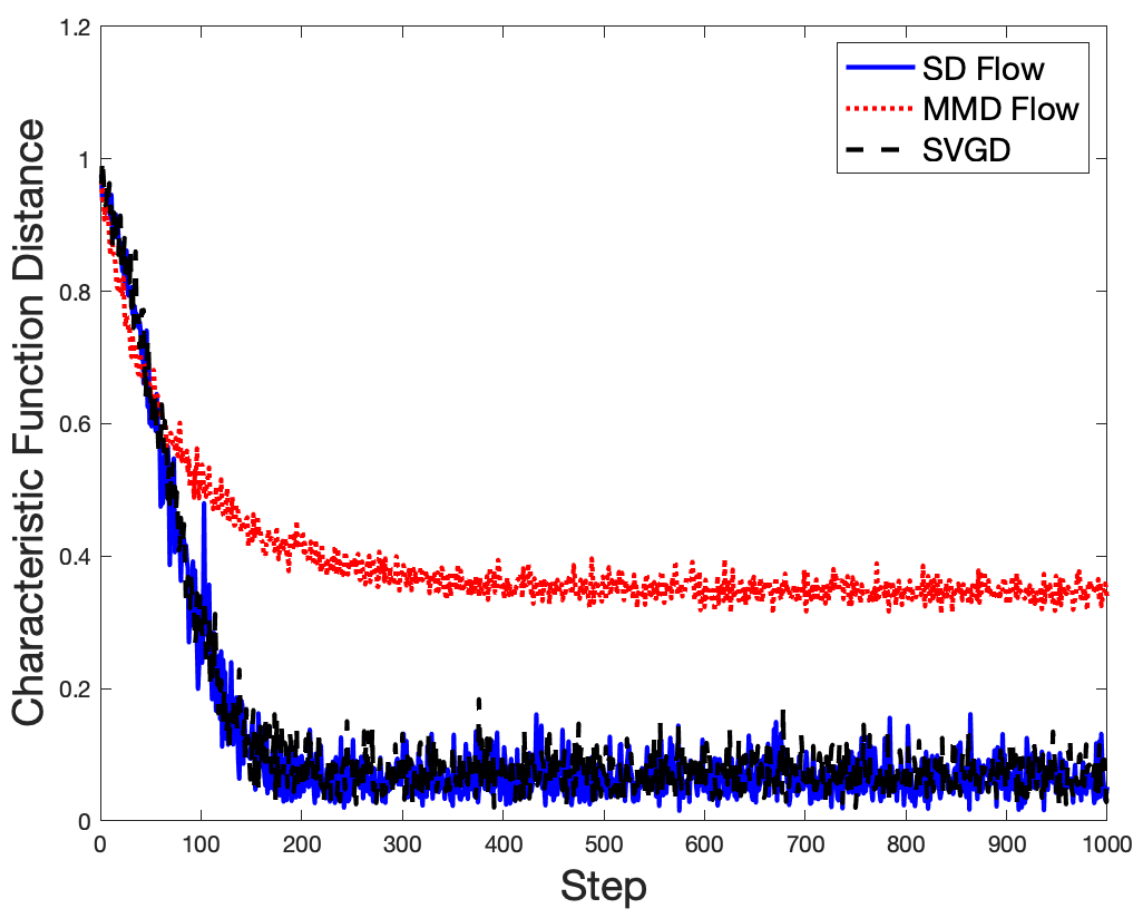


**SD Flow**

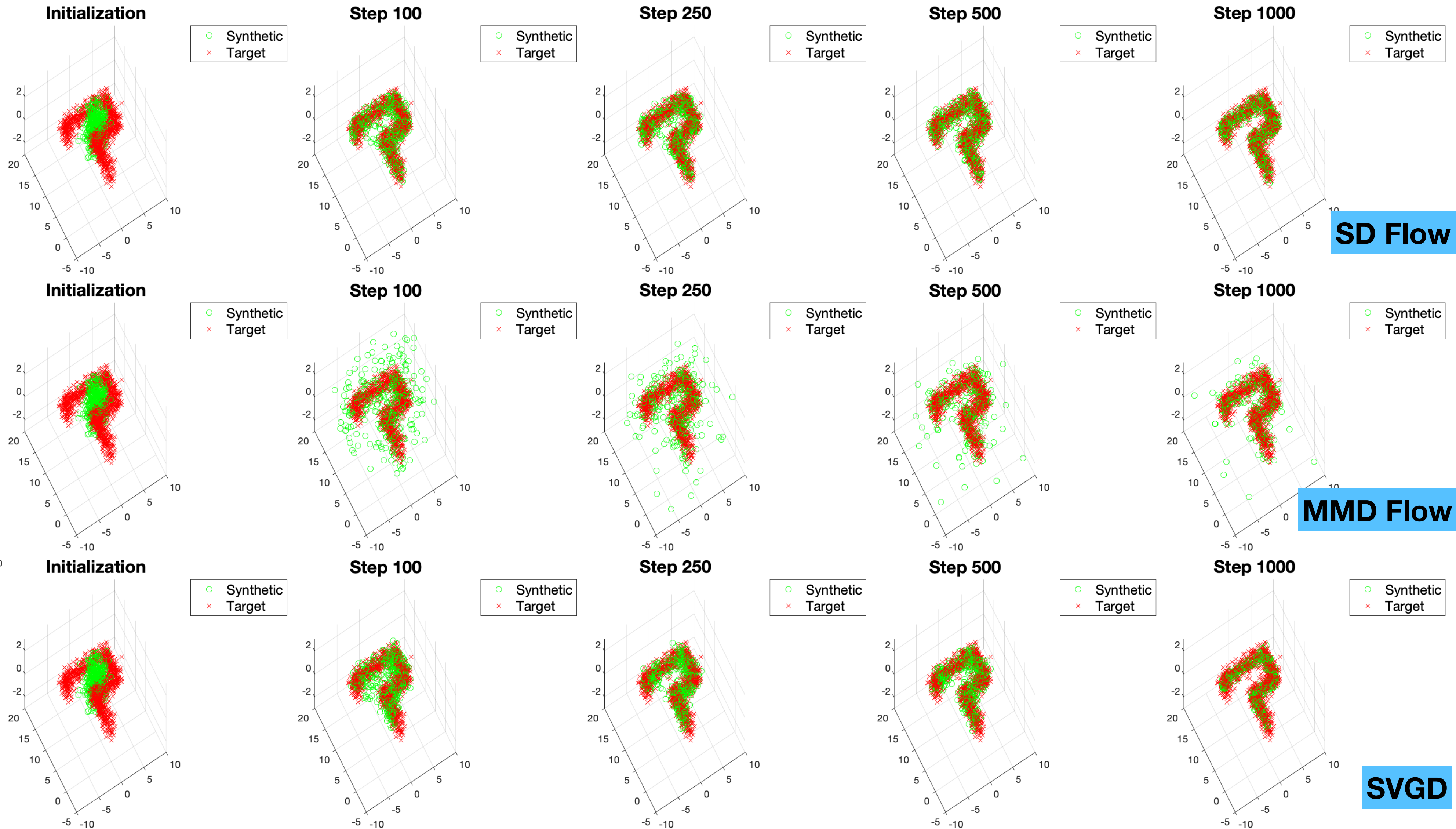
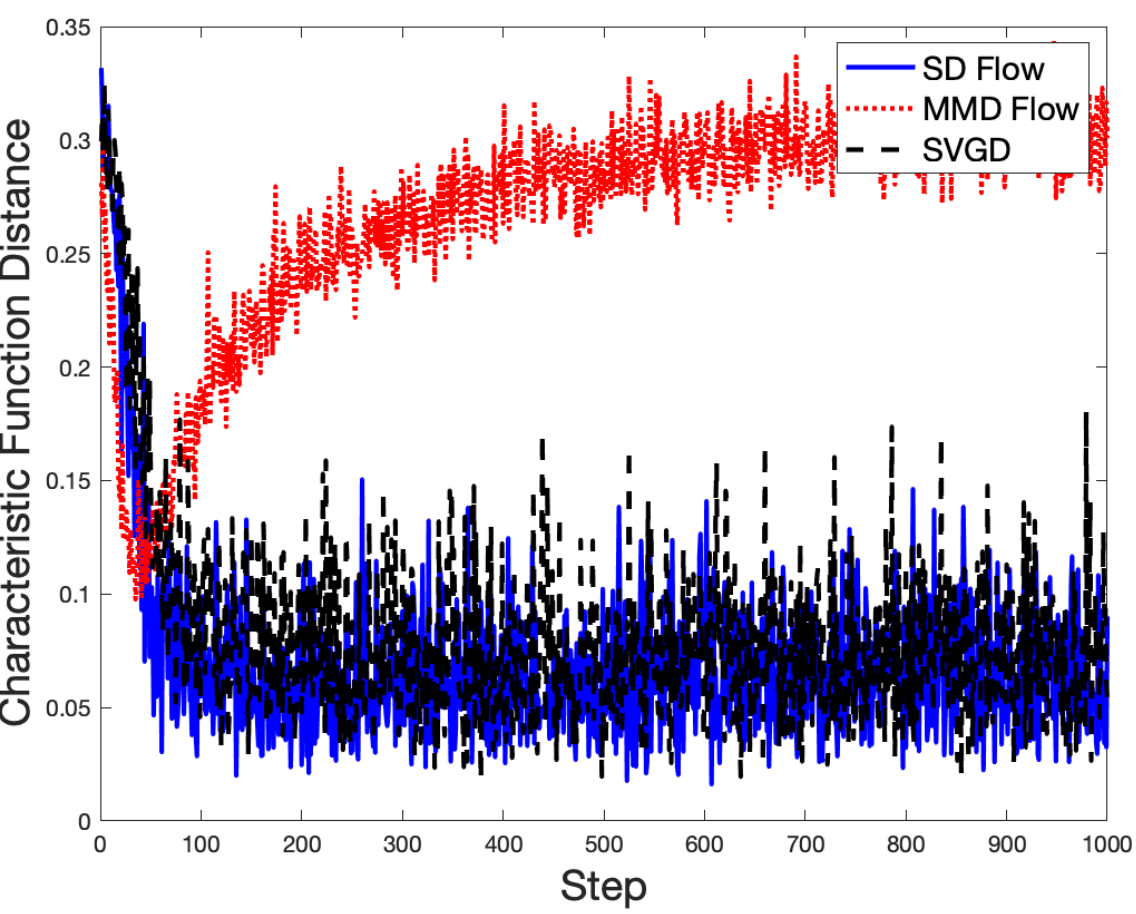
**MMD Flow**

**SVGD**

ADAGRAD	BATCH	CONST	ANNEAL	OFFSET
Y	Y	N	N	Y



ADAGRAD	BATCH	CONST	ANNEAL	OFFSET
Y	Y	N	Y	N

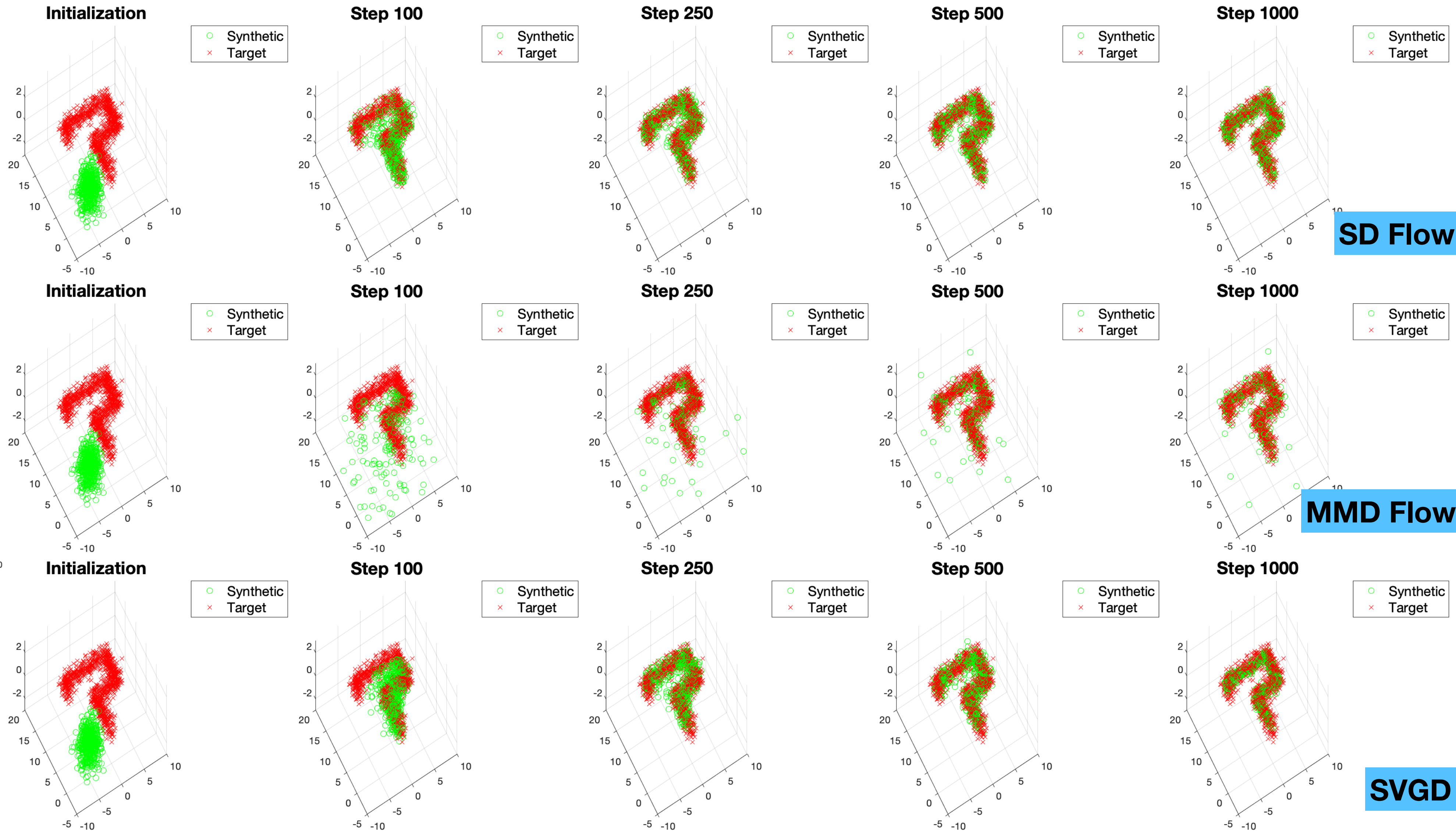
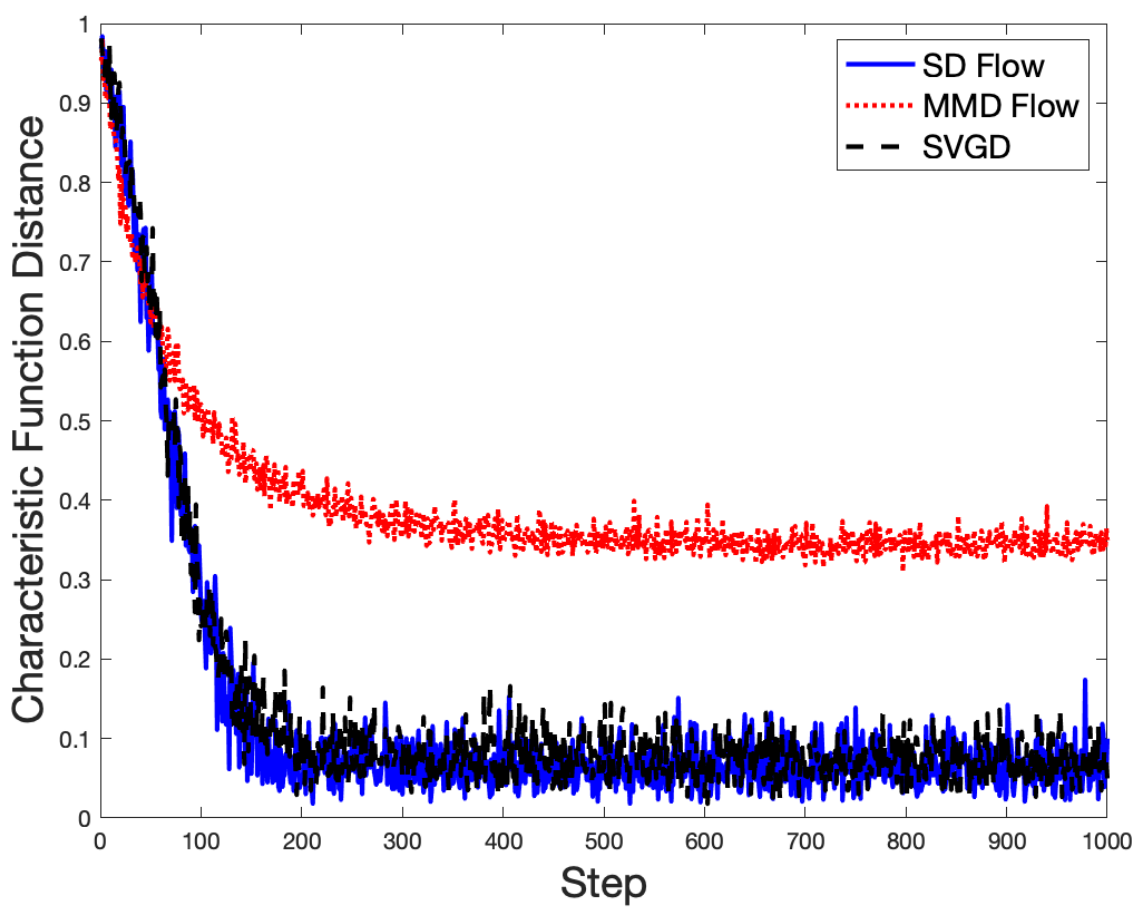


**SD Flow**

**MMD Flow**

**SVG D**

ADAGRAD	BATCH	CONST	ANNEAL	OFFSET
Y	Y	N	Y	Y

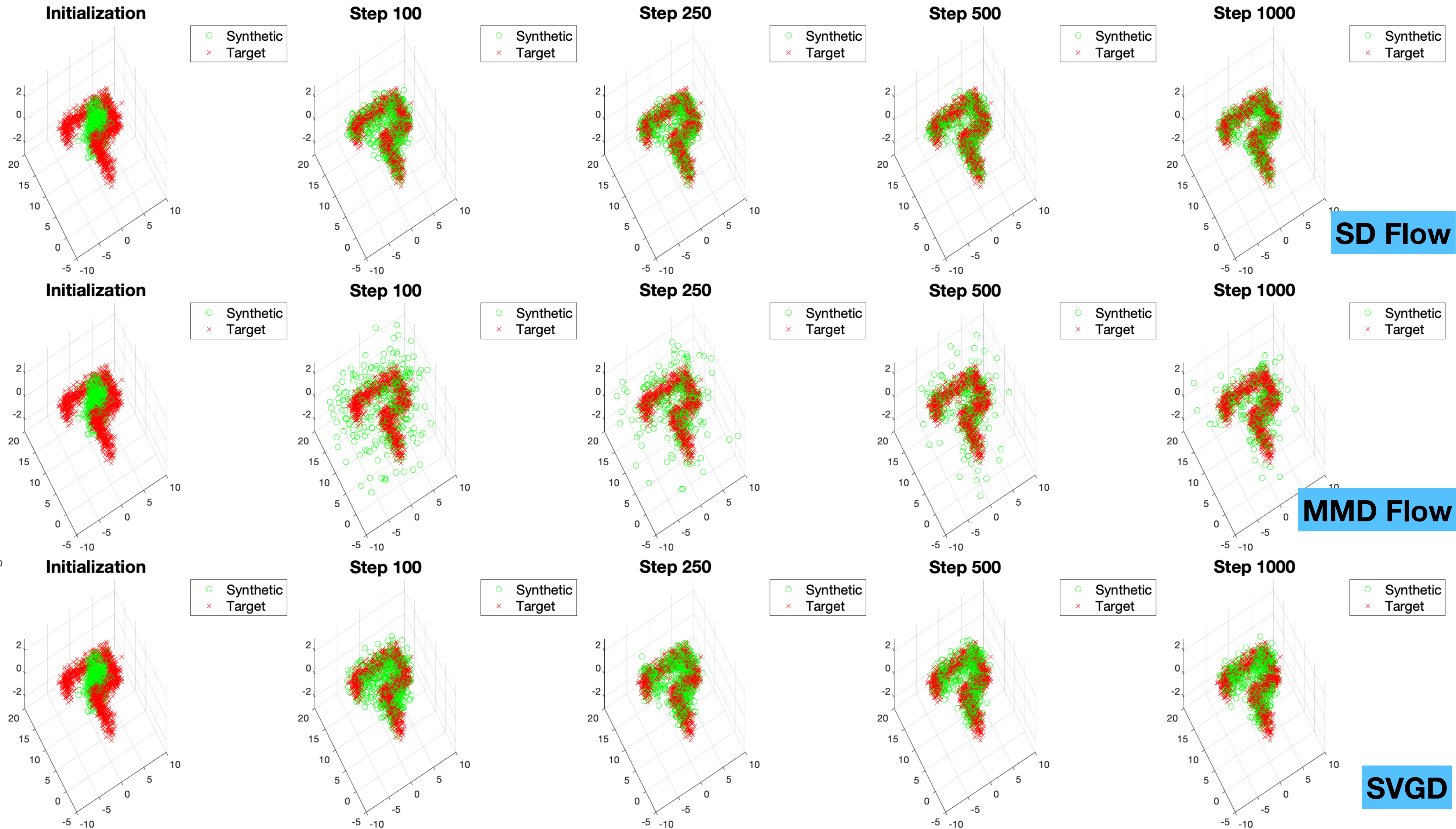
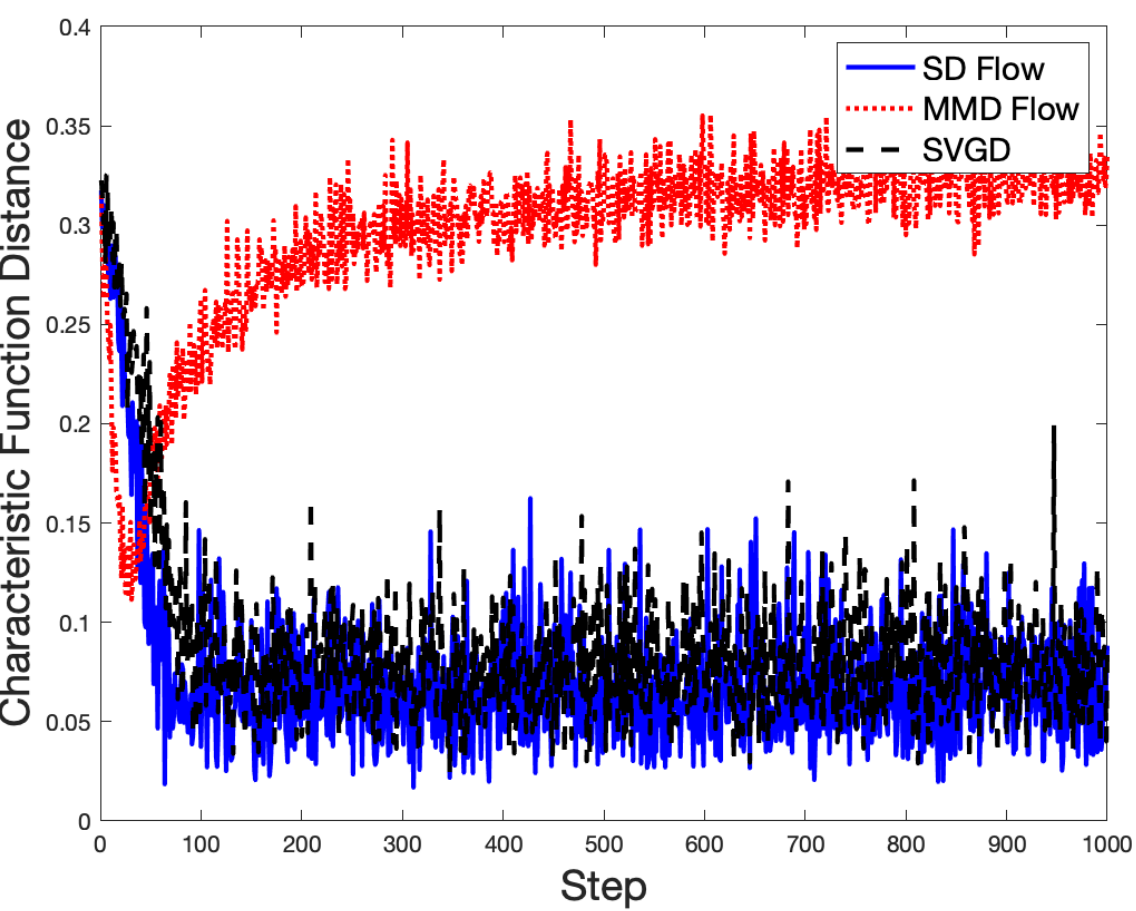


**SD Flow**

**MMD Flow**

**SVGD**

ADAGRAD	BATCH	CONST	ANNEAL	OFFSET
Y	Y	Y	N	N

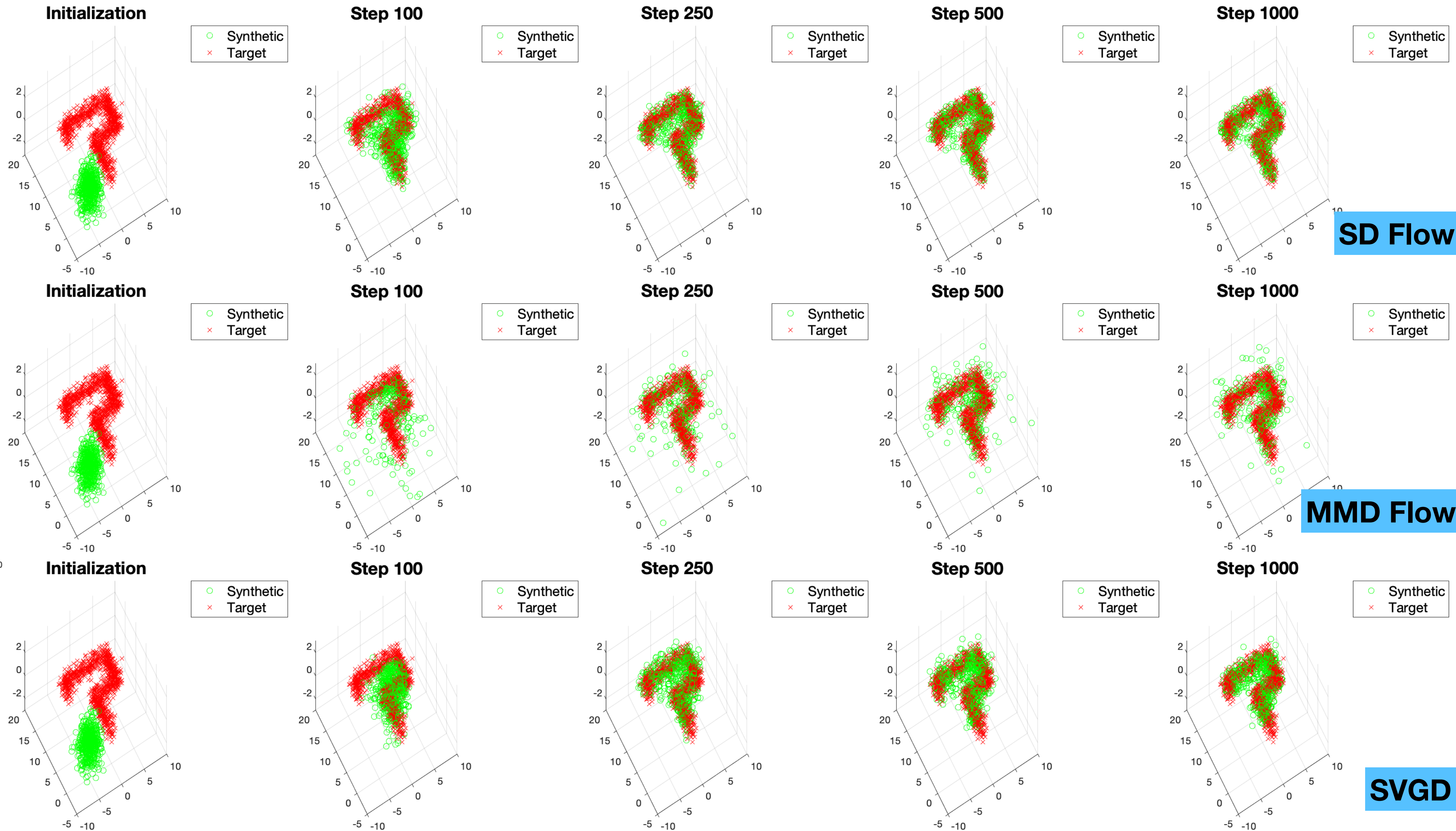
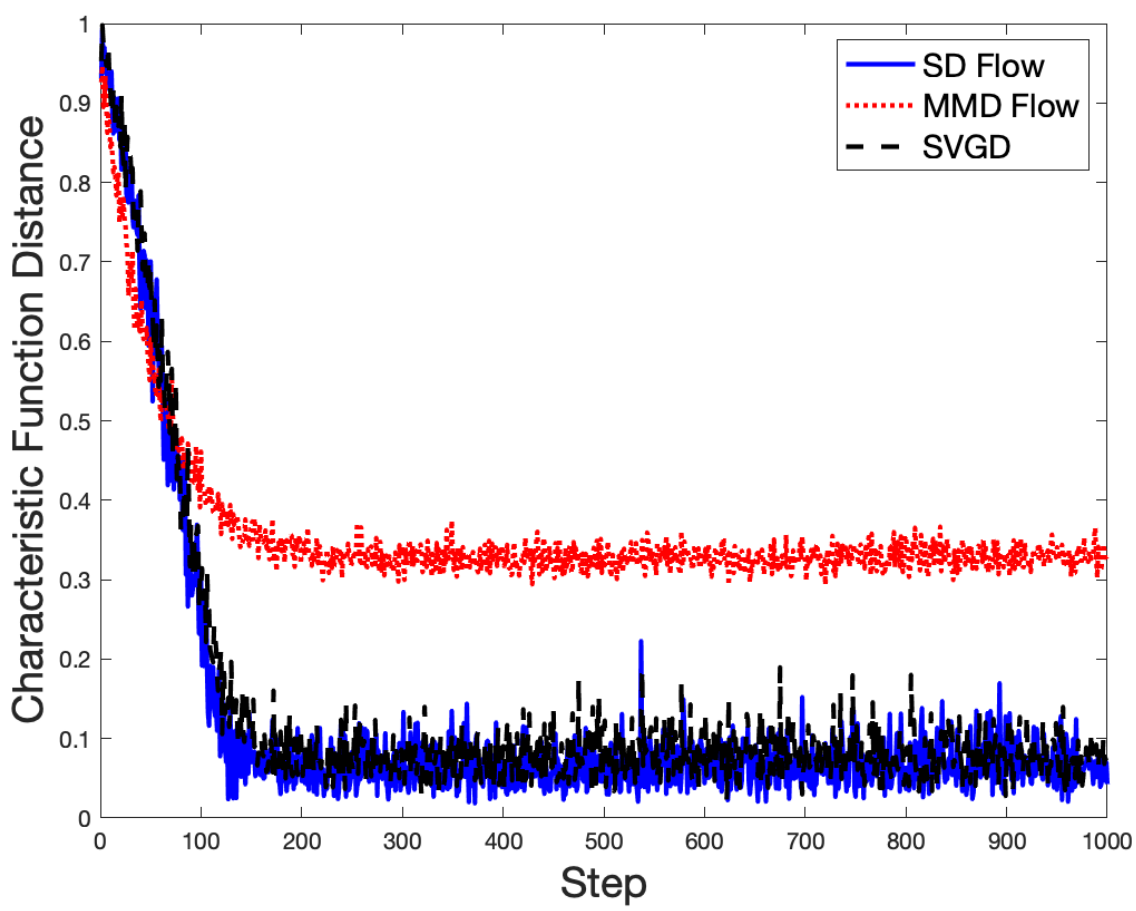


**SD Flow**

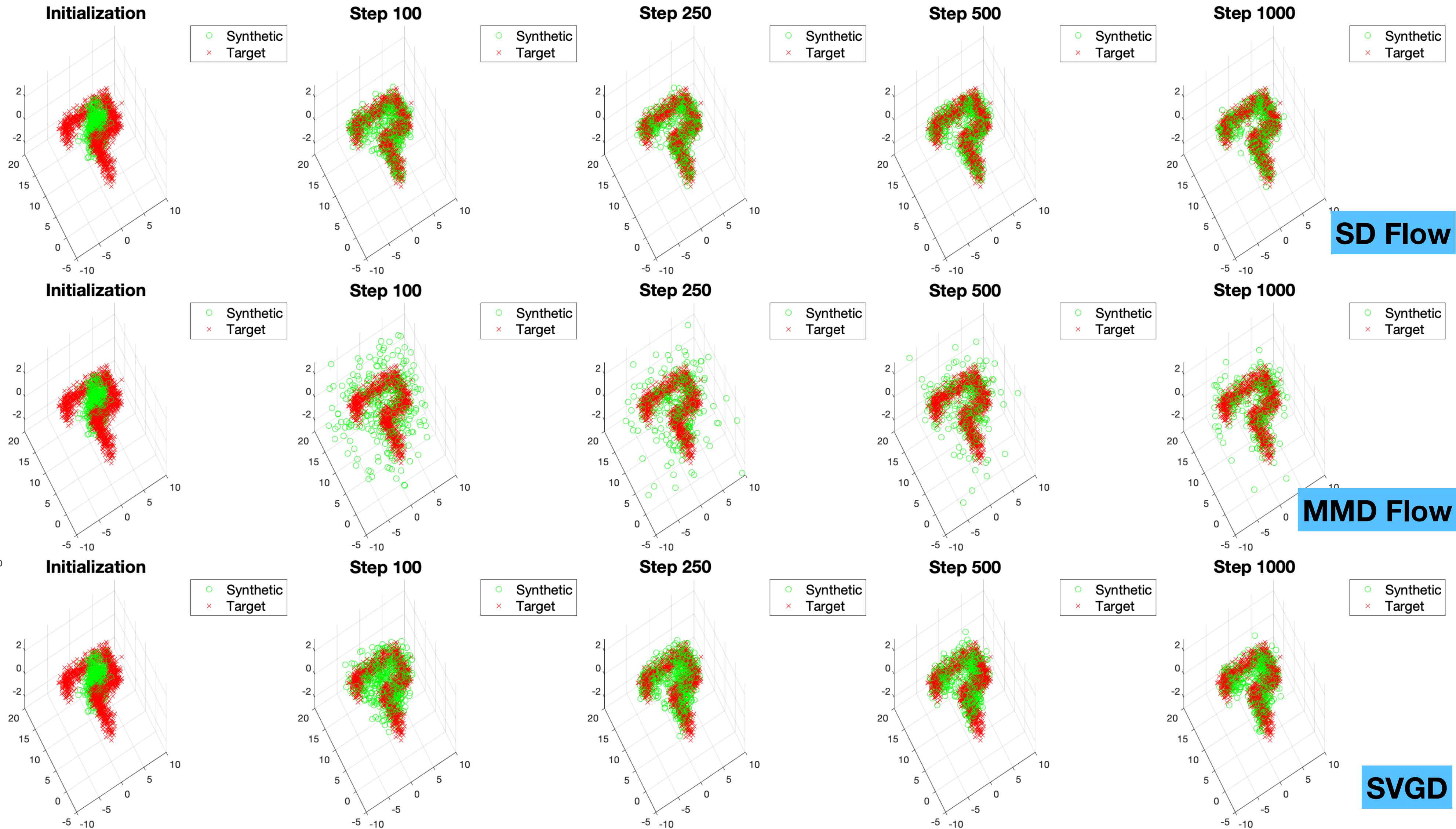
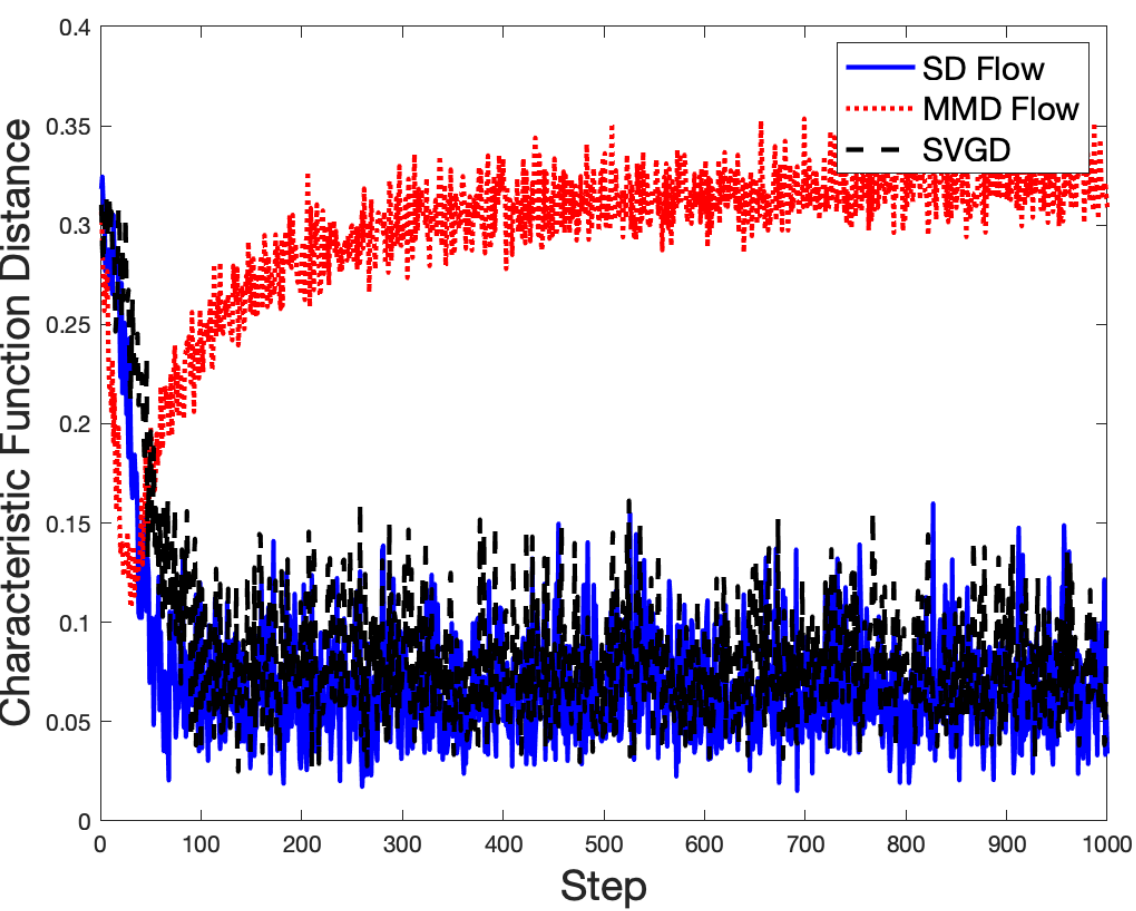
**MMD Flow**

**SVGD**

ADAGRAD	BATCH	CONST	ANNEAL	OFFSET
Y	Y	Y	N	Y



ADAGRAD	BATCH	CONST	ANNEAL	OFFSET
Y	Y	Y	Y	N

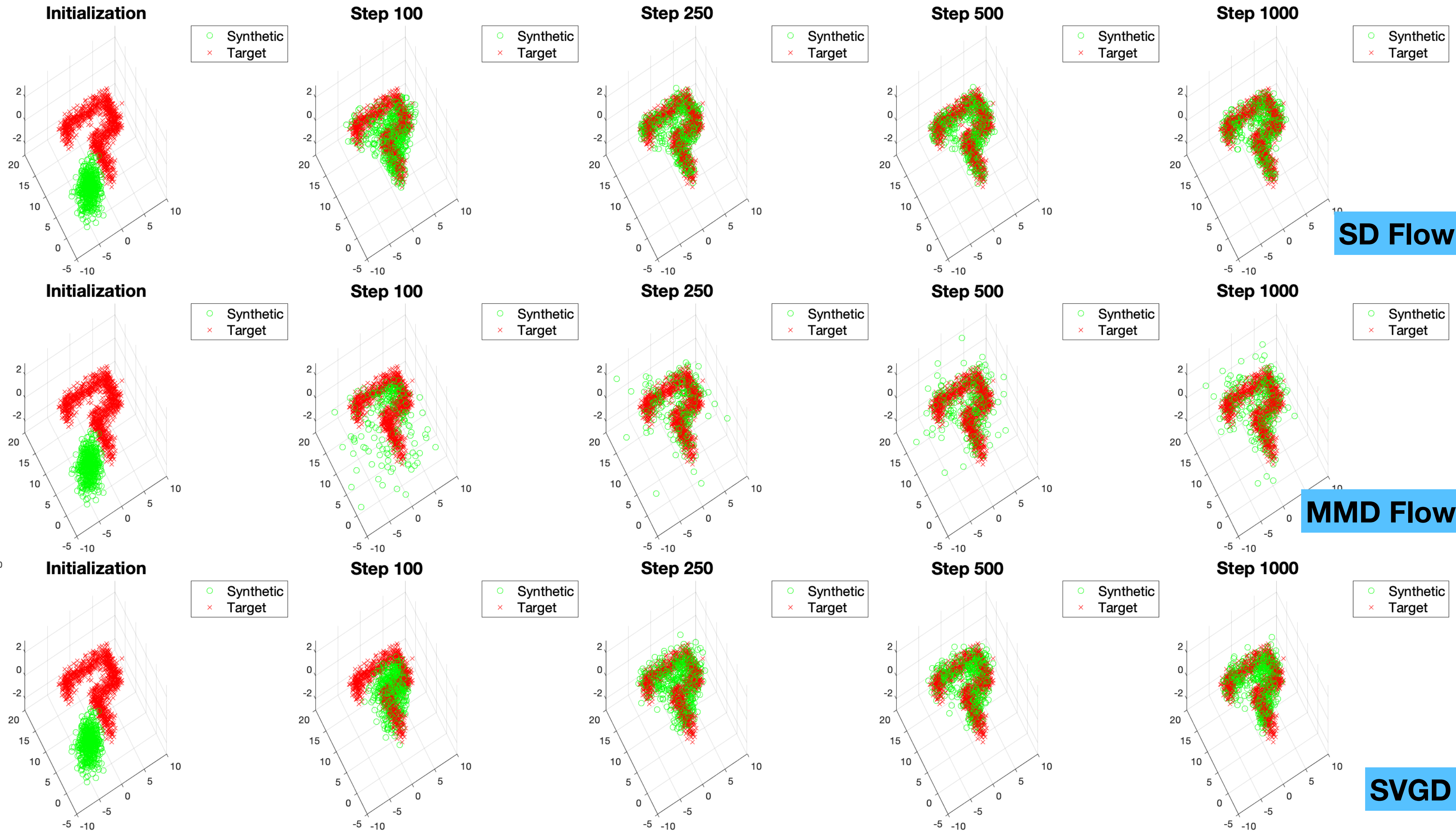
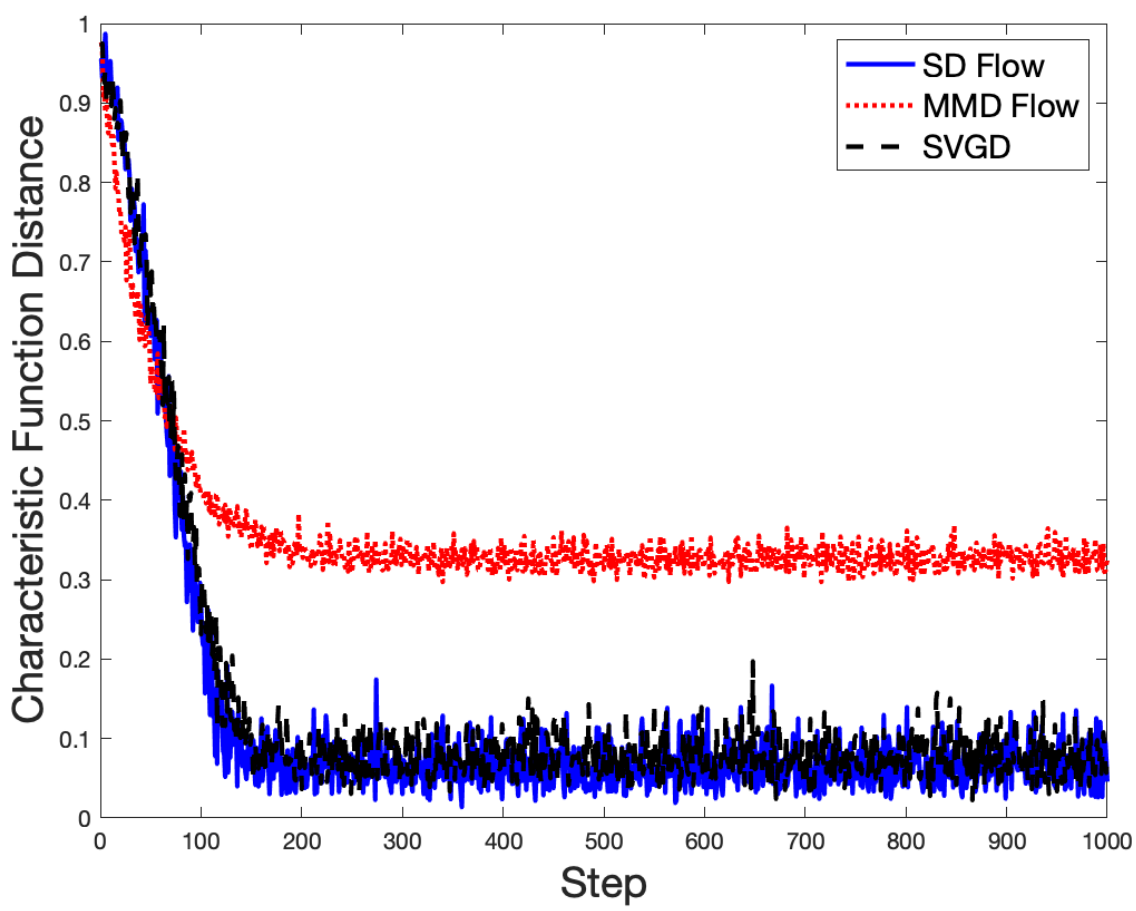


**SD Flow**

**MMD Flow**

**SVGD**

ADAGRAD	BATCH	CONST	ANNEAL	OFFSET
Y	Y	Y	Y	Y



**SD Flow**

**MMD Flow**

**SVGD**