

ACCELERATING BLOCK COORDINATE DESCENT FOR LLM FINETUNING VIA LANDSCAPE CORRECTION

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ABSTRACT

Training and finetuning large language models (LLMs) are resource-intensive tasks, with memory limitations being a key bottleneck. A classic optimization method, block coordinate descent (BCD), offers solutions by segmenting the trainable parameters into multiple blocks and optimizing one active block at a time while freezing the others, thereby significantly reducing memory cost. However, we identify that blindly applying BCD to train LLMs can be inefficient for two reasons. First, optimizing only the active block requires backpropagating through multiple deeper yet inactive blocks, resulting in wasteful computations. Second, the frozen blocks, when they are not quite close to optimality, can narrow the optimization landscape, potentially misguiding the training of the active block. To address these issues simultaneously, we propose integrating BCD with *landscape correction*, which unfreezes the inactive blocks and updates them in a cost-efficient manner during the same backpropagation as the update to the active block. We show that our method empirically improves vanilla BCD with minimal additional computation and memory. Experiments on 8B and 70B models demonstrate that our proposed method surpasses memory efficient baselines and matches Adam’s downstream performance while reducing memory cost by 80% compared to Adam.

1 INTRODUCTION

Large language models (LLMs) have gained significant popularity within the research community and industry. Training these models typically entails a pretraining phase on a vast dataset, followed by a series of finetuning adjustments to tailor the model for specific domain tasks. Both phases demand extensive computational resources, with memory being a primary constraint. For instance, optimizing a model containing N billion parameters using standard mixed-precision training requires at least $18N$ gigabytes of GPU memory (refer to [Section 2](#) for additional details). Given the usual limitations of GPU memory, this constraint impedes researchers from experimenting with larger models.

To address this practical challenge, researchers have developed memory efficient algorithms for LLM training such as parameter efficient finetuning (PEFT), including Adapter [Houlsby et al. \(2019\)](#), LoRA [Hu et al. \(2021\)](#), prompt tuning [Lester et al. \(2021\)](#), prefix tuning [Li & Liang \(2021\)](#), etc. These techniques focus on training a small set of additional parameters while maintaining the original pretrained model unchanged. Other memory efficient methods for full parameter training have also been investigated. For example, Galore [Zhao et al. \(2024\)](#) applies a low-rank space projection to both the gradient and the optimizer’s states to reduce memory consumption.

In addition to the existing approaches, a classic optimization paradigm, known as *block coordinate descent* (BCD), holds a strong potential for memory efficient LLM training and finetuning. Intuitively, BCD reduces the memory cost by partitioning the trainable parameters into several blocks and optimizing over only one active block at a time. For instance, segmenting an N -billion parameter model into D blocks decreases the memory consumption from $18N$ to $2N + \frac{16N}{D}$ GB, as only the gradient and optimizer states of the active block need to be stored. In fact, BCD has been the method of choice in many data science problems, with a wide array of variants developed for improving the memory, performance, convergence, and efficiency; see, e.g., ([Hsieh et al., 2008](#); [Chang & Roth, 2011](#); [Yu et al., 2012](#); [Treister & Turek, 2014](#); [Richtárik & Takáč, 2014](#)).

In stark contrast to the previous memory efficient methods, and despite its intuitive memory benefits, BCD has been overlooked and rarely explored in the context of LLMs. It was not until very recently that Luo et al. (2024) proposed BAdam, which integrates BCD into an LLM finetuning framework by training each active block with several Adam steps. Even though BAdam has shown preliminary success in reducing memory cost during training and improving performance at test time, its direct use of vanilla BCD leaves at least two fundamental aspects to be questioned:

- Computing the (stochastic) gradient for a *single* active block via backpropagation necessitates calculating the partial derivatives of the activations of *multiple* deeper yet inactive layers. This is wasteful of computation as these partial derivatives are not even used to update their corresponding weights.
- Given that the training objective is highly nonconvex, and since all blocks are frozen except for the active one, BCD tends to be misled by *its local view of the optimization landscape*, which potentially slows down its convergence speed.

Standing on the ground offered by BAdam, we push the research frontier by proposing a simple remedy to the above two issues. Our method, termed BREAD, is a blend of two components: (1) Similarly to BAdam, we update the active block using several Adam steps (the BCD component); (2) differently, we unfreeze the inactive blocks and update them using lightweight memory efficient optimization techniques (the *landscape correction* component). Since landscape correction utilizes the gradients of the activations that are already calculated, it adds minimal additional computation and in fact addresses the wasteful computation issue. Furthermore, the landscape correction component provides BCD a better view of the optimization landscape for better updating the current active block, thereby addressing the second point of concern. In combination, BREAD maintains the memory efficient feature of BCD with improved learning capability and faster convergence. Our main contributions are outlined as follows:

- **Limitations of Standard BCD in LLMs:** Our research identifies two fundamental limits of vanilla BCD when applied to neural networks (hence LLMs): The wasted computation of gradients during backpropagation and the suboptimal landscape caused by freezing inactive blocks. These limitations partly explain why the application of BCD in neural networks is uncommon.
- **Blending BCD with Landscape Correction:** We propose a new method termed BREAD, which combines BCD with a landscape correction technique to address these two limitations simultaneously. It unfreezes some of (or all) the inactive blocks and updates them using memory efficient optimization techniques. BREAD maintains the memory efficiency of BCD with improved optimization ability.
- **Excellent Performance:** Our experiments on instruction tuning and preference optimization with the Llama 3.1-8B and Llama 3.1-70B models demonstrate that BREAD clearly outperforms state-of-the-art memory efficient training methods and achieves comparable downstream performance to that of Adam on five math benchmarks and MT-bench scores.

2 PRELIMINARIES ON BLOCK COORDINATE DESCENT FOR LLM TRAINING

Our main focus lies in improving the efficiency of BCD when utilized to finetune LLMs. Therefore, we first review some preliminary concepts of LLM and BCD in this section.

Objective of training LLMs. Consider minimizing a general objective function $\min_{\mathbf{W}} H(\mathbf{W}) = \frac{1}{n} \sum_{j=1}^n h_j(\mathbf{W})$, where $\mathbf{W} \in \mathbb{R}^d$. In the context of training/finetuning LLMs, $h_j(\mathbf{W})$ represents the negative log-likelihood of the autoregressive probability $\mathbb{P}_{\mathbf{W}}[\mathbf{y}_j|\mathbf{x}_j] = \prod_{s=1}^m \mathbb{P}_{\mathbf{W}}[\mathbf{y}_{j,s}|\mathbf{y}_{j,1:s-1}, \mathbf{x}_j]$ for the j -th prompt \mathbf{x}_j and its corresponding j -th output \mathbf{y}_j . In most LLM models, this autoregressive probability is modeled by a transformer architecture Vaswani et al. (2017), and thus $\mathbf{W} \in \mathbb{R}^d$ encompasses all trainable parameters of the transformer, including the query, key, value, and output attention matrices, as well as the gate, up, and down projection matrices of each transformer layer.

BCD for LLM training. Instead of minimizing the objective function over the entire set of trainable parameters \mathbf{W} , the key idea of BCD is to break down this high dimensional optimization problem into a series of lower dimensional ones, thereby significantly reducing the memory requirement of GPU RAM. Specifically, BCD first splits the model parameters into D block, i.e.,

$\mathbf{W} = \{\mathbf{W}_1, \dots, \mathbf{W}_\ell, \dots, \mathbf{W}_D\}$, where $\mathbf{W}_\ell \in \mathbb{R}^{d_\ell}$ and $\sum_{\ell=1}^D d_\ell = d$. The block partition in such a splitting can be very flexible. For instance, the block variable \mathbf{W}_ℓ can be either a single matrix or all the trainable matrices of a transformer layer. Then, at each block iteration, BCD updates only one active block while fixing the others at their most up-to-date values. This makes each sub-problem of BCD a $D \times$ smaller problem compared to the original one if the D blocks are partitioned evenly. Suppose at the $(t+1)$ -th block iteration the active block is \mathbf{W}_ℓ , BCD solves the following problem:

$$\mathbf{W}_\ell^{t+1} \in \underset{\mathbf{W}_\ell \in \mathbb{R}^{d_\ell}}{\operatorname{argmin}} \frac{1}{|\mathcal{N}|} \sum_{j \in \mathcal{N}} h_j(\mathbf{W}_1^{t+1}, \dots, \mathbf{W}_{\ell-1}^{t+1}, \mathbf{W}_\ell, \mathbf{W}_{\ell+1}^t, \dots, \mathbf{W}_D^t) \quad (1)$$

where $\mathcal{N} \subseteq \{1, \dots, n\}$ is a batch of the training dataset. Updating from block $\ell = 1$ to block $\ell = D$ is counted as one block-epoch. Since it is intractable to solve (1) exactly, one can instead approximate the solution by implementing K Adam steps, as utilized in BAdam Luo et al. (2024).

BCD is a memory efficient full parameter optimization method for LLM training. It is evident to see that BCD is a full parameter optimization method, as all the trainable parameters \mathbf{W} will be updated after one block-epoch. More importantly, BCD is also memory efficient.

Let us first analyze the memory consumption of the Adam optimizer under the mixed precision training setting Micikevicius et al. (2017). The memory cost is attributed to the storage of the model parameters, gradients, and optimizer states. We consider an LLM with N billion parameters and express GPU memory consumption in gigabytes (GB). Initially, one must store the FP16 model parameters for the backpropagation (BP) process, requiring $2N$ memory. Additionally, the optimizer maintains a copy of the model in FP32 precision, consuming another $4N$ memory. The gradients, momentum, and second moment vectors are all stored in FP32 precision with each requiring $4N$ memory. Consequently, the total memory required is at least $18N$. For example, in order to train a Llama 3-8B or a Llama 3-70B model, Adam requires at least 144 GB or 1260 GB of GPU RAM, respectively, which can be prohibitive in limited memory scenarios.

In sharp contrast to Adam, BCD only requires storing the FP32 model parameters, gradients, and optimizer states for the *active block* \mathbf{W}_ℓ , which is only $1/D$ of the memory consumption needed for all the parameters. Thus, in addition to maintaining an FP16 model that requires $2N$ memory, BCD needs a total of only $2N + \frac{16N}{D}$ memory. Therefore, for training a Llama 3-8B or a Llama 3-70B model and when $D = 32$ or $D = 80$ (partition each transformer layer as a block), BCD only needs roughly 20 GB or 154 GB of GPU RAM, respectively, which is significantly cheaper compared to the costs of Adam. For a more detailed analysis on memory cost, we refer to Luo et al. (2024).

3 LIMITATIONS OF BCD FOR NEURAL NETWORKS

Although BCD is proven to be memory efficient for training and finetuning LLMs, we will illustrate two major limitations of the BCD optimization scheme when it is used for training models induced by a neural network structure in this section. The results apply to the setting of training and finetuning LLMs as well. Motivated by these limitations, we will develop a more efficient training method based on BCD, which achieves superior performance compared to that of the vanilla BCD.

To ease our analysis, let us consider a L -layer feedforward neural network model:

$$\mathbf{z}_{\ell+1} = f_{\mathbf{W}_\ell}^\ell(\mathbf{z}_\ell), \forall 1 \leq \ell \leq L, \quad \text{with } \mathbf{z}_1 = \mathbf{x}, \quad (2)$$

where L is the total number of layers, \mathbf{x} is the input, $f_{\mathbf{W}_\ell}^\ell$ is the ℓ -th layer's transform.

Limitation I: Ineffective utilization of intermediate derivatives during backpropagation. Due to the compositional structure of deep neural networks, the gradients of the trainable parameters are calculated according to the chain rule. For example, taking the stochastic gradient of the ℓ -th layer's parameters \mathbf{W}_ℓ requires computing the partial derivatives with respect to all the activation values of deeper layers, as shown in the following equation:

$$\frac{\partial H}{\partial \mathbf{W}_\ell} = \underbrace{\frac{\partial H}{\partial \mathbf{z}_{L+1}} \frac{\partial \mathbf{z}_{L+1}}{\partial \mathbf{z}_\ell} \dots \frac{\partial \mathbf{z}_{\ell+2}}{\partial \mathbf{z}_{\ell+1}} \frac{\partial \mathbf{z}_{\ell+1}}{\partial \mathbf{W}_\ell}}_{I_{\ell+1}}, \quad (3)$$

where H is the objective function of the neural network training problem. During the backpropagation process in optimization method like Adam, the intermediate partial derivatives $I_{\ell+1}$ of the

activations in (3) are properly utilized for computing the gradients of the $L, L-1, \dots, \ell+1$ -th layers' weight parameters as well. However, since BCD only updates the active block \mathbf{W}_ℓ , the term $I_{\ell+1}$ is merely used for calculating the gradient of \mathbf{W}_ℓ , resulting in ineffective utilization of the computed partial derivatives of the activations during backpropagation.

Limitation II: Suboptimal landscape of BCD's sub-problem. To tackle a training problem, optimization methods such as Adam optimize over all the trainable parameters \mathbf{W} , while the BCD optimization scheme (1) minimizes the objective over only the current active block, keeping the others fixed. Intuitively, BCD appears to narrow the optimization landscape of the training problem by freezing most of the parameters in each of its sub-problems, potentially eliminating better search directions that can lead to rapid decrease of the objective function. To establish such an intuition formally, we consider the following simple regression problem modeled by a 3-layer neural network:

$$\min_{\mathbf{W}_1, \mathbf{W}_2, \mathbf{W}_3} H(\mathbf{W}_1, \mathbf{W}_2, \mathbf{W}_3) := \|\mathbf{y} - \hat{\mathbf{y}}\|_2^2,$$

$$\text{where } \mathbf{z}_2 = \sigma(\mathbf{W}_1 \mathbf{x}), \quad \mathbf{z}_3 = \sigma(\mathbf{W}_2 \mathbf{z}_2), \quad \hat{\mathbf{y}} = \mathbf{W}_3 \mathbf{z}_3.$$

Here, \mathbf{x} is a non-zero input vector, \mathbf{y} is the true label vector, and $\sigma(z) := \max(0, z)$ is the ReLU activation function. The following proposition states that the optimization landscape of one of the BCD sub-problems is suboptimal in terms of minimizing H .

Proposition 1. (*suboptimal landscape of BCD's sub-problem*) Define

$$\tilde{H}^* := \min_{\mathbf{W}_2} \|\mathbf{y} - \hat{\mathbf{y}}\|_2^2, \quad \text{and} \quad H^* := \min_{\mathbf{W}_1, \mathbf{W}_2, \mathbf{W}_3} \|\mathbf{y} - \hat{\mathbf{y}}\|_2^2.$$

Here, \tilde{H}^* is the optimal value returned BCD that minimizes H only over block \mathbf{W}_2 , while fixing \mathbf{W}_1 and \mathbf{W}_3 . Suppose that the fixed \mathbf{W}_3 in BCD has full column rank. If $\mathbf{z}_3^* := (\mathbf{W}_3^\top \mathbf{W}_3)^{-1} \mathbf{W}_3^\top \mathbf{y}$ has at least one negative entry, then $\tilde{H}^* > H^*$.

Proof. We first show that $H^* = 0$. One can construct $\mathbf{z}_3 = [1, 0, \dots, 0]^\top$ and $\mathbf{W}_3 = [\mathbf{y}, 0, \dots, 0]$. Note that such a choice of \mathbf{z}_3 is always achievable by choosing a specific \mathbf{W}_2 . Hence, 0 function value can be attained by the constructed feasible point. This yields $H^* = 0$ after realizing that the objective function must be nonnegative. We illustrate this issue in Figure 1.

In BCD, \mathbf{W}_1 and \mathbf{W}_3 are fixed. We further assume that the fixed \mathbf{W}_3 has full column rank. We split our discussion into two cases. Case I: $\mathbf{y} \notin \text{range}(\mathbf{W}_3)$. We trivially have $\tilde{H}^* > 0 = H^*$. Case II: $\mathbf{y} \in \text{range}(\mathbf{W}_3)$. In this case, $\mathbf{z}_3^* := (\mathbf{W}_3^\top \mathbf{W}_3)^{-1} \mathbf{W}_3^\top \mathbf{y}$ is the unique point that can achieve 0 function value. However, since \mathbf{z}_3^* has at least one negative entry and $\mathbf{z}_3 \geq 0$ (due to the ReLU activation), we have $\|\mathbf{z}_3 - \mathbf{z}_3^*\|_2^2 > 0$. Therefore, we have $\|\mathbf{y} - \hat{\mathbf{y}}\|_2^2 = \|\mathbf{W}_3(\mathbf{z}_3^* - \mathbf{z}_3)\|_2^2 > 0 = H^*$, where the last inequality follows from the full column rankness of \mathbf{W}_3 . \square

We note that the full column rank assumption of the fixed weight matrix \mathbf{W}_3 is mild. In LLMs, the last matrix is typically the tall LM head matrix, which has more rows (vocabulary size) than columns (embedding dimension). In practice, a tall matrix often has full column rank. In addition, it is always feasible to have negative entries in \mathbf{z}_3^* by choosing a specific \mathbf{y} . Moreover, although we prove such a result for a simple regression problem modeled by a 3-layer neural network, we expect it to occur more frequently in the training and finetuning of LLMs, as the training objective of LLMs is much more complicated. Finally, the result in Proposition 1 can be readily generalized to any nonnegative or bounded activation functions, e.g., Swish Ramachandran et al. (2017), SiLU Elfving et al. (2018), Sigmoid, etc.

When fixing \mathbf{W}_1 and \mathbf{W}_3 and training only on the intermediate layer's weight matrix \mathbf{W}_2 , the sub-problem of BCD represents a partial landscape of the full optimization problem. Proposition 1 demonstrates that this sub-problem might be incapable of finding the optimal value 0. It is important to note that this result does not necessarily imply that BCD cannot find an optimal solution. Recall that BCD is a full parameter optimization method, and the weight matrices \mathbf{W}_1 and \mathbf{W}_3 will indeed be updated in subsequent block iterations. Therefore, BCD may eventually converge to an optimal solution that achieves 0 function value. However, our analysis reveals that the sub-problem of BCD potentially excludes parts of the optimization landscape that provide search directions toward the optimal solution. This observation suggests that BCD might slow down the convergence speed compared to Adam.

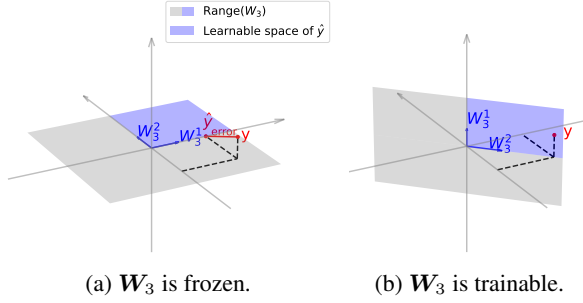


Figure 1: When W_3 is frozen, the projection error $\|y - \hat{y}\|_2^2 > 0$. When W_3 is trainable, the learnable space rotates to cover the label y , the error $\|y - \hat{y}\|_2^2 = 0$.

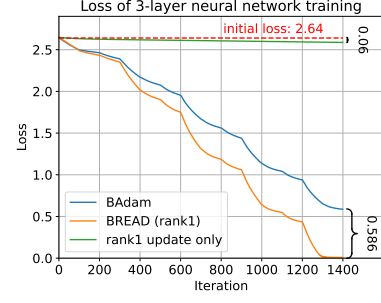


Figure 2: With rank-1 landscape correction, BREAD converges significantly faster than BCD.

Based on these limitations of BCD, we will design a new method to correct the landscape of BCD’s sub-problem and simultaneously utilize the computed partial derivatives of the activations properly.

4 ACCELERATING BCD VIA LANDSCAPE CORRECTION

In this section, we propose a new BCD method with landscape correction to solve the two limitations revealed in Section 3 simultaneously.

4.1 THE BREAD METHOD

In Section 3, our analysis indicates that the incomplete landscape of BCD’s sub-problem may slow down the convergence speed. To address this issue, one immediate approach is to apply Adam to other blocks as well. However, this essentially reverts to using Adam and undermines the memory efficient property of the BCD optimization scheme. Fortunately, for the regression problem we analyzed in Section 3, we show in the following proposition that a *low-rank landscape correction* is sufficient to compensate for the incompleteness of BCD’s sub-problem.

Proposition 2 (rank-1 landscape correction). *For any non-zero input x and label y , there exists a rank-1 matrix C such that the following problem has optimal value 0:*

$$\min_{W_2, C} \|y - \hat{y}\|_2^2,$$

$$\text{where } z_2 = \sigma(W_1 z), \quad z_3 = \sigma(W_2 z_2), \quad \hat{y} = (W_3 + C)z_3.$$

Proof. We construct $z_3 = e_1 = [1, 0, \dots, 0]^\top$. Let $C = [W_3^{(1)} - y, 0, \dots, 0]$, where $W_3^{(1)}$ is the first column of W_3 . Then, we have $\|(W_3 + C)z_3 - y\|_2^2 = \|Ce_1 - (y - W_3 e_1)\|_2^2 = 0$. \square

Motivated by this proposition, we propose to introduce additional *trainable low-rank correction matrices* to the matrices in the frozen inactive blocks $\{W_{\ell'}\}_{\ell' \neq \ell}$, where W_ℓ is the current active block. For simplicity, let us assume that each block is a matrix, and our design applies to general layer-wise or other types of partitions as well. Mathematically, all the inactive block matrices are corrected by low-rank correction matrices as follows:

$$W_{\ell'} + C_{\ell'} \text{ with } \text{rank}(C_{\ell'}) \leq r, \quad \forall \ell' \neq \ell. \quad (4)$$

To maintain memory efficiency, we use the Burer-Monteiro factorization representation of a low-rank matrix [Burer & Monteiro \(2003\)](#):

$$C = UV, \quad U \in \mathbb{R}^{m \times r}, \quad V \in \mathbb{R}^{r \times n}. \quad (5)$$

Following the spirit of adapter [Houlsby et al. \(2019\)](#) and LoRA [Hu et al. \(2021\)](#), we update only the low dimensional matrices U and V rather than C . This approach allows us to store the gradients

Algorithm 1: BREAD: Block coordinate descent with landscape correction.

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1 input: model parameters  $\{W_\ell^0\}_{\ell=1}^L$ , number of blocks  $D$ , iterations per block  $K$ .
2 initialization: block-epoch index  $t \leftarrow 0$ , the correction matrices  $C_j^0 \leftarrow \mathbf{0}$ ,  $\forall j \in [P]$ , and
   the corresponding optimizer states  $\tilde{s}_j^0 \leftarrow \mathbf{0}$ ,  $\forall j \in [P]$ .
3 while stopping criterion not meet do
4   generate a block partition  $\pi = \{\pi_1, \dots, \pi_D\}$ ;
5   repeat for one block-epoch  $i \leftarrow 1, \dots, D$ 
6     select correction matrices' indices  $J \subset [P]$  as in (7);
7      $s_{\pi_i}^{t,0} \leftarrow \mathbf{0}$ ; // initialize Adam optimizer states
8      $W_{\pi_i}^{t,0} \leftarrow W_{\pi_i}^t$ ;  $\tilde{s}_J^{t,0} \leftarrow \tilde{s}_J^t$ ;
9     repeat for landscape corrected block updates  $k \leftarrow 1, \dots, K$ 
10      within one backward pass
11        calculate the active block's grad.  $g_i^{t,k}$  and correction matrices' grad.  $\tilde{g}_J^{t,k}$ ;
12         $W_{\pi_i}^{t,k}, s_{\pi_i}^{t,k} \leftarrow \text{AdamStep}(g_{\pi_i}^{t,k}, s_{\pi_i}^{t,k-1})$ ; // Update the active block
13         $C_J^{t,k}, \tilde{s}_J^{t,k} \leftarrow \text{AdamStep}(\tilde{g}_J^{t,k}, \tilde{s}_J^{t,k-1})$ ; // Update correction matrices
14      end
15       $W_{\pi_i}^{t+1} \leftarrow W_{\pi_i}^{t,K}$ ;  $C_J^{t+1} \leftarrow C_J^{t,K}$ ;  $\tilde{s}_J^{t+1} \leftarrow \tilde{s}_J^{t,K}$ ;  $s_{\pi_i}^{t,K} \leftarrow \text{None}$ ;
16    end
17     $t \leftarrow t + 1$ ;
18 end
19 return parameters  $\{W_\ell^t\}_{\ell=1}^L$  and correction matrices  $\{C_j^t\}_{j=1}^P$ .

```

and optimizer states for these much smaller sized matrices, resulting in only negligible additional memory consumption.

We note that one can add the landscape correction matrix C to each matrix in the inactive blocks or to part of the matrices, leading to two different variants. Additionally, we may also add a *full-rank* correction matrix C , training it using an on-the-fly SGD method [Lv et al. \(2023\)](#) to maintain the memory efficient feature of BCD. We refer to [Section 4.3](#) for details.

Algorithm design. Based on the above developments, we propose accelerating block coordinate descent via landscape correction (BREAD). We present the detailed procedure in [Algorithm 1](#). Suppose we can add a total of P landscape correction matrices. BREAD first splits the model into D blocks, which can be partitioned either in a layer-wise or matrix-wise manner. Then, each block sub-problem is approximately solved using K steps of landscape corrected updates. In each update, the active block and the correction matrices are updated during the same backward pass. It is important to note that the optimizer states of the correction matrices are accumulated throughout the entire algorithm execution, as they occupy only negligible memory space.

We now carry out a simple experiment to validate the effectiveness of [Algorithm 1](#) and the insights in [Proposition 1](#) and [Proposition 2](#). Specifically, we train a 3-layer neural network with rank-1 BREAD, with 100 Adam optimization steps for each sub-problem. As shown in [Figure 2](#), rank-1 BREAD (orange) achieves significantly faster convergence than BCD (blue). The loss difference between rank-1 BREAD and BCD is eventually 0.586. This is a significant amount, as the loss is only decreased by 0.06, if we train C only (rank-1 update only, green). These empirical observations verify that the proposed combination of BCD and landscape correction accelerates the convergence of the individual scheme (BCD alone or landscape correction alone).

4.2 MEMORY AND COMPUTATIONAL EFFICIENCY OF BREAD

In this section, we analyze the memory and computational cost of BREAD, showing that BREAD only introduces marginal additional costs compared to vanilla BCD.

Memory cost analysis. To simplify the analysis, we consider a D -layer neural network where each layer consists of one matrix with dimensions $\mathbb{R}^{m \times m}$. The rank r correction matrix introduces additional $2Dmr$ parameters. Since the correction matrix is primarily used for coarse-grained landscape

correction, the rank is set to be small, e.g., $r \in [1, 8]$, making the additional memory required almost negligible. In the scenario where $r = 4$, $D = 32$, and $m = 4096$, BREAD only increases memory cost by $\frac{Dr(m+n)}{mn} = 1.6\%$ compared to BCD.

Computational cost analysis. We now show that the additional backward cost is also cheap, since the intermediate partial derivatives used for computing the active block’s gradient can be directly used for computing correction matrices’ gradients, as we have identified in (3). Specifically, the gradient of the correction matrix C_j can be expressed as

$$\frac{\partial H}{\partial C_j} = \frac{\partial H}{\partial z_{L+1}} \underbrace{\frac{\partial z_{L+1}}{\partial z_L} \dots \frac{\partial z_{j+2}}{\partial z_{j+1}}}_{\text{Computed in (3), } \forall j \geq \ell} \frac{\partial z_{j+1}}{\partial C_j}. \quad (6)$$

Clearly, when $j \geq \ell$, computing the (stochastic) gradient of C_j only requires additional computation of $\frac{\partial(z_{j+1})}{\partial C_j}$, which is cheap given the low dimensionality after low-rank factorization representation, i.e., $C_j = U_j V_j$. We empirically measure the memory and epoch training time in Table 1.

4.3 PRACTICAL VARIANTS OF BREAD

A computational efficient variant. For simplicity, we consider an L -layer neural network where each layer consists of one weight matrix, and our block partition is layer-wise. Based on the derivation of (6), evaluating the gradients of the correction matrices is inexpensive for layers $\ell + 1, \dots, L$. However, the gradient evaluation for layers $1, \dots, \ell - 1$ is more costly, as it requires calculating $\frac{\partial z_{j+1}}{\partial z_j}$ for $j = 1, \dots, \ell - 1$. These intermediate partial derivatives of the activations are not computed during the backpropagation to the active layer ℓ . Therefore, one computationally efficient variant of BREAD is to add correction matrices only for layers $\ell + 1, \dots, L$. This leads to two strategies of selecting correction matrices:

$$J = \begin{cases} [P], & \text{if use BREAD} \\ \{j | j \in [P], C_j \text{ corrects layers } \ell + 1, \dots, L\}, & \text{if use BREAD-partial} \end{cases} \quad (7)$$

A full-rank memory efficient variant. The previous implementation uses low-rank matrices for landscape correction. Alternatively, one can apply a potentially full-rank linear transformation to correct features. This can be achieved through a memory efficient on-the-fly SGD update on the inactive blocks. Specifically, due to the compositional structure of neural networks, the gradient of the model is computed from the deep layers to the shallow layers. The strategy is to perform an SGD update on a matrix whenever its (stochastic) gradient is available, and then immediately discard the corresponding gradient after the update. We term this approach BREAD-SGD. It introduces additional memory cost for storing the gradient of the largest matrix, but this overhead is usually negligible. We formally present this variant in Algorithm 2.

We compare the performance of these two variants of BREAD in Section 5.4.

5 NUMERICAL EXPERIMENTS

We evaluate the proposed BREAD in finetuning Llama 3.1-8B and Llama 3.1-70B model on math finetuning and instruction tuning tasks, comparing its memory cost, time cost and downstream performance with full training algorithm and memory efficient baselines.

5.1 SETUP

We begin by introducing the experimental setup.

Baselines. We compare BREAD with **1) BAdam** Luo et al. (2024), which applies vanilla BCD algorithm with Adam as the inner solver; **2) LoRA** Hu et al. (2021), which freezes the pre-trained weight and only updates the injected low-rank adapters; **3) Galore** Zhao et al. (2024), which projects the gradient into low-rank spaces for reducing the memory cost; **4) Adam** Kingma (2014), which serves as the full parameter training baseline.

Model	Method	Peak Memory Cost	Epoch GPU Hour	GPU #
Llama 3.1-70B	Adam (estimated)	1260 GB+	–	16+ A100-80G
	LoRA	296.8 GB	213.1	8 A100-40G
	BAdam	276.2 GB	119.0	8 A100-40G
	BREAD	288.6 GB	212.4	8 A100-40G
	BREAD-partial	288.6 GB	152.7	8 A100-40G
Llama 3.1-8B	Adam	208.2 GB	37.3	8 A100-40G
	Galore	40.5 GB	10.3	1 A100-80G
	LoRA	25.0 GB	6.0	1 A100-40G
	BAdam	21.8 GB	3.3	1 A100-40G
	BREAD	23.2 GB	5.8	1 A100-40G
	BREAD-partial	23.2 GB	4.0	1 A100-40G

Table 1: Memory footprint and time cost for finetuning models on MathInstruct.

Math finetuning. We finetune the Llama 3.1-70B and Llama 3.1-8B models on MathInstruct dataset [Yue et al. \(2023\)](#) for 3 epochs, which contains 260K questions that covers wide range of fields in mathematics. The finetuned models are evaluated on 4 in-domain mathematical benchmarks, i.e., GSM8K, MATH, NumGLUE, and AQUA [Cobbe et al. \(2021\)](#); [Hendrycks et al. \(2021\)](#); [Mishra et al. \(2022\)](#); [Ling et al. \(2017\)](#), and 1 out-of-domain mathematical benchmarks, i.e., SimulEq [Koncel-Kedziorski et al. \(2016\)](#). The evaluations are based on 0-shot prompt and 4-shot chain-of-thought prompt, respectively. Due to the limited computational resource, we do not include the Adam’s results for 70B model. Since there is no model parallel implementation released for Galore by the finish of the manuscript, we are unable to report its 70B results as well.

Instruction tuning. We perform supervise finetuning on the Llama 3.1-8B model using Alpaca-GPT4 dataset [Peng et al. \(2023\)](#), which contains 52K questions and corresponding GPT-4 generated answers. The model is evaluated on MT-bench [Zheng et al. \(2023\)](#) for examining the model’s instruction-following capability.

Preference optimization. After the instruction tuning, we further align the tuned model using direct preference optimization (DPO) [Rafailov et al. \(2024\)](#) on Ultrafeedback dataset [Cui et al. \(2023\)](#). To compare with the baseline optimization methods more comprehensively, we report the evaluation results of using Adam instruction tuned model as base model, and using the same optimization method for both phases.

All the experiments are run for 3 epochs. The reported scores are the best one among checkpoints at epoch 1, 2, 3. The detailed hyper-parameters are presented in Appendix A.

5.2 MEMORY AND TIME COST MEASURE

In Table 1, we empirically measure the peak memory cost and one epoch’s time cost of finetuning models on the MathInstruct dataset. The GPU hour is calculated as the training time \times GPU number.

We set the LoRA rank to 64 to keep its number of trainable parameters (0.83 billion) close to a single block of BREAD (0.86 billion). Compared with LoRA, the proposed BREAD consumes slightly lower memory and costs about the same amount of training time. The computational-efficient variant BREAD-partial requires significantly less training time than BREAD, and is comparable to BAdam.

Remark. The reported memory cost is higher than the theoretical value, especially for the 70B model’s experiments which requires distributed training. This additional memory cost arises from storing activation values and computational buffers, e.g. the gradient buffer for performing reduce scatter operation. Furthermore, the training time may have slight fluctuations for different runs.

5.3 FINETUNING PERFORMANCE

Math finetuning. The evaluation results on math benchmarks are shown in Table 2. For the 8B model’s finetuning, BREAD beats all the other 3 memory efficient baselines in both 0-shot and 4-shot average score. Under the 0-shot setting, BREAD even outperforms Adam baseline by 1.1. For

Base model: Llama 3.1-8B												
Method	GSM8K		MATH		NumGLUE		SimulEq		AQuA		Avg.	
	0-shot	4-shot	0-shot	4-shot	0-shot	4-shot	0-shot	4-shot	0-shot	4-shot	0-shot	4-shot
Base model	17.8	52.5	8.6	23.2	25.7	40.6	12.2	28.8	19.3	43.7	16.7	37.8
Adam	62.3	64.9	17.4	22.9	56.4	56.8	28.6	33.5	44.9	52.8	41.9	46.2
Galore	46.7	57.2	16.2	22.9	42.8	45.0	28.7	32.3	47.8	48.4	36.4	41.2
LoRA	48.7	58.1	13.7	23.0	34.6	54.4	29.6	29.0	47.3	50.3	34.8	43.0
BAdam	53.9	58.3	17.2	23.6	53.7	57.2	32.5	32.8	50.4	49.6	41.5	44.3
BREAD	57.0	57.6	20.0	23.7	55.9	58.2	32.5	32.8	49.6	50.0	43.0	44.5

Base model: Llama 3.1-70B												
Method	GSM8K		MATH		NumGLUE		SimulEq		AQuA		Avg.	
	0-shot	4-shot	0-shot	4-shot	0-shot	4-shot	0-shot	4-shot	0-shot	4-shot	0-shot	4-shot
Base model	58.8	79.4	24.9	41.4	43.7	55.8	26.3	38.1	52.0	64.2	41.1	51.2
LoRA	83.8	82.0	41.7	44.2	70.4	69.0	40.3	48.8	61.4	65.8	59.5	62.0
BAdam	81.4	82.9	40.3	43.8	68.1	69.7	50.0	52.7	65.3	70.1	61.0	63.8
BREAD	83.4	84.2	41.4	44.7	73.1	74.4	51.3	56.8	68.3	70.5	63.5	66.1

Table 2: Math evaluation results for models finetuned on MathInstruct dataset.

Method	SFT		DPO	
	GPT-4	GPT-4o	GPT-4	GPT-4o
Base model	6.07	5.18	6.63	5.66
Adam	6.63	5.66	7.83	6.18
LoRA	6.52	5.60	7.48	5.95
Galore	6.33	5.48	6.99	5.93
BAdam	6.53	5.57	7.63	6.14
BREAD	6.77	5.82	7.68	6.31

Table 3: MT-bench scores of different methods finetuning Llama 3.1-8B.

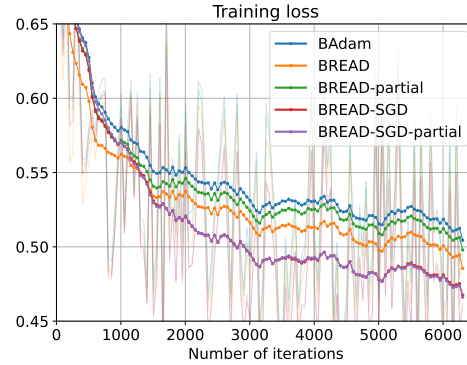


Figure 3: Convergence of BAdam, BREAD and its variants on MathInstruct dataset.

the finetuning of 70B model, BREAD outperforms BAdam in all tasks, demonstrating the effectiveness of landscape correction. Furthermore, BREAD beats LoRA in 8 out of 10 tasks.

Instruction tuning and DPO. We report the MT-bench score evaluated by both GPT-4 and GPT-4o models in Figure 3. After SFT, the MT-bench score of all baseline approaches improves over the base model. BREAD achieves the highest scores in both evaluations, which are even higher than Adam, demonstrating the effectiveness of landscape correction. Based on the model finetuned by Adam, we further align the model using direct preference optimization (DPO). Notably, BREAD achieves the highest evaluation score by GPT-4o model.

5.4 ABLATION ON VARIANTS OF BREAD

We present 1-epoch training loss of BREAD and its variants in Figure 3. For reference, we also display the loss of BAdam. Here, BREAD-partial means that only deeper layers of the active block will be updated; the BREAD-SGD applies on-the-fly SGD update for all the frozen blocks; the BREAD-SGD-partial incorporates the feature of both, updating frozen blocks that are deeper than the active block using SGD. Since SGD usually requires higher learning rate, we scale the SGD’s learning rate up by 10 times compared to the update of the active block. As we can see, all the variants converge faster than the baseline method BAdam, justifying the effectiveness of landscape correction. The BREAD outperforms BREAD-partial since it optimizes all the correction matrices in each iteration. The BREAD-SGD variants converge faster than the low-rank ones, which may

attribute to the higher learning rate of the correction matrices and the high-rank update. Notably, the BREAD-SGD-partial exhibits similar convergence as BREAD-SGD.

6 RELATED WORKS

Block coordinate descent method. Block coordinate descent (BCD) is a classic optimization paradigm that dates back at least to [Hildreth \(1957\)](#). It has gained popularity in recent years, due to its scalability and efficiency for many machine learning applications ([Nesterov, 2012](#); [Richtárik & Takáč, 2014](#); [Peng & Vidal, 2023](#); [Ding et al.](#); [Peng & Yin, 2024](#)). The community seems to converge to a consensus that, in order for BCD to be efficient, the problem it optimizes needs to possess the so-called coordinate-friendly structure ([Shi et al., 2016](#)). Nevertheless, deep networks are of a compositional nature and not coordinate-friendly, which is perhaps why recent surveys or books have never mentioned training deep networks as an application of BCD ([Wright, 2015](#); [Shi et al., 2016](#); [Beck, 2017](#); [Wright & Recht, 2022](#); [Sayed, 2022](#)). Recently, BAdam [Luo et al. \(2024\)](#) was proposed to finetune LLMs based on the BCD framework, where each block sub-problem is approximately solved using several Adam steps. Although BAdam achieved preliminary success, it is based on the vanilla BCD framework and shares the fundamental limitations we revealed in this work. In light of these, we believe identifying the limitations of BCD for LLM finetuning and fixing them entail certain insights, and this is what makes our contributions non-trivial and valuable.

Memory efficient finetuning. To address memory issue, multiple variants have been proposed. Parameter efficient finetuning (PEFT) methods achieve memory efficiency by only training small portion of (possibly extra) parameters while freezing most of the others, such as Adapter tuning [Houlsby et al. \(2019\)](#), prompt tuning, and prefix tuning [Lester et al. \(2021\)](#); [Li & Liang \(2021\)](#). Low-rank adaptation (LoRA) is perhaps the most popular technique that approximates model updates using two smaller, trainable low-rank matrices [Hu et al. \(2021\)](#). LoRA’ variants have been proposed to address its rank constraints and further reducing the memory cost [Lialin et al. \(2024\)](#); [Xia et al. \(2024\)](#); [Dettmers et al. \(2023\)](#). Galore [Zhao et al. \(2024\)](#) projects the gradient into low-rank space so that it does not need to store the full gradient and optimizer states in the memory. LOMO updates parameters in real time during the backpropagation process [Lv et al. \(2023\)](#), so that one can perform SGD without store stochastic gradients. MeZO offers an alternative by approximating SGD using only forward passes [Malladi et al. \(2023\)](#), drawing from zeroth-order optimization that estimates stochastic gradients through the difference in function values. While this paper addresses the same application as these methods, they remain orthogonal to the proposed approaches. They can function as lightweight updates in the frozen layers for landscape correction.

7 CONCLUSIONS AND DISCUSSIONS

This paper investigates the application of a classic optimization method, known as BCD, to the finetuning of LLMs. We pinpoint two primary shortcomings of the standard BCD approach when applied to deep neural networks: the unnecessary computational overhead during backpropagation, and the misleading optimization landscape caused by frozen blocks. To overcome these challenges, we introduce a new method termed BREAD, which unfreezes the inactive blocks and updates them in a lightweight manner. Our experimental results demonstrate that BREAD significantly enhances downstream task performance while maintaining the original BCD algorithm’s memory and computational efficiency.

For future research, it would be intriguing to explore the potential of BCD in the (continual) pre-training phase of LLMs. Moreover, it is not necessary to apply feature correction at each iteration. Exploring the frequency of updating correction matrices is another meaningful direction, which can further save the computational cost. Additionally, since the downstream task performance of LLMs is not strictly determined by the training loss, it would be interesting to reveal deeper insights on why BREAD is better than vanilla BCD from a scientific perspective that is beyond this work’s optimization interpretation.

Ethics Statement: We did not use any non-public data, unauthorized software, or API in our paper, there are no privacy or other related ethical concerns.

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A DETAILED EXPERIMENTAL SETUP

We introduce the detailed hyperparameters and experimental setup in this section.

Global setup. For all the experiments in math finetuning, instruction tuning and direct preference optimization, we fix the effective batch size to be 16 and train the model for 3 epochs. We use DeepSpeed ZeRO-3 to implement all the experiments that require distributed training (shown in Table 1). For all the experiments, we apply gradient checkpointing to reduce the memory cost for storing activation values. We use mixed-precision training with BFloat 16 as the low-precision datatype except for Galore, where we follow the setup in its paper, using pure BFloat 16 and 8-bit Adam optimizer for reducing the memory cost. Since the downstream tasks’ performance of the language model have high variability, we grid search learning rate from $\{1e-6, 1e-5, 5e-5\}$ with cosine learning rate schedule, and report the best result among the checkpoints at the end of epoch 1, 2, 3. The grid search is not extensive due to our limited computation resources. The implementation of BAdam, Galore, LoRA are based on LLama-Factory [Zheng et al. \(2024\)](#).

Math finetuning. The benchmarks scores are evaluated using the MAMmoTH’s repository¹ (without using program-of-thought). The rank of correction matrices for BREAD and BREAD-partial is set to 8. The rank of LoRA is set to 80 and 64 for finetuning Llama 3.1-8B and Llama 3.1-70B, respectively, so that the trainable parameter number of LoRA is close to that of one BAdam/BREAD’s active block. We set Galore’s rank to be 256, with the period of re-calculating the projection matrix being 256. We set $K = 100$ for BAdam and BREAD.

Instruction tuning and direct preference optimization. The evaluation of MT-bench score is based on FastChat [Zheng et al. \(2023\)](#) using both GPT-4 and GPT-4o API. The maximum sequence is set to 1024 and 2048 for the experiments on Alpaca-GPT4 and UltraFeedback, respectively. For BAdam and BREAD, we solve each block sub-problem for 128 steps, i.e. $K = 128$.

¹<https://github.com/TIGER-AI-Lab/MAMmoTH>

B BREAD-SGD ALGORITHM

We introduce a variant of the BREAD algorithm, termed BREAD-SGD, which employs Adam for updating the active block and on-the-fly SGD for the inactive block. The detailed procedure is outlined in [Algorithm 2](#). Analogous to BREAD, BREAD-SGD partitions the model into D distinct blocks and combines the gradient computation and update steps into a singular operation. Specifically, gradients are calculated on a layer-by-layer basis; active layers are updated using Adam, while inactive layers undergo a single SGD step. Once an inactive layer is updated, its gradient is discarded to enhance memory efficiency.

Algorithm 2: BREAD-SGD

```

1 input: model parameters  $\{W_\ell^0\}_{\ell=1}^L$ , number of blocks  $D$ , iterations per block  $K$ .
2 initialization: block-epoch index  $t \leftarrow 0$ , and the corresponding optimizer states
    $\tilde{s}_j^0 \leftarrow 0, \forall j \in [P]$ .
3 while stopping criterion not meet do
4   generate a block partition  $\pi = \{\pi_1, \dots, \pi_D\}$ ;
5   repeat for one block-epoch  $i \leftarrow 1, \dots, D$ 
6     select correction matrices' indices  $J \subset [P]$ ;
7      $s_{\pi_i}^{t,0} \leftarrow 0$ ; // initialize Adam optimizer states
8      $W_{\pi_i}^{t,0} \leftarrow W_{\pi_i}^t$ ;
9     repeat for landscape corrected block updates  $k \leftarrow 1, \dots, K$ 
10       $g_L^{t,k} = \frac{\partial H}{\partial z_{L+1}}$ 
11      repeat for layers  $\ell \leftarrow L, \dots, 1$ 
12        if  $\ell == \pi_i$  then
13           $W_{\pi_i}^{t,k}, s_{\pi_i}^{t,k} \leftarrow \text{AdamStep}(g_{\pi_i}^{t,k}, s_{\pi_i}^{t,k-1})$ ; // Update active blocks
14        end
15        else
16           $G_\ell^{t,k} \leftarrow g_\ell^{t,k} \cdot \frac{\partial z_{\ell+1}}{\partial W_\ell}$ ;
17           $W_\ell^{t,k} \leftarrow W_\ell^{t,k-1} - \eta G_\ell^{t,k}$ ; // Update the inactive block
18           $G_\ell^{t,k} \leftarrow \text{None}$ ; // Clear gradients to save memory
19        end
20       $g_{\ell-1}^{t,k} = g_\ell^{t,k} \cdot \frac{\partial z_{\ell+1}}{\partial z_\ell}$ ;
21    end
22  end
23   $W_{\pi_i}^{t+1} \leftarrow W_{\pi_i}^{t,K}; s_{\pi_i}^{t,K} \leftarrow \text{None};$ 
24 end
25  $t \leftarrow t + 1$ ;
26 end
27 return parameters  $\{W_\ell^t\}_{\ell=1}^L$ .

```
