# Reservoir Computing for Edge-based Automatic Speech Recognition

Nicolo Micheletti - 2024280039 Diego Cerretti - 2024280040 Thomas Adler - 2024389002 29 October 2024

#### 1 1 Introduction

2 Automatic Speech Recognition (ASR) ensures seamless interaction between humans and LLMpowered AI. Current state-of-the-art ASR models are transformer-based neural networks [1] that 3 have a very high level of accuracy but come with the cost of high complexity, partly due to the 4 attention mechanisms present in the model [1]. The latest ASR model by Open AI, Whisper Large, 5 has 1.55bn parameters ( $\tilde{2}.9$  GB) [11] and is reported by users <sup>1</sup> to require around 12 GB of VRAM to 6 run. A model of this size has to be deployed on the cloud, which introduces network latency, slowing 7 response times and degrading user experience. Indeed, edge devices cannot host a model of this size: 8 the latest iPhone 15 Pro Max is estimated to have around 8 GB of RAM. Due to limited resources, 9 current edge-based ASR models also struggle with accuracy [6]. Reservoir Computing offers the 10 potential for a new generation of edge-based ASR models with low latency and high accuracy. 11

#### 12 2 Background

Reservoir Computing (RC) [15, 9] is an ideal candidate for an edge-based ASR model because 13 of its low training requirement and strong predictive capabilities in complex time series. The key 14 property of an RC is that its internal layer is fixed, requiring no training. As a result, RCs require 15 very little memory, computational power, fine-tuning or retraining. They can potentially be hosted on 16 memory-constrained edge devices and avoid the network latency issues introduced by cloud-based 17 18 models. RCs can achieve high-performance levels on specific tasks because of the rich non-linear and recurrent dynamics within the reservoirs. This enables them to extract complex spatio-temporal 19 features when transforming input data into a higher-dimensional space. We further describe the RC 20 architecture in Appendix A. 21

<sup>22</sup> The following system of equations describes an RC.

$$\begin{cases} x(t+1) = (1-\gamma)x(t) + \gamma f(Wx(t) + W^{(in)}u(t) + b), \\ y(t) = W^{(out)}x(t), \end{cases}$$
(1)

It operates with discrete time steps, denoted by t. The non-linear activation function is represented by f, and the reservoir is the internal weight matrix W.  $W^{in}$  is the input weight matrix, defining the nodes in the network that receive inputs (input-to-reservoir mapping), while  $W^{out}$  represents the output weight matrix (reservoir-to-output mapping). The leakage rate  $\gamma$  controls the amount of past information passed to the next time step. The input at time t is denoted by u(t), and b is a bias term [15, 8].

<sup>&</sup>lt;sup>1</sup>https://github.com/openai/whisper

## 29 **3 Related Work**

**Optimization strategies** State-of-the-art ASR models achieve high accuracy but require complex 30 architectures. Several techniques can be used to alleviate this issue. Pruning and quantization 31 techniques can be combined to compress a model. Pruning cuts links in a dense network to reduce 32 complexity, whereas quantization lowers the precision of parameters to save memory [7, 4]. Other 33 methods have been implemented to improve convergence rates and training efficiency of ASR 34 models, like Sortagrad and automatic segmentation. SortaGrad consists of initially training neural 35 networks on shorter audio clips, and gradually increasing their length [2]. Automatic segmentation of 36 37 input audio into meaningful units enhances the training of automatic speech recognition systems by 38 improving how input features align with phonetic labels and simplifies the data the model needs to handle [5]. 39

**ASR for Edge Devices** Recent works have aimed to reduce the latency and memory footprint of 40 ASR models to run on resource-constrained devices. Specifically, Gondi et al. [6] achieved this by 41 building transformer-based models with quantization and other optimization techniques. Xu et al. 42 [14] ran ASR on low-memory devices using Conformer CTC (Connectionist Temporal Classification 43 44 Automatic Speech Recognition) models. Conformer CTC ASR is a speech recognition model that combines Conformer neural architecture, known for its efficiency in capturing both local and global 45 speech patterns, with CTC loss, which aligns predictions with input sequences without requiring 46 pre-labeled data alignment. Compared to traditional models, these systems exhibit an increase in 47 Word Error Rate (WER), a common accuracy metric in speech recognition tasks. 48

49 Reservoir Computing for ASR Recent works have attempted to reduce complexity while main-50 taining high accuracy in ASR using RC models. Picco et al. [10] implemented a photonic-based 51 system that makes RC architectures suitable for high-dimensional audio processing tasks. Ansari et 52 al. [3] used heterogeneous single and multi-layer RC models to create non-linear transformations of 53 the inputs, capturing temporal context at different scales.

## 54 4 Proposal

Lighter models operating at the edge face challenges in balancing accuracy, latency, and efficiency. RC

provides a promising alternative due to its lower inference latency and reduced memory consumption.
 In this study, we aim to harness RC to improve the performance of edge-based ASR.

<sup>58</sup> For this project, we plan to evaluate our model on the English portion of the following two datasets:

• The *LibriSpeech*<sup>2</sup> corpus is a dataset comprising about 1,000 hours of audiobooks from the LibriVox project.

• The *Common Voice*<sup>3</sup> corpus is a multilingual collection of transcribed speech aimed at advancing research in ASR. The dataset contains 9,283 recorded hours.

We will compare our implementation against current state-of-the-art approaches in edge-based ASR:
Whisper Small from OpenAI [11] and Wav2Vec 2.0 from Meta [6]. Both models performed well
on ASR benchmarks but still struggled with accuracy compared to cloud-models, hampering their
effectiveness.

To evaluate the performance of the models, we will use Word Error Rate (WER), Character Error Rate (CER), and frame-wise accuracy, representing the correct prediction of a sound present in a short audio segment. In addition, the models will be compared based on their memory usage and inference time.

To improve the accuracy of our RC model, we will explore various approaches, including those mentioned in Section 3. In addition, we will fine-tune RC-specific parameters. These include reservoir

<sup>73</sup> size and connectivity, the number of reservoir layers, recurrent weights, spectral radius, and the

<sup>74</sup> degree of chaotic dynamics within the reservoir [12, 16].

<sup>&</sup>lt;sup>2</sup>https://paperswithcode.com/dataset/librispeech

<sup>&</sup>lt;sup>3</sup>https://paperswithcode.com/dataset/common-voice

#### 75 **References**

- [1] Sadeen Alharbi et al. "Automatic Speech Recognition: Systematic Literature Review". In:
   *IEEE Access* 9 (2021), pp. 131858–131876. DOI: 10.1109/ACCESS.2021.3112535.
- [2] Dario Amodei et al. "Deep speech 2: End-to-end speech recognition in english and mandarin".
   In: *International conference on machine learning*. PMLR. 2016, pp. 173–182.
- [3] Zohreh Ansari, Farzin Pourhoseini, and Fatemeh Hadaeghi. "Heterogeneous reservoir computing models for persian speech recognition". In: 2022 International Joint Conference on Neural Networks (IJCNN). IEEE. 2022, pp. 1–7.
- [4] Alexei Baevski et al. "wav2vec 2.0: A framework for self-supervised learning of speech representations". In: *Advances in neural information processing systems* 33 (2020), pp. 12449– 12460.
- [5] Ronan Collobert, Christian Puhrsch, and Gabriel Synnaeve. "Wav2letter: an end-to-end convnet-based speech recognition system". In: *arXiv preprint arXiv:1609.03193* (2016).
- [6] Santosh Gondi and Vineel Pratap. "Performance Evaluation of Offline Speech Recognition on Edge Devices". In: *Electronics* 10.21 (2021). ISSN: 2079-9292. DOI: 10.3390/
   electronics10212697. URL: https://www.mdpi.com/2079-9292/10/21/2697.
- [7] Song Han et al. "Ese: Efficient speech recognition engine with sparse lstm on fpga". In:
   *Proceedings of the 2017 ACM/SIGDA International Symposium on Field-Programmable Gate Arrays.* 2017, pp. 75–84.

Mantas Lukoševičius. "A Practical Guide to Applying Echo State Networks". In: *Neural Networks: Tricks of the Trade: Second Edition.* Ed. by Grégoire Montavon, Geneviève B. Orr, and Klaus-Robert Müller. Berlin, Heidelberg: Springer Berlin Heidelberg, 2012, pp. 659– 686. ISBN: 978-3-642-35289-8. DOI: 10.1007/978-3-642-35289-8\_36. URL: https: //doi.org/10.1007/978-3-642-35289-8\_36.

- [9] Mantas Lukoševičius and Herbert Jaeger. "Reservoir computing approaches to recurrent neural network training". In: *Computer Science Review* 3.3 (2009), pp. 127–149. ISSN: 1574-0137. DOI: https://doi.org/10.1016/j.cosrev.2009.03.005. URL: https: //www.sciencedirect.com/science/article/pii/S1574013709000173.
- <sup>103</sup> [10] Enrico Picco, Alessandro Lupo, and Serge Massar. "Deep photonic reservoir computer for <sup>104</sup> speech recognition". In: *IEEE Transactions on Neural Networks and Learning Systems* (2024).
- [11] Alec Radford et al. "Robust Speech Recognition via Large-Scale Weak Supervision". In:
   *Proceedings of the 40th International Conference on Machine Learning*. Ed. by Andreas Krause
   et al. Vol. 202. Proceedings of Machine Learning Research. PMLR, July 2023, pp. 28492–
   28518.
- [12] Bin Ren and Huan-Fei Ma. "Global optimization of hyper-parameters in reservoir computing". In: *Electronic Research Archive* 30 (May 2022), pp. 2719–2729. DOI: 10.3934/era.
  2022139.
- 112 [13] David Verstraeten et al. "The unified reservoir computing concept and its digital hardware 113 implementations". In: *Proceedings of the 2006 EPFL LATSIS Symposium*. 2006, pp. 139–140.
- In: arXiv preprint arXiv:2312.10359 (2023).
- [15] Min Yan et al. "Emerging opportunities and challenges for the future of reservoir computing".
   In: *Nature Communications* 15.1 (Mar. 2024), p. 2056.
- [16] Bolin Zhao. "Seeking optimal parameters for achieving a lightweight reservoir computing: A computational endeavor". In: *Electronic Research Archive* 30 (June 2022), pp. 3004–3018.
   DOI: 10.3934/era.2022152.

## 121 A Reservoir Computing Architecture



Figure 1: RC architecture [15] (Authorized under (CC BY 4.0)

As Figure 1 shows, an RC consists of three layers: an input (sensing) layer, a reservoir (processing) layer, and an output (control) layer. The input layer sends data to the reservoir, a fixed-weighted network that projects input data into a higher-dimensional feature space. The output layer, the only trainable component, typically uses linear regression to map these signals to the final output. The reservoir is initialized once, with its size, connectivity, and chaotic dynamics fixed. Then, the RC processes sequential data at every time step, and its recurrent connections ensure past data is carried over to future time steps [15, 13, 9, 8].

4