SpikingVTG: Saliency Feedback Gating Enabled Spiking Video Temporal Grounding

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Abstract

Video Temporal Grounding (VTG) seeks to retrieve consecutive intervals or specific clips from a video based on specified natural language queries. VTG requires accurately aligning video segments with corresponding natural language instructions, highlighting the need for effective methodologies to capture semantic correspondence and maintain temporal coherence. Spiking neural networks (SNNs), previously underexplored in this domain, present a unique opportunity to tackle VTG challenges from both the architectural and energy-efficiency perspectives. In this paper, we leverage sparse spike-based communication of SNNs to propose a multimodal architecture tailored for VTG tasks, namely SpikingVTG, providing a biologically inspired and efficient solution. Leveraging temporal saliency feedback, our proposed spiking video-language model (VLM) achieves competitive performance with non-spiking VLMs across diverse moment retrieval and highlight detection tasks. We introduce a Saliency Feedback Gating (SFG) mechanism that improves performance while reducing overall neural activity. To efficiently train our spiking VLM, we analyze the convergence dynamics of each neuronal layer and utilize equilibrium states to enable training using implicit differentiation at equilibrium. This approach eliminates the need for computationally expensive backpropagation through time while also enabling the use of knowledge distillation for efficient model training. To further improve operational efficiency and facilitate the on-chip deployability of our model, we leverage a multi-stage training pipeline that focuses on eliminating non-local computations, such as softmax and layer normalization, leading to the development of the Normalization Free (NF)- SpikingVTG model. Additionally, we create an extremely quantized variant, a 1-bit NF-SpikingVTG model, which vastly improves computational efficiency during inference while maintaining minimal performance degradation from our base model. Our work introduces the first spiking model to demonstrate competitive performance on VTG benchmarks, including QVHighlights and Charades-STA.

1 Introduction

The rapid expansion of various social medias and portable smart technologies has triggered an unprecedented surge in video content. This vast influx of data has intensified the need for efficient

38th Conference on Neural Information Processing Systems (NeurIPS 2024).

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Figure 1: High-level overview of the proposed SpikingVTG architecture. The spiking Vision-Language Model (VLM) takes video and textual features as inputs, employing a spiking transformer core that utilizes Saliency Feedback Gating through temporal feedback connections. The model incorporates a spiking decoder module that takes the output of the transformer core to predict parameters for the VTG task.

methods to retrieve and analyze video information. Consequently, the field of Video Temporal Grounding (VTG) $(1, 2)$ has emerged as a vital area of research. The main objective of VTG is to identify the precise segment of a video that corresponds to a given natural language query, enabling more accurate and context-driven video content retrieval. In this paper, we focus on two tasks: moment retrieval ([\[3,](#page-4-2) [4\]](#page-4-3)), which aims to identify video intervals relevant to a given query, and highlight detection ([\[5\]](#page-4-4)), which retrieves the best candidate segment of the video in response to the query. Therefore, our work involves analyzing multimodal data—combining video content with natural language queries—to develop an effective solution to the problem. With the rise of foundation models like large language models, the field of VTG has seen significant advancements ([\[6,](#page-4-5) [1\]](#page-4-0)). However, these models demand substantial computational power and energy ([\[7\]](#page-4-6)) to operate. Furthermore, VTG is inherently resource-intensive, requiring the analysis of long video sequences, which leads to significant computational overhead. In this work, we leverage sparse spike-based communication and simplified accumulation-based computation in spiking neural networks (SNNs) to develop an efficient, lightweight solution for VTG ([\[8\]](#page-4-7)).

We have provided the detailed motivation of ourwork in Appendix [A](#page-7-0) and the primary contribution of our work are as follows:

- SpikingVTG Model and Training Framework: We propose a transformer-based, multimodal spiking video language model with a spiking decoder module for moment retrieval and highlight detection in VTG tasks. We leverage the layer-wise convergence dynamics in our model to train our model using implicit differentiation at equilibrium, bypassing memory intensive BPTT. The result is the first spiking architecture to demonstrate competitive performance on VTG.
- Saliency Feedback Gating Mechanism: We introduce a saliency feedback gating mechanism for input video, that leverages the ASR of the output of the spiking transformer core at each time step. This temporal feedback enhances task-specific performance while minimizing neural activity, ultimately reducing overall computational overhead.
- Multi-Stage Training Pipeline: We propose a multi-stage training pipeline for our SpikingVTG framework, utilizing knowledge distillation and architectural modifications to create lightweight and computationally efficient spikingVTG variants. We replace computationally intensive non-local operations like layer normalization and softmax with hardware-friendly alternatives. We further introduce extreme quantization, developing a 1-bit NF-SpikingVTG model that significantly reduces memory as well as computational overhead.

Figure 3: High-level overview of the multi-stage training framework for our proposed SpikingVTG models, enabling the development of lightweight and computationally efficient spiking models. Below each model we have noted the primary operations involved in that architecture.

2 Methodology

The VTG problem formulation is added in Appendix [B.](#page-7-1) In this section we discuss the primary contributions of our work. The training framework leveraging layer-wise convergence dynamics at equilibrium to shown in Appendix [B.](#page-7-1)

2.1 SpikingVTG: Architecture Overview

The core computational unit of the proposed SpikingVTG model is a leaky integrate-and-fire (LIF) neuron ([\[9\]](#page-4-8)) detailed in Appendix [C.](#page-8-0) Neurons communicate with each other using sparse, spike-based activations instead of real-valued signals, significantly improving energy/power efficiency. The model architecture includes a spiking transformer core (see Appendix [E\)](#page-8-1) for processing inputs, a saliency feedback gating mechanism for dynamic input control, and a spiking decoder module (Appendix [D\)](#page-8-2) to predict the parameters required for the VTG task, as described in Appendix [B.](#page-7-1)

2.1.1 Saliency Feedback Gating (SFG)

SpikingVTG operates over a specific number of convergence time steps (T) , with the convergence dynamics detailed in the following section. This temporal processing allows us to leverage intermediate outputs to dynamically update the input to the model at every time step for better predictions. This approach conforms to the feedback connections observed in the human visual cortex ([\[10\]](#page-5-0)), providing a bio-plausible explanation for its efficacy. The average spiking rate of the final transformer encoder layer (a_N^t) of the Spiking Transformer module is used to compute a temporal saliency score with the input query enabling the design of a gating mechanism, allowing selective focusing on relevant segments of the video while minimizing computation on irrelevant segments. The operation in the saliency feedback gating mechanism is demonstrated below,

Figure 2: The internal operations of the saliency feedback gating mechanism.

$$
F_s^{v_i}[t] = \cos(\mathbf{a_i^{N_v}}[t], \mathbf{M}) := \frac{\mathbf{a_i^{N_v}}[t] \cdot \mathbf{M}}{\|\mathbf{a_i^{N_v}}[t]\|_2 \|\mathbf{M}\|_2},
$$

$$
\bar{V}[t+1] = V * F_s^v[t]
$$

$$
I[t+1] = \bar{V}[t+1] \oplus \mathbf{Q}
$$
 (1)

where, using attentive pooling operation, sentence representation $\mathbf{M} = Softmax(\mathbf{W_pQ})\mathbf{Q}, \mathbf{M} \in$ $\mathbb{R}^{1\times D}$, $\mathbf{Q} \in \mathbb{R}^{L_q \times D}$, $V \in \mathbb{R}^{L_v \times D}$ and $\mathbf{W_p}$ is a learnable embedding. $F_s^{v_i}[t]$ is the dynamic saliency score at time t for the segment of i of the video. $a_i^{N_v}[t]$ is the ASR of output of the spiking transformer for the the i corresponding segment of the video. The SFG mechanism not only results in better performance of our SpikingVTG architecture on evaluation metrics (see Table [2\)](#page-3-0) but also reduces overall neural activity by sparsifying input spikes (Fig. [S1b](#page-1-0)).

2.2 Multi-Staged Training Pipeline

Training a multimodal spiking architecture like SpikingVTG is resource-intensive. To enhance the efficiency of this process and develop computationally efficient variants of our model, we propose a multi-staged training framework, as illustrated in Fig. [3.](#page-2-0) We utilize a non-spiking "teacher" VLM to guide the training of our "student" SpikingVTG model using knowledge distillation (see Appendix [G\)](#page-9-0). After this initial stage, we fine-tune SpikingVTG using the true labels. Once the base SpikingVTG model is established, we modify its architecture, as outlined in Appendix [H](#page-10-0) & [I,](#page-10-1) followed by additional fine-tuning to create computationally efficient variants with minimal performance degradation. The resulting computationally efficient, lightweight models are well-suited for deployment on neuromorphic chips, enabling efficient inference.

Table 1: Performance comparison of the different SpikingVTG variants as highlighted in Fig. [3](#page-2-0) on the evaluation set of QVHighlights dataset.

3 Experimentation

Our model outperforms most non-spiking VTG models and achieves competitive results compared to the current state-of-the-art pretrained UniVTG model. It is important to note that SpikeMba ([\[14\]](#page-5-4)) is not a fully spiking architecture; rather, one component of its multi-stage network uses an SNN. The results for each of the compared methods are taken from their respective papers. Our model establishes a baseline for future spiking VLM architectures on VTG tasks. The results are shown in Table [1](#page-3-1) & [S1.](#page-3-1) Additional experimental details are in Appendix [J.](#page-10-2)

Table 2: Comparing the performance of different SpikingVTG variants on the evaluation set of QVHighlights dataset.

$2*Method$	QVHighlights-MR				QVHighlights-HL		Activity
	@0.3	@0.5	@0.7	mAP@avg	@mAP	HT@1	
Spiking VTG w/o SFG	78.65	65.10	47.46	42.56	40.60	67.42	0.41
SpikingVTG w/ SFG	80.72	67.42	50.65	43.81	40.74	68.32	0.34
NF-SpikingVTG w/ SFG	79.87	66.45	48.27	42.68	40.54	67.61	0.25
1-bit NF-SpikingVTG w/ SFG	78.77	65.16	47.35	42.32	40.31	67.29	0.19

3.1 Ablation Study

As shown in Table [2,](#page-3-0) the use of the Spike Feedback Gating (SFG) mechanism enhances performance compared to the model without SFG. Furthermore, as highlighted in Fig. [S1](#page-1-0) it results in reduced neuronal activity as well. Moreover, the computationally efficient NF-SpikingVTG model with SFG performs competitively even when compared to other state-of-the-art (SOTA) non-spiking video-language models (VLMs). Although the 1-bit NF-SpikingVTG variant shows a slight reduction in performance across evaluation metrics, it is highly memory efficient and involves simpler computations, making it well-suited for deployment on resource-constrained hardware. Furthermore, Table [2](#page-3-0) also presents the average model-wide neural activity of the spiking model, calculated over $T = 10$ time steps. This metric represents the proportion of active neurons per timestep, averaged across all layers. This demonstrates that the optimizations aimed at enhancing computational efficiency (i.e. reducing non-local normalization operation and introducing quantized weights) also effectively reduce overall neural activity in the model. Preliminary energy analysis is in Appenix [K.](#page-11-0)

4 Conclusions

Our saliency feedback gating-enabled SpikingVTG model offers a computationally efficient approach for VTG tasks while maintaining competitive performance with state-of-the-art non-spiking models. By harnessing layer-wise convergence dynamics, we efficiently train our model using implicit differentiation at equilibrium. We employ a multi-stage training pipeline that incorporates knowledge distillation, using the non-spiking pretrained UniVTG model as the "teacher" and the SpikingVTG model as the "student". This training pipeline further enables architectural optimizations, leading to the development of Normalization Free (NF)-SpikingVTG and 1-bit NF-SpikingVTG, enhancing computational efficiency and facilitating the on-chip deployment of these complex models.

5 Acknowledgments

This work was supported in part by the United States Air Force and Defense Advanced Research Projects Agency (DARPA) under Contract No. FA8750-23-C-0519, the Defense Advanced Research Projects Agency (DARPA) under Agreement No. HR0011-24-9-0424, and the U.S. Army Research Laboratory Cooperative Research Agreement W911NF-17-2-0196. Any opinions, findings and conclusions or recommendations expressed in this material are those of the authors and do not necessarily reflect the Department of Defense or the United States Government.

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A Motivation

Beyond the computational efficiency of SNN-based frameworks, we also harness their temporal dynamics to propose a spiking transformer-based VLM architecture (Fig. [1\)](#page-1-0), namely SpikingVTG, that matches or surpasses the performance of current state-of-the-art non-spiking VLM. The input video for the VTG task typically consists of a long sequence of segments or clips. A key challenge in VTG is thus accurately identifying salient segments ([\[2\]](#page-4-1)) or temporally dependent segments that exhibit a strong semantic correspondence with the given query. Our SNN-based VLM, operated over a period of simulation time steps, allows us to leverage its intermediate output as a feedback to identify the salient segments. We use the average spiking rate (ASR) of the output of the transformer-based SpikingVTG model to compute a dynamic saliency score of each video segment w.r.t the given query, which we then leverage as a mask for a multiplicative gating mechanism. This enables the model to focus on relevant portions of the video, as demonstrated in our experiments, while also reducing computational overhead by minimizing attention to irrelevant segments.

From a bio-plausibility perspective, as explored by Kar et al. [\[10\]](#page-5-0), feedback based connection plays a prominant role in human visual cortex primarily responsible for object recognition. Furthermore, the feedback connection maintains the layer-wise convergence of ASR at equilibrium, enabling the implementation of an implicit differentiation framework ([\[15\]](#page-5-5)), allowing for more efficient training of our model. This learning framework, leverages layer-wise converged ASR values at equilibrium to train the spiking model in one backpropagation step, instead of using the computationally expensive backpropagation through time (BPTT) ([\[16\]](#page-5-6)). The SpikingVTG framework further involves a multistage training pipeline aimed at developing spiking models to fascilitate potential deployment on resource-constrained edge-based device enabled with neuromorphic chips. To allow for efficient training of our spiking model, we employ a knowledge distillation strategy ([\[17\]](#page-5-7)), enabling knowledge transfer from a non-spiking UniVTG model, used as the "teacher", to our "student" SpikingVTG model. This process utilizes the ASR of converged intermediate states at equilibrium, enabling efficient training of our multimodal spiking video-language model.

Furthermore, transformer architectures ([\[18\]](#page-5-8)) utilize non-local normalization operations such as softmax and layer normalization, which present challenges for implementation on neuromorphic hardware ([\[19\]](#page-5-9)). To address this limitation, we introduce the Normalization-Free (NF)-SpikingVTG model, which eliminates all layer normalization operations and substitutes softmax spiking attention with a ReLU-based spiking attention mechanism. While ReLU-based attention mechanisms have previously been explored in non-spiking domains ([\[20\]](#page-5-10)), we are the first to introduce this concept within the spiking attention mechanism, demonstrating competitive performance compared to softmaxbased approaches. Additionally, to reduce computational complexity, following works on quantization in analog LLMs ([\[21\]](#page-5-11)), we propose a 1-bit quantized variant of SpikingVTG. Our multi-stage training approach enables minimal performance degradation while enhancing operational efficiency during inference, in our SpikingVTG models. To our knowledge, this work is the first to evaluate the performance of an operational spiking VLM framework across various VTG tasks, including moment retrieval and highlight detection, on datasets such as QVHighlights and Charades-STA.

B Video Temporal Grounding (VTG) Formulations

Given a video V and a language query Q, we first divide V into a sequence of L_v fixed-length clips v_1, \ldots, v_{L_v} , where each clip v_i is of length l and has a centered timestamp t_i . The free-form text query Q consists of L_q tokens, denoted as $Q = \{q_1, \ldots, q_{L_q}\}\$. Following previous studies on VTG ([\[2\]](#page-4-1)), we define three elements for each clip $v_i = (f_i, d_i, s_i)$, where $f_i = 1$ if the clip is in foreground, i.e. relevant else $f_i = 0$. $d_i = [d_{s_i}, d_{e_i}] \in \mathbb{R}^2$ represent the temporal distance that converts the clip timestamp t_i to its interval boundaries. Here, d_i is valid when $f_i = 1$. The term d_{s_i} denotes the distance between the start of the interval and t_i , while d_{e_i} denotes the distance between the end of the interval and t_i . Let $s_i \in [0,1]$ be a continuous score that quantifies the relevance between the visual content of clip v_i and the query Q. Our proposed SpikingVTG model predicts these three parameters for each video clip. In this paper, we focus on specific VTG tasks, which are carried out as follows:

Moment Retrieval: We rank the predicted clip boundaries $\{\tilde{b}_i\}_{i=1}^{L_v}$, where $b_i = [t_i - d_{s_i}, t_i + d_{e_i}]$, based on their associated probabilities given by $\{\tilde{f}_i\}_{i=1}^{L_v}$. Since the predicted L_v boundaries are dense, we employ a 1-dimensional Non-Maximum Suppression (NMS) ([\[22\]](#page-5-12)) with a threshold of 0.7 to eliminate highly overlapping boundary boxes, resulting in a final prediction.

Highlight Detection For each clip, we rank all clips based on their combined scores $\{\tilde{f}_i + \tilde{s}_i\}_{i=1}^{L_v}$. This combined value represents how well the chip i match with the underlying query. We then return the top clips (e.g., Top-1) as predictions.

C Spiking Neural Networks

The discrete time dynamics of an LIF-based spiking neuron can be given as follows,

$$
u_i[t + \delta] = \gamma u_i[t] + W_{(i-1)}(s_{(i-1)}[t]) + b_i,
$$

\n
$$
u_i[t + 1] = u_i[t + \delta] - V_{th_i}s_i[t + 1],
$$
\n(S1)

where, at time t, $u_i[t]$ is the membrane potential of the i^{th} neuronal layer; b_i indicates a bias term and γ is the leaky term. $W_{(i-1)}$ represents the layer-specific operation; $t + \delta$ is an intermediate time step to determine if the neuron fired; V_{th_i} is the threshold of layer *i*. We use a ternary spiking model ([\[23\]](#page-5-13)) in our work for spike $(s[t + 1])$ generation, thus the spiking operation is given as,

$$
s_i[t+1] = \begin{cases} +1 & \text{if } u_i[t+\delta] \ge V_{th_i}, \\ -1 & \text{if } u_i[t+\delta] \le -V_{th_i}, \\ 0 & \text{otherwise} \end{cases}
$$
(S2)

This approach enhances performance while avoiding the introduction of additiional floating-point multiplicative and accumulative (fp-MAC) operations. The average spiking rate (ASR) [\[15\]](#page-5-5) of LIF neurons within each layer i at time t can be defined as a weighted-average function:

$$
a_i[t] = \frac{\sum_{\tau=1}^t \gamma^{t-\tau} s_i[\tau]}{\sum_{\tau=1}^t \gamma^{t-\tau}}.
$$
 (S3)

D Spiking Decoder

The spiking decoder comprises of stacked 1-D convolution operations followed by integrate-fire (IF) neuron layers ($\gamma = 1$ in Eqn. [S1\)](#page-8-3), for spike generation. The spiking decoder used for predicting foreground indicator (f_i) per clip, applies n_1 1-D convolution operations with kernel size k_1 , each followed by an IF layer. The final layer consists of a single output channel, and its temporal mean is passed through a sigmoid activation to produce the final prediction. The spiking decoder used for d_i applies n_2 1-D convolution operations with kernel size k_2 , each followed by an IF layer, and the final convolution layer has two output channels to predict both components in d_i .

E Spiking Transformer Core

The fundamental computational unit in our work is a leaky-integrate and fire neuron (LIF) (see Appendix [B\)](#page-7-1)The high-level overview of each encoder block of our spiking transformer architecture is demonstrated in Fig. [1.](#page-1-0) The model consists of N encoder layers, each consists of a spiking multi-headed attention block, followed by an intermediate layer and an output layer. Communication within and between encoder layers occurs via spikes. Furthermore, all matrix multiplications involved in linear layers and attention layer comprises of more efficient fp-accumulative (ACC) operations instead of fp-MAC operations in conventional neural architectures. Detailed descriptions of each layer are provided in the appendix. In the following section, we highlight modifications made to the architecture to facilitate the development of more efficient SpikingVTG variants. In Section [H,](#page-10-0) we replace non-local normalization operations and introduce a ReLU-based attention mechanism. In Section [I,](#page-10-1) we quantize all weights in the linear layers, including those in the intermediate and output layers, to 1-bit precision.

Figure S1: Results obtained upon passing a random input sample from QVHighlights dataset to our SpikingVTG models. (a) Graph shows convergence dynamics of layer-wise mean ASR against operating time steps for a randomly selected spiking transformer encoder layer (Fig. [1\)](#page-1-0). It is to be noted that since we allow ternary spikes ASR can be negative as well. (b) Graph shows, layer-wise mean spiking activity ($act_i[t]$, averaged over number of neurons in that layer) against operating time steps in x-axis. The model with SFG shows markedly reduced activity in both the input layer and the spiking attention layer, underscoring its role in minimizing neuronal activity.

F Training Leveraging Convergence Dynamics

Following, Eqn. [S1](#page-8-3) & [S3,](#page-8-4) we can formulate $a_i[t + 1] = \frac{1}{V_{th_i}}(\hat{f}(a_{(i-1)}[t + 1]) + b_i - \frac{u_i[t+1]}{\sum_{j=0}^{t} \gamma_j}),$ where \hat{f} is operation of layer i. As time approaches $t \to \infty$, the layer-wise ASRs converge to equilibrium, enabling the derivation of steady-state equations for linear layers ([\[15\]](#page-5-5)). Moreover, surrogate steady-state functions can be formulated for non-linear layers ([\[24\]](#page-5-14)) as,

 $a_i^* = \sigma(\frac{1}{V_{th_i}}(\hat{f}(a_{i-1}^*) + b_i))$, where, clipping function $\sigma(x)$ clamps the values within [−1, 1]. This is because following Eqn. [S2,](#page-8-5) we allow ternary spikes thus ASR must be with $[-1, 1]$. The empirical convergence of ASR is shown in Fig. [S1a](#page-1-0). To analyze the overall layer-wise neural activity, which includes both positive and negative spiking event, we present the layer-wise dynamics of the absolute spiking events in Fig. [S1b](#page-1-0), i.e. $act_i[t] = \frac{\sum_{i=1}^{t} |s_i[t]|}{t}$.

During training, leveraging implicit differentiation ([\[25\]](#page-5-15)) at equilibrium, only ASR values at equilibrium are used, $\frac{\partial L(a^*)}{\partial \theta} = -\frac{\partial L(a^*)}{\partial a^*} (J_{g_{\theta}}^{-1}|_{a^*}) \frac{\partial f_{\theta}(a^*)}{\partial \theta}$ where, θ is the model parameters, $g_{\theta}(a) = f_{\theta}(a) - a$, f is the steady-state equation of ASR, J^{-1} is the inverse Jacobian of g_{θ} when $a = a^*$, i.e., at equilibrium. Thus, unlike BPTT, we do not need to store the intermediate computational graph and the model parameters can be updated using a single backpropagation step.

G Leveraging Knowledge Distillation (KD)

To enable efficient training of our spiking multimodal architecture, we utilize Knowledge Distillation (KD) techniques ([\[17,](#page-5-7) [26,](#page-5-16) [27\]](#page-6-0)). We use the pre-trained UniVTG, currently the state-of-the-art in VTG, as a "teacher" and rather than distilling based on the prediction layer, we exploit the outputs of the internal layers of the "teacher". We establish a one-to-one mapping, by design, between the internal representations at equilibrium of our spiking transformer and the corresponding layers of the "teacher", ensuring that the number of layers in both architectures remains consistent. The internal representation-based KD is formulated as,

$$
L_{KD} = \sum_{i=1}^{N} MSE(a_{r_i}^* W_d, T_{r_i})
$$
 (S4)

where, $W_d \in \mathbb{R}^{d_s \times d_t}$ is a linear transformation that aligns the dimensionality of the layer of the "students" with that of the corresponding layer of the "teacher". $a_{r_i}^*$ denotes the converged ASR of the internal representation (dimension d_s) layer r_i , which is the output from the spiking transformer encoder layer *i*. T_{r_i} is the representation of the the corresponding block *i* (dimension d_t) of the

teacher model. The KD process is an integral part of the framework and serves as the first stage of our multi-stage pipeline, followed by fine-tuning on the true labels (Fig. [3\)](#page-2-0).

H Replacing Softmax and Removing Layer Normalization

In our work, we use a spiking attention mechanism (detailed in Appendix) which uses the key and value inputs as spikes instead of real values. Given d -dimensional queries, keys, and values ${q_i[t], s_{k_i}[t], s_{v_i}[t]}_{i=1}^L$, at time t, the attention weights α_{ij} are generally computed as follows:

$$
\alpha_{ij}[t] = \phi \left(\frac{1}{\sqrt{d}} \left[q_i[t]^\top s_{k_1}[t], \cdots, q_i[t]^\top s_{k_L}[t] \right] \right)_j \tag{S5}
$$

where, ϕ is usually the softmax function and output of spiking attention layer at time t is $attn_i[t] =$ $\sum_{j=1}^{L} \alpha_{ij} [t] s_{v_j} [t]$. Given that softmax requires expensive non-local fp-MAC operations, we replace it with the less costly ReLU() operation and perform a simple scaling with L^{-1} . This replacement, while maintaining competitive model performance, is only feasible when following the multi-stage training process outlined in Fig. [3.](#page-2-0) This highlights the importance of the initial KD and fine-tuning stages, which help stabilize the model. Additionally, we explore the removal of all layer normalization layers, from Fig. [1,](#page-1-0) during training (as shown in Fig. [3\)](#page-2-0), further streamlining the model design for on-chip deployment. We refer to the resulting model, which uses ReLU in place of Softmax and omits layer normalization, as a Normalization-Free (NF) spiking model.

I 1-BIT weight quantized SpikingVTG

Following prior work ([\[21\]](#page-5-11)), 1-bit quantization consists of centering the weights W to achieve a zero mean, followed by binarization to $+1$ or −1 using the signum function as shown,

$$
W_q = \text{sgn}(W - \alpha),
$$

\n
$$
\alpha = \frac{1}{nm} \sum_{ij} W_{ij}
$$
 (S6)

where, $W \in \mathbb{R}^{n \times m}$. The signum function, denoted as $sgn(x)$, categorizes the element x based on its sign. It outputs $+1$ when x is positive and -1 when x is zero or negative. . Additionally, the output of the linear layer is scaled by a constant $\hat{\beta} = \frac{1}{nm} \sum_{ij} |W_{ij}|$. Thus, with ternary

Table S1: Performance comparison of our SpikingVTG model with SFG against other non-spiking VTG solutions on the evaluation set of the QVHighlights for highlight detection task.

2*Method	SNN	OVHighlights	
		mAP	HIT@1
$\overline{\text{UniVTG+PT}}$ ([2])	No	41.34	68.77
DVSE ([28])	No	18.75	21.79
$XML+([1])$	No	35.38	55.06
$M-DETR([1])$	No	35.65	55.55
EaTR (11)	No	37.15	58.65
$M-DETR + PT$ ([1])	No	37.70	60.32
UniVTG (2)	No	38.83	61.81
OD-DETR ([13])	No	39.13	63.03
UMT([6])	No	39.85	
SpikingVTG (Our Model)	Yes	40.74	68.32

activations, our model now incorporates binary weights. Following the multi-stage learning approach illustrated in Fig. [3,](#page-2-0) our 1-bit SpikingVTG model emerges as a light-weight multimodal spiking VLM model. Additionally, we empirically demonstrate that employing binary weights while eliminating normalization layers achieves competitive performance, resulting in 1-bit NF-SpikingVTG, enabling on-chip implementation and significantly improving computational efficiency. Thus, in the resulting model the primary computational operation involve interger accumulations since individual weight values are $W_{q_{ij}} \in \{-1, 1\}$ and activations are $s_{ij} \in \{-1, 0, 1\}.$

J Additional Experimental Details

The Spiking Transformer core in our model comprises four encoder layers, each with a hidden dimension of 1024, with 8 attention heads. For the knowledge distillation phase, we employ a pre-trained UniVTG model ([\[2\]](#page-4-1) that has been fine-tuned on our specific dataset.

Dateset Details: QVHighlights ([\[1\]](#page-4-0)) is the only public dataset that includes ground-truth annotations for moment retrieval and highlight detection, allowing for a thorough evaluation of the performance of our model and additional ablation studies. We also employ the Charades-STA dataset ([\[29\]](#page-6-2)) to conduct further assessments on additional moment retrieval tasks.

Evaluation Metrics: For QVHighlights, following previous work ([\[1\]](#page-4-0)) we use Recall@1 with IoU thresholds of 0.3, 0.5 and 0.7 and average mean average precision (mAP) as the evaluation metric for moment retrieval tasks. For highlight detection, we use mAP and HIT@1, where a clip is considered a true positive if it receives a score of "Very Good" ([\[6\]](#page-4-5)). For Charades-STA, we employ Recall@1 with IoU thresholds of 0.3, 0.5, and 0.7, along with the mean IoU (mIoU).

K Analysis of Energy and Power Efficiency

We conduct a preliminary energy analysis of the proposed SpikingVTG variants during inference and compare it to a non-spiking UniVTG model with comparable depth and hidden state dimensions (also hidden dimension (D) is same as intermediate layer dimension in our implementation). From a simpler circuit design standpoint, for our analysis we consider 45nm CMOS technology and 32-bit precision, thus floating point (fp)-MAC operations consume $4.6pJ$, fp-ACC operations consume $0.9pJ$ and integer(int)-ACC operations consume $0.1pJ$ ([\[30\]](#page-6-3)). The primary energy consumption is attributed to the transformer encoder layers, which consist primarily of the attention mechanism and multiple linear layers ([\[21\]](#page-5-11). The primary computation cost, calculated for an input sequence of length L, of each transformer encoder-layer

Figure S2: Graph depicting the performance of each model type on the QVHighlights highlight detection task, alongside their potential energy efficiency (E_f) .

of the non-spiking model can be expressed as: $Cost_A = [(3LD^2) + (LD^2 + L^2D) + (LD^2) + (LD^2)]$ fp-MAC operations, corresponding to the three projection layers for query, key, and values, the attention mechanism, the intermediate layer, and the output layer.

For the SpikingVTG model, per spiking transformer encoder layer the energy cost per time step is given by: $Cost_{S_t} = [(3 \cdot IFR_{in} \cdot LD^2) + (IFR_{k} \cdot LD^2 + IFR_{v} \cdot L^2D) + (IFR_{attn} \cdot LD^2) +$ $(IFR_{interm.} \cdot LD^2)]$ fp-ACC operations, where each term is associated for each component similar to the one specified above. IF \bar{R}_l represents the mean firing rate of the corresponding layer l. The total energy cost for the spiking model is: $Cost_S = (Cost_{S_t} * T)$ fp-ACC operations, where T represents the number of time steps. The models that include normalization also have an added cost of energy for normalization however, it is of the order $O(LD)$ so have not been included in our computation however it is to be noted that both our NF-SpikingVTG and 1-BIT NF-SpikingVTG models are normalization free so they do not incur this added cost. Moreover, for 1-bit spikingVTG, the core computations in matrix multiplications shift from using fp-ACC to int-ACC operations.

We define energy efficiency of the spiking model as $E_f = Cost_A / Cost_S$. Specific examples of the energy analysis are provided in the Appendix. When operating the underlying models for $T = 10$ time steps, the energy efficiency and performance of each model are illustrated in Fig. [S2.](#page-2-1) The average power efficiency for each model type is calculated as $P_f = E_f \times T$, demonstrating that our models are significantly more power-efficient (ranging from $12.5\times$ in SpikingVTG to upto $200\times$, in 1-bit NF-SpikingVTG) compared to non-spiking models. This efficiency arises from the ability of SNNs to unroll complex operations over time, thus providing low-powered solutions for complex tasks. Although this method of analysis does not account for various architectural energy advantages, it provides a useful approximation to gauge the potential benefits of spiking models over their non-spiking counterparts.