

# Turn Waste into Worth: Rectifying Top- $k$ Router of MoE

Anonymous ACL submission

## Abstract

Sparse Mixture of Experts (MoE) models are popular for training large language models due to their computational efficiency. However, the commonly used top- $k$  routing mechanism suffers from redundancy computation and memory costs due to the unbalanced routing. Some experts are overflow, where the exceeding tokens are dropped. While some experts are vacant, which are padded with zeros, negatively impacting model performance. To address the dropped tokens and padding, we propose the Rectify-Router, comprising the Intra-GPU Rectification and the Fill-in Rectification. The Intra-GPU Rectification handles dropped tokens, efficiently routing them to experts within the GPU where they are located to avoid inter-GPU communication. The Fill-in Rectification addresses padding by replacing padding tokens with the tokens that have high routing scores. Our experimental results demonstrate that the Intra-GPU Rectification and the Fill-in Rectification effectively handle dropped tokens and padding, respectively. Furthermore, the combination of them achieves superior performance, surpassing the accuracy of the vanilla top-1 router by 4.7%.

## 1 Introduction

Sparse Mixture of Experts (MoE) is gaining popularity as a model architecture for training large language models (Fedus et al., 2022; Du et al., 2022; Zoph et al., 2022; Jiang et al., 2024; Dai et al., 2024) owing to its computational efficiency. In a sparse MoE model, each token is assigned to one or more experts based on a routing mechanism. The top- $k$  router is currently the most widely used routing mechanism, where tokens are directed to the experts with the top- $k$  scores.

However, top- $k$  router is unbalanced, where the number of tokens routed to different GPUs is not the same. In order to achieve a balanced workload across GPUs, top- $k$  routing imposes a maximum

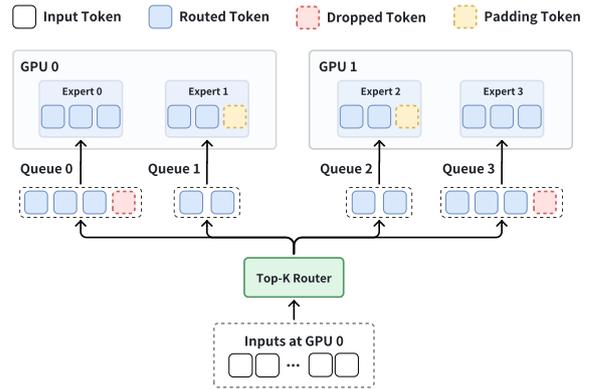


Figure 1: The illustration of dropped token and padding in top- $k$  router of MoE. Queue  $i$  represents the queue of tokens to be sent to expert  $i$ . The capacity of each expert is fixed to 3.

limit on the number of tokens that each expert can process. Consequently, any tokens exceeding this limit are dropped, and vacant experts are padded with zeros, which negatively impacts the overall model performance (Gale et al., 2022).

Previous studies have attempted to address the balance issue in routing by introducing auxiliary loss mechanisms (Shazeer et al., 2017; Lepikhin et al., 2021; Zoph et al., 2022). But even with these enhancements, the performance drop resulting from dropped tokens is still significant (Zhou et al., 2022; Gale et al., 2022). Although some approaches have proposed absolutely balanced routers, they have been found to underperform the original top- $k$  routing methodology (Yu et al., 2022).

Rather than focusing on improving the balance of the top- $k$  router, this work introduces an alternative approach called the **Rectify-Router**, which rectifies top- $k$  router by post-processing the dropped tokens and padding from the top- $k$  router. We propose two Rectify-Routers: the **Intra-GPU Rectification** and the **Fill-in Rectification**.

065 The Intra-GPU Rectification is designed to handle  
066 the dropped tokens, while the Fill-in Rectification  
067 specifically addresses the padding issue.

068 Post-processing the dropped tokens with another  
069 router may bring expensive communication cost.  
070 Therefore, we propose the Intra-GPU Rectifica-  
071 tion which routes the dropped tokens to the ex-  
072 perts within the GPU where they are located, elim-  
073 inating the need for inter-GPU communication.  
074 Our empirical experiments have demonstrated that  
075 the Intra-GPU Rectification effectively handles the  
076 post-processing of dropped tokens and is more effi-  
077 cient than the commonly used routers, in terms of  
078 communication.

079 To address the padding issue, we present the  
080 Fill-in Rectification, which replace padding tokens  
081 with the tokens that have high routing scores. Fill-  
082 in Rectification first identifies the optimal expert  
083 for each token based on the routing scores and  
084 subsequently selects the tokens with the highest  
085 routing score to replace the padding for each expert.  
086 By employing Fill-in Rectification, tokens with the  
087 higher routing scores receive more computational  
088 allocation.

089 The Intra-GPU Rectification and Fill-in Rectifi-  
090 cation are orthogonal approaches that can be seam-  
091 lessly combined. Our experiments have demon-  
092 strated their effectiveness in handling dropped to-  
093 kens and padding. Furthermore, combing the Intra-  
094 GPU Rectification and Fill-in Rectification yield  
095 improved performance compared to using them  
096 individually.

097 **Contributions** The contributions of our work can  
098 be summarized as follows:

- 099 1. We introduce the concept of Rectify-Router  
100 to handle the dropped tokens and padding in  
101 MoE models. Specifically, the dropped tokens  
102 are efficiently processed using the Intra-GPU  
103 Rectification, while the padding tokens are  
104 optimally managed using the Fill-in Rectifica-  
105 tion.
- 106 2. Our experiments demonstrate that both the  
107 Intra-GPU Rectification and the Fill-in Recti-  
108 fication significantly improve the performance  
109 of the top- $k$  routing, even without additional  
110 training.
- 111 3. We found that our methods are robust to vari-  
112 ous settings of expert capacity and that Intra-  
113 GPU Rectification can be used for accelerat-  
114 ing MoE by reducing expert capacities.

## 2 Related Works 115

116 The routing of MoE can be classified into two cat-  
117 egories: balanced and unbalanced. The balanced  
118 routing assigns the same number of tokens to each  
119 expert, while the unbalanced routing does not make  
120 sure that the number of tokens received by each ex-  
121 pert is the same.

**Unbalanced Routing** Top- $k$  routing was the  
122 most commonly used unbalanced routing proposed  
123 by Shazeer et al. (2017), which greedily assigns  
124 tokens to experts, according to the token-expert  
125 assignment scores. Numerous MoE models have  
126 adopted top- $k$  routing, including Switch Trans-  
127 former (Fedus et al., 2022), Glam (Du et al., 2022),  
128 ST-MoE (Zoph et al., 2022), Flan-MoE (Shen et al.,  
129 2023), and NLLB (Koishekenov et al., 2022), to  
130 name just a few. 131

132 It is worth noting that many unbalanced rout-  
133 ing methods are variations or derivatives of top- $k$   
134 routing. For example, Switch Transformer (Fedus  
135 et al., 2022) argues in favor of using top-1 routing  
136 instead of top-2 routing for improved efficiency.  
137 ST-MoE (Zoph et al., 2022) and LIMoE (Mustafa  
138 et al., 2022) propose auxiliary loss functions to  
139 enhance the stability of MoE during training. Ad-  
140 ditionally, SCoMoE (Zeng and Xiong, 2023) and  
141 Gating-Dropout (Liu et al., 2022) improve the effi-  
142 ciency of top- $k$  routing by designing hierarchical  
143 routing systems based on the hierarchical structure  
144 of the communication topology. 144

145 The routing method proposed in this paper is  
146 also a variation of top- $k$  routing. However, unlike  
147 the aforementioned approaches, our objective is to  
148 address the issues of dropped tokens and padding  
149 that arise from unbalanced routing. Switch Trans-  
150 former (Fedus et al., 2022) tackles the problem  
151 of dropped tokens by increasing the capacity of  
152 experts, allowing each expert to handle more to-  
153 kens. While this approach reduces the number of  
154 dropped tokens, it introduces additional overhead  
155 in terms of both speed and memory. On the other  
156 hand, Megablocks (Gale et al., 2022) addresses  
157 the challenges of padding and dropped tokens by  
158 gathering all experts onto the same GPU and em-  
159 ploying model parallelism rather than expert par-  
160 allelism. However, their approach may encounter  
161 difficulties when dealing with a large expert size or  
162 a substantial number of experts.

**Balanced Routing** In response to the imbalance  
163 issue inherent in top- $k$  routing, several balanced  
164

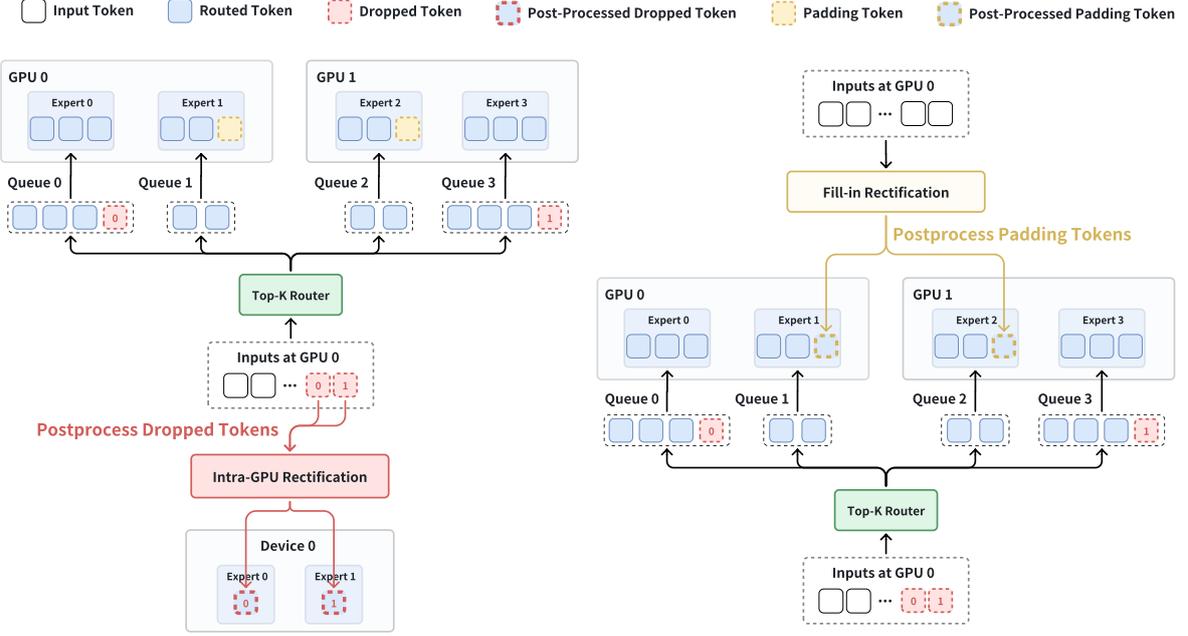


Figure 2: Left: Post-processing of dropped tokens at GPU 0 with Intra-GPU Rectification. Right: Post-processing of padding at GPU 0 with Fill-in Rectification.

routing methods have been proposed. For instance, the Base Layer approach (Lewis et al., 2021) employs a balanced assignment algorithm to evenly distribute tokens among experts. However, their assumption that tokens within the same batch can be evenly clustered may not hold true in all cases, which can potentially result in poorer performance (Yu et al., 2022). Another alternative to balanced routing is random routing (Zuo et al., 2022), which assigns tokens to experts in a random manner. While random routing achieves balance and efficiency, it lacks any specialization or optimization in the routing process. Another approach called expert choices (Zhou et al., 2022) allows each expert to select a fixed number of tokens, rather than relying on tokens to determine their target experts. This approach helps to avoid padding issues but still results in dropped tokens. Soft routing (Puigcerver et al., 2023) is a method that compresses tokens by applying a linear transformation to generate fixed-size hidden states for each expert. However, this method is only suitable for encoder models with fixed input lengths and may not be applicable to autoregressive decoder models.

### 3 Preliminary

In this section, we will introduce expert parallelism, top- $k$  routing, and two prevalent challenges that

emerge while employing top- $k$  routing: padding and dropped tokens.

**Expert Parallelism and Top- $k$  Routing** In expert parallelism, experts are distributed across GPUs uniformly. If there are  $n$  experts and  $k$  GPUs, each GPU contains  $k/n$  experts. The process of transmitting tokens to the respective experts entails inter-GPU communication.

Top- $k$  routing greedily assigns tokens to experts according to the routing score:

$$\mathbb{R}_i = \operatorname{argtopk}_{j \in [m]} \{a_{ij} | a_{ij} = w_j^T x_i\} \quad (1)$$

where  $a_{ij}$  is the score of assigning the  $i$ th token to the  $j$ th expert,  $w_j$  denotes the embedding vector of the  $j$ th expert,  $x_i$  corresponds to the hidden states of the  $i$  token. The index set  $\mathbb{R}_i$  signifies the target experts of the  $i$ th token. Given the scores of assigning token  $x_i$  to  $m$  experts, denoted as  $a_{i0}, a_{i1}, \dots, a_{im}$ ,  $\mathbb{R}_i$  contains the indices of experts with top- $k$  scores.

Since each token undergoes processing by multiple experts, the outputs of these experts for the same token are consolidated through linear combination. The combining weights are determined by the normalized routing scores, as defined in Eq. (1):

$$o_i = \sum_{j \in \mathbb{R}_i} \frac{e^{a_{ij}}}{\sum_{j \in \mathbb{R}_i} e^{a_{ij}}} E_j(x_i). \quad (2)$$

Here,  $o_i$  represents the combined result of token  $x_i$ . The term  $\frac{e^{a_{ij}}}{\sum_j^k e^{a_{ij}}}$  denotes the normalized routing scores, while  $E_j(x_i)$  refers to the outputs of the  $j$ th expert with token  $x_i$  as its input.

The top- $k$  routing approach exhibits an inherent imbalance, wherein the distribution of tokens among different experts is not uniform. However, the current distributed framework exclusively supports balanced computation across GPUs. Consequently, there exists a limitation on the maximum number of tokens that each expert can receive, which is referred to as the capacity. The capacity is determined by the capacity factor, which is typically set to  $k$  for top- $k$  routing (Lepikhin et al., 2021; Rajbhandari et al., 2022). Mathematically, the capacity can be expressed as:

$$\text{capacity} = \text{capacity factor} \times \frac{\text{number of tokens}}{\text{number of experts}}.$$

**Dropped Tokens and Padding** The issue of dropped tokens and padding arises naturally when dealing with the expert capacity setting, as depicted in Figure 1. With a fixed expert capacity, overflow experts are compelled to drop tokens with the lowest routing scores and directly pass them to the next layer through residual connections, as highlighted in red in Figure 1. Consequently, due to the dropped tokens, the set  $\mathbb{R}_i$  defined in Eq. (1) only includes the successfully routed experts, i.e.,  $|\mathbb{R}_i| \leq k$ .

Conversely, certain experts may receive fewer tokens than the capacity limitation, leading to redundant computation in the form of padding. These padding instances are illustrated in yellow in Figure 1.

If the capacity factor for top- $k$  routing is set to  $k$ , the number of dropped tokens and padding tokens will be equal. However, this equality does not hold if we modify the capacity factor. Increasing the capacity factor results in fewer dropped tokens but more padding. Conversely, reducing the capacity factor reduces padding tokens but increases the number of dropped tokens.

## 4 Method

In this paper, we introduce a novel approach to address both the dropped tokens and padding associated with top- $k$  routing by utilizing Rectify-Routers. Specifically, we propose two Rectify-Routers: the Intra-GPU Rectification and the Fill-in Rectification, which are visualized in Figure 2. The

Intra-GPU Rectification is designed to efficiently post-process the dropped tokens, while the Fill-in Rectification is dedicated to addressing the padding problem.

### 4.1 Rectify-Router for Dropped Tokens: Intra-GPU Rectification

We expect to post-process the dropped tokens by evenly routing them across GPUs. But sending tokens among GPUs requires expensive communication cost. Furthermore, the dropped tokens have the lower routing scores than the other tokens routed to the same expert, which may be less important. Therefore, we propose an efficient Rectify-Router for the dropped tokens: Intra-GPU Rectification, which dispatch the dropped tokens to the experts inside GPU, which does not require any communication among GPUs. This process is visualized in the left part of Figure 2, where the dropped tokens from GPU 0 are routed to the expert 0 or expert 1 at GPU 0.

Given the input token  $x_i$ , the Intra-GPU Rectification greedily assigns token  $x_i$  to the optimal expert within the same GPU according to the routing scores. The Intra-GPU Rectification can be seen as a variant of the top- $k$  routing. If all experts are distributed in the same GPU, then the Intra-GPU Rectification is exactly the top-1 routing.

In top- $k$  routing, the same token may be dropped by multiple times. Take the top-2 routing as an example, if a token  $x_i$  is dropped at both the first and second routing, it should be sent to two experts at Intra-GPU Rectification. To simplify the problem, we only send  $x_i$  to one expert, although it is dropped twice. In another example, the token  $x_i$  is dropped only at the second routing, while the first routing is successful. In this case, we have to combine the results of top- $k$  routing and Intra-GPU Rectification. We combine them linearly according to the routing scores:

$$o_i = \frac{\sum_{j \in \mathbb{R}_i} e^{a_{ij}} E_j(x_i) + (k - |\mathbb{R}_i|) e^{a_{ih}} E_h(x_i)}{(\sum_{j \in \mathbb{R}_i} e^{a_{ij}}) + (k - |\mathbb{R}_i|) e^{a_{ih}}}, \quad (3)$$

where  $E_j(x_i)$  represents the expert outputs obtained through top- $k$  routing, while  $E_h(x_i)$  denotes the expert outputs from Intra-GPU Rectification. We normalize the routing scores  $a_{ij}$  and  $a_{ih}$  as the combining weights of  $E_j(x_i)$  and  $E_h(x_i)$  respectively. Specifically, we scale the combining weights of  $E_h(x_i)$  with a constant factor  $(k - |\mathbb{R}_i|)$ , because a token is dropped  $(k - |\mathbb{R}_i|)$  times but

only processed by one expert in the Intra-GPU Rectification.

Similar to the top- $k$  router, the Intra-GPU Rectification also exhibits imbalance. However, this imbalance does not affect the computational fairness among GPUs. In Intra-GPU Rectification, the computation cost of a GPU is solely determined by the number of dropped tokens at that particular GPU, rather than the routing outcomes. Since the data is independently and identically distributed across devices, the number of dropped tokens on different GPUs is approximately the same.

## 4.2 Rectify-Router for Padding: Fill-in Rectification

Fill-in Rectification aims to replace the unnecessary padding with the tokens that have high routing scores, which is visualized in the right part of Figure 2. This process is divided into two separate stages. Firstly, we identify the most suitable expert for each token, and subsequently, we select the optimal tokens for each expert.

During the initial stage, each token will choose the expert ranked as the  $k + 1$ th highest score as the optimal expert. This decision is based on the fact that the top- $k$  experts have already been assigned, and the  $k + 1$ th expert is considered the most suitable among the remaining experts. Furthermore, each token is only allowed to select one expert, which avoids the same token being processed by multiple experts during the second stage.

Upon completion of the first stage, we transition to the second stage. It is worth noting that multiple tokens may select the same expert. Consequently, it is possible that the number of tokens choosing a particular expert surpasses the number of padding tokens of that expert. In such scenarios, we prioritize the tokens with higher routing scores for replacing the padding tokens.

Indeed, implementing this algorithm can be achieved by extending the top- $k$  router to a top- $k + 1$  router while ensuring the expert capacity remains unchanged. As the expert capacity is fixed, introducing the Fill-in Rectification incurs minimal additional overhead. Alternatively, we can view this approach as reducing the capacity factor of the top- $k + 1$  routing from  $k + 1$  to  $k$  to avoid the padding.

The Fill-in Rectification has a potential issue related to the normalization of routing scores, where the gradient of routing scores may vanish due to

the invalid normalization. We address this issue in Appendix A with straight-through trick (Bengio et al., 2013).

## 5 Experiments

### 5.1 Experiment Settings

**Model** We follow previous work (Komatsuzaki et al., 2023) to train MoE models from a pretrained dense model. We initialize all experts in the same layer of MoE as the FFN parameters of the corresponding layer in the Dense model. We use the Llama2-7b (Touvron et al., 2023) to initialize MoE models. Following previous work (Lepikhin et al., 2021; Fedus et al., 2022), for every alternate layer, we replaced the FFN layer in Llama2-7b with an MoE layer to transform it to an MoE model. Consequently, Llama2-7b, with 32 FFN layers, is transformed into an MoE model with 16 FFN layers and 16 MoE layers. In most of our experiments, we employ eight experts per layer in the MoE models. But in Appendix B, we explore the extension of the number of experts to 32. To ensure efficient computation, we evenly distribute the experts across multiple GPUs, with each GPU hosting a single expert for each layer. Our experiments are conducted using the MoE implementation of DeepSpeed (Rajbhandari et al., 2022) and the training framework of gpt-neox (Andonian et al., 2021).

For simplicity, we denote our Intra-GPU Rectification as **IR**, and the Fill-in Rectification as **FR**. The top- $k$  router, depending on whether it uses the Intra-GPU Rectification or the Fill-in Rectification, will be denoted as **Top-k +IR** or **Top-k+FR**, respectively.

**Training** During the training phase, we utilize the OpenOrca dataset (Lian et al., 2023), which is an open-source reimplementation of Orca dataset (Mukherjee et al., 2023). It augments the instructions from flan data (Longpre et al., 2023) by adding complex system prompts and generate the step-by-step reasoning or explanation using chatgpt (OpenAI et al., 2023).

We conduct our model training on a cluster of 32 GPUs (80GB). The training process consists of 10k steps with a global batch size of 256 and a micro batch size of 8. Following Mukherjee et al. (2023), we construct training examples by concatenating instructions with their corresponding responses: “[instruct][response]”. However, only the tokens in the response are utilized for the next-token-prediction

Model	Router	CF	Train Speed	MMLU	SuperGLUE	TruthfulQA	LogiQA	Avg
LLama2-raw	-	-	3.2k	25.85	59.06	25.21	25.03	33.78
LLama2	-	-	3.2k	35.01	63.74	30.23	27.64	<b>39.15</b>
LLama-MoE (Top-1)	Top-1	1.0	2.4k	33.05	64.34	29.49	28.11	38.74
	Top-1+IR	1.0	2.3k	<b>36.27</b>	64.52	30.35	<b>30.56</b>	40.42
	Top-1+FR	1.0	2.3k	34.66	63.97	28.51	29.18	39.08
	Top-1+FR+IR	1.0	2.2k	35.81	<b>65.08</b>	<b>30.84</b>	<b>30.56</b>	<b>40.57</b>
LLama-MoE (Top-2)	Top-2	2.0	1.7k	35.39	64.58	29.98	29.33	39.82
	Top-2+IR	2.0	1.6k	35.92	65.11	29.98	29.03	40.01
	Top-2 + FR	2.0	1.6k	35.90	64.35	<b>31.08</b>	29.80	40.28
	Top-2 +FR+IR	2.0	1.5k	<b>36.01</b>	<b>65.60</b>	30.72	<b>29.95</b>	<b>40.57</b>

Table 1: The performance of LLama2-7b and MoE models on MMLU, SuperGLUE, TruthfulQA and LogiQA. CF denotes the capacity factor defined in Eq. (3). Avg represents the average accuracy. The training speed is measured as the number of tokens that each GPU can process per second. All models were trained on OpenOrca except for LLama2-raw. Top- $k$ +FR and Top- $k$ +IR represents the top- $k$  router using Fill-in Rectification and Intra-GPU Rectification respectively. Top- $k$ +FR+IR combines both the Fill-in Rectification and the Intra-GPU Rectification.

loss. To remove the padding, we fix the sequence length to be 2048 and pack multiple examples to the same sequence. For optimization, we use the Adam optimizer (Kingma and Ba, 2015) with an initial learning rate of  $1e-5$ , which is decayed to  $1e-6$  using a cosine learning rate scheduler. Regarding the load-balance loss for the top- $k$  router, we set the weights to  $1e-2$ , following Fedus et al. (2022).

**Evaluation** We evaluated our models on multiple benchmarks, including MMLU (Li et al., 2023), SuperGLUE (Wang et al., 2019), TruthfulQA (Lin et al., 2022) and LogiQA (Liu et al., 2020), which covers the evaluation in knowledge, natural language understanding, safety, and logical reasoning respectively. All evaluations were conducted in a zero-shot setting. Our evaluation metric was accuracy, and we utilized the lm-evaluation-harness (Gao et al., 2023) framework for conducting the evaluations.

## 5.2 Main Results

We trained both LLama2-7b and LLama-based MoE on OpenOrca and evaluated them on MMLU (knowledge), SuperGLUE (NLU), TruthfulQA (Safety) and LogiQA (Reasoning), the results of which are shown in Table 1. Comparing the performance of LLama2-raw (pretrained) and LLama2 (trained on OpenOrca), we observed that the LLama2 outperforms LLama2-raw substantially, which demonstrates the effectiveness of finetuning on openorca. To evaluate the effectiveness of our methods, we applied our Intra-GPU Rectification (IR) and Fill-in Rectification (FR) to both the top-1

router and top-2 router. These configurations are grouped as LLama-MoE (Top-1) and LLama-MoE (Top-2) in Table 1.

**LLama-MoE (Top-1)** We conducted 4 top-1 based MoE models (Top-1, Top-1+FR, Top-1+IR, Top-1+FR+IR). The performance of the vanilla top-1 router is subpar, and it is even inferior to the dense model (LLama2-FT) on both MMLU and TruthfulQA. But after incorporating our proposed Intra-GPU Rectification (Top-1+IR), the performance of the top-1 router are significantly improved on all benchmarks, especially on MMLU and LogiQA. This indicates that the dropped tokens have a substantial impact on model performance, and the Intra-GPU Rectification effectively handles these dropped tokens. Our Fill-in Rectification (Top-1+FR) also significantly improves the performance of the model on MMLU and LogiQA tasks. But it is worth noting that the performance of the model declined on the other two benchmarks. Therefore, it can be concluded that the primary issue with top-1 routing lies in dropped tokens rather than padding. Combing the Intra-GPU Rectification and Fill-in Rectification resulted in the best top-1-based router (Top-1+FR+IR), which outperforms the vanilla top-1 router by 1.83 (4.7%) in terms of the average accuracy across benchmarks.

**LLama-MoE (Top-2)** Top-2 based routers also encompass 4 routers (Top-2, Top-2-FR, Top-2-IR, Top-2-FR+IR). Both the Intra-GPU Rectification and Fill-in Rectification significantly enhance the performance of Top-2 router on at least 2 benchmarks, which demonstrate that our methods are

Train Router	Test Router	Test CF	Test Speed	MMLU	SuperGLUE	TruthfulQA	LogiQA	Avg
Top-1	Top-1	1.0	9.4k	33.05	64.34	29.49	28.11	38.74
	Top-1+IR		9.2k	36.21	<b>64.64</b>	29.86	29.18	39.97
	Top-1+FR		8.9k	33.28	62.76	28.51	29.49	38.51
	Top-1+FR+IR		8.6k	<b>36.40</b>	63.94	<b>29.98</b>	<b>29.80</b>	<b>40.03</b>
Top-2	Top-2	2.0	6.2k	35.39	64.58	29.98	29.33	39.82
	Top-2+IR		6.0k	35.70	64.40	<b>30.47</b>	29.95	40.13
	Top-2+FR		5.8k	35.96	<b>65.37</b>	30.35	30.26	40.48
	Top-(2+1)+IR		5.5k	<b>36.14</b>	65.16	30.35	<b>31.49</b>	<b>40.78</b>

Table 2: The performance of applying Intra-GPU Rectification and Fill-in Rectification only at inference. All models are trained with the vanilla top-1 router and top-2 router (referred to as the train router), but they were evaluated with Intra-GPU Rectification or Fill-in Rectification at inference (referred to as the test router). Test CF denotes the capacity factor set during inference. Test speed represents the number of tokens processed per second on each GPU during inference.

effective for the top-2 router as well. Just as we observed with the top-1 routing results, combining the Intra-GPU Rectification and the Fill-in Rectification in the top-2 router yielded the best performance on all benchmarks. Specifically, the Top-2+FR+IR outperformed the vanilla top-2 router by a margin of 0.75 (1.8%) in terms of the average accuracy across benchmarks.

Interestingly, we observed that the top-1 router outperformed the top-2 router in some benchmarks. For example, Top-1+FR+IR outperforms Top-1+FR+IR on both TruthfulQA and LogiQA, which raises concerns about potential overfitting in the top-2 router. Finally, it is important to note that that both the Intra-GPU Rectification and Fill-in Rectification do not alter the capacity of experts, hence they do not significantly influence the training speed.

### 5.3 Improve Top- $k$ Routing at Inference

In this experiment, we conducted a study to evaluate the effectiveness of applying Rectify-Routers at the inference stage of MoE models. The results are presented in Table 2. We found that both the Intra-GPU Rectification and Fill-in Rectification can improve the performance of top-1 and top-2 routers at inference, even they are not applied at training. Similar to the results in Table 1, combining Intra-GPU Rectification and Fill-in Rectification yielded better results than using either method alone. Moreover, both the Intra-GPU Rectification and Fill-in Rectification only slightly slows down (<10%) the inference speed of top- $k$  routers.

Comparing the results of Table 1 and Table 2, we observed that using the Rectify-Routers (Intra-GPU Rectification and Fill-in Rectification) at both

training and inference is better than only applying them at inference for top-1 based models. Conversely, for top-2 based models, the application of Rectify-Routers solely during the inference stage proves to be sufficient, as it demonstrates comparable performance to using them during both training and inference. Therefore, we recommend training Rectify-Routers for the top-1 router and directly applying them at inference for the top-2-router.

### 5.4 Capacity Factor Variation

In the previous experiments, we maintained a fixed capacity factor of  $k$  for top- $k$  routing, as it is a common practice. However, there are instances where it may be beneficial to adjust the capacity factor for improved efficiency or performance. Therefore, in this section, we examine the performance of our Rectify-Routers under different capacity factors. We anticipate that the Intra-GPU Rectification will be more effective with a lower capacity factor, as it deals with a larger number of dropped tokens. On the other hand, we expect the Fill-in Rectification to perform better with a higher capacity factor, as it introduces more padding. We validate these hypotheses in Table 3 and Table 4, respectively. To minimize training costs, we train MoE models using the vanilla top- $k$  router with a capacity factor of  $k$ , and evaluate models with different capacity factors. We only present the average accuracy of models in Table 3 and Table 4. The complete results are shown in Appendix D

**Post Routing with Low Capacity** From Table 4, we can see that decreasing the capacity factor improves the efficiency of both top-1 and top-2 based models. However, It also leads to noticeable decrease in the model performance on benchmarks. It

Train Router	Test Router	Test CF	Test Speed	Avg
Top-1	Top-1	1.0	9.4k	38.74
	Top-1+IR		9.2k	<b>39.97</b>
	Top-1	0.75	12.1k	37.83
	Top-1+IR		9.9k	<b>40.06</b>
	Top-1	0.5	16k	34.84
	Top-1+IR		10.6k	<b>40.40</b>
Top-2	Top-2	2.0	6.2k	39.82
	Top-2+IR		6.0k	<b>40.13</b>
	Top-2	1.5	7.4k	39.50
	Top-2+IR		6.6k	<b>39.60</b>
	Top-2	1.0	8.9k	38.51
	Top-2+IR		7.3k	<b>40.01</b>

Table 3: Performance of top- $k$  routers and their variants with low capacity factors ( $\leq k$ ).

Train Router	Test Router	Test CF	Test Speed	Avg
Top-1	Top-1	1.0	9.4k	<b>38.74</b>
	Top-1+FR		8.9k	38.51
	Top-1	1.25	8.6k	39.59
	Top-1+FR		8.1k	<b>40.10</b>
	Top-1	1.5	7.9k	39.86
	Top-1+FR		7.3k	<b>40.33</b>
Top-2	Top-2	2.0	6.2k	39.82
	Top-2+FR		5.8k	<b>40.48</b>
	Top-2	2.5	5.4k	39.89
	Top-2+FR		5.1k	<b>40.51</b>
	Top-2	3.0	4.9k	40.03
	Top-2+FR		4.5k	<b>40.44</b>

Table 4: Performance of top- $k$  routers and their variants with high capacity factors ( $\geq k$ ).

is interesting that the top-2 router is more robust to the decrease in capacity factor. Specifically, reducing the capacity factor of the vanilla top-2 router from 2 to 1.5 only results in a slight performance decline (0.32).

In contrast to the vanilla top-1 or top-2 routers, the MoE models incorporating our Intra-GPU Rectification (Top-1+IR and Top-2+IR) are robust to the decrease of capacity factor. We even observed that the lower capacity factor leads to a better performance for both Top-1+IR and Top-2+IR, which suggests that the Intra-GPU Rectification acts as a form of regularization for the MoE models by constraining the choices made by the experts. The similar results are also observed in [Zeng and Xiong \(2023\)](#); [Liu et al. \(2022\)](#). By setting the capacity factor of Top-1+IR to 0.5 and that of Top-2+IR to 1.0, we observed that they are faster than the vanilla top-1 (1.13x) and top-2 routers (1.18x) respectively, while maintaining comparable or superior performance.

**Fill-in Rectification with High Capacity** Increasing the capacity factor of MoE models has been widely suggested in previous research studies ([Fedus et al., 2022](#); [Zoph et al., 2022](#)). In alignment with these findings, we have also observed the benefits of increasing the capacity factor in terms of improving model performance, as demonstrated in Table 4. Notably, we have found that increasing the capacity factor of the top-1 router leads to a more substantial improvement in model performance than that of the top-2 router.

Our Fill-in Rectification introduces a more significant and consistent improvement with the in-

crease in capacity factor. Top-1+FR and Top-2+FR consistently outperform Top-1 and Top-2, respectively, across various capacity factor settings.

Through our empirical validation, we have confirmed our hypothesis that decreasing the capacity factor benefits Intra-GPU Rectification, while increasing the capacity factor enhances the Fill-in Rectification. By combining the Intra-GPU Rectification and the Fill-in Rectification, we have achieved a model that is robust to both high and low capacity settings.

## 5.5 Other Experiments

1) We scale the number of experts from 8 to 32 in Appendix B; 2) We analyze the impact of experts-GPUs distribution on our Intra-GPU Rectification in Appendix C.1; 3) We validate the importance of straight-through trick in Appendix C.2; 4) We explore whether our methods is still effective without load-balance loss in Appendix C.3.

## 6 Conclusion

In this paper, we present the Rectify-Router, a method to tackle dropped tokens and padding in MoE models. By introducing the Intra-GPU Rectification and the Fill-in Rectification, we effectively handle the issues of dropped tokens and padding, respectively. Experimental results demonstrate the individual effectiveness of both techniques and the synergistic performance improvement when they are combined. Furthermore, our methods prove to be effective in diverse settings, including varying numbers of experts, different expert capacities, and even without the load-balance loss.

## 7 Limitation

In this paper, we propose Rectify-Router to tackle the issues of dropped tokens and padding in the top- $k$  router. The effectiveness of our methods has been demonstrated through experiments. But our experiments still have the following limitations due to the expensive training cost:

1. The MoE models trained in this work are initialized from a dense model (LLama2-7b). We have not validated our methods by training from scratch.
2. Our experiments are conducted based on LLama2-7b, while the other settings like LLama2-70B have not been explored.

These limitations highlight potential areas for future research and expansion of our work.

## References

Alex Andonian, Quentin Anthony, Stella Biderman, Sid Black, Preetham Gali, Leo Gao, Eric Hallahan, Josh Levy-Kramer, Connor Leahy, Lucas Nestler, Kip Parker, Michael Pieler, Shivanshu Purohit, Tri Songz, Wang Phil, and Samuel Weinbach. 2021. [GPT-NeoX: Large Scale Autoregressive Language Modeling in PyTorch](#).

Yoshua Bengio, Nicholas Léonard, and Aaron C. Courville. 2013. [Estimating or propagating gradients through stochastic neurons for conditional computation](#). *CoRR*, abs/1308.3432.

Damai Dai, Chengqi Deng, Chenggang Zhao, R. X. Xu, Huazuo Gao, Deli Chen, Jiashi Li, Wangding Zeng, Xingkai Yu, Y. Wu, Zhenda Xie, Y. K. Li, Panpan Huang, Fuli Luo, Chong Ruan, Zhifang Sui, and Wenfeng Liang. 2024. [Deepseekmoe: Towards ultimate expert specialization in mixture-of-experts language models](#). *CoRR*, abs/2401.06066.

Nan Du, Yanping Huang, Andrew M. Dai, Simon Tong, Dmitry Lepikhin, Yuanzhong Xu, Maxim Krikun, Yanqi Zhou, Adams Wei Yu, Orhan Firat, Barret Zoph, Liam Fedus, Maarten P. Bosma, Zongwei Zhou, Tao Wang, Yu Emma Wang, Kellie Webster, Marie Pellat, Kevin Robinson, Kathleen S. Meier-Hellstern, Toju Duke, Lucas Dixon, Kun Zhang, Quoc V. Le, Yonghui Wu, Zhifeng Chen, and Claire Cui. 2022. [Glam: Efficient scaling of language models with mixture-of-experts](#). In *International Conference on Machine Learning, ICML 2022, 17-23 July 2022, Baltimore, Maryland, USA*, volume 162 of *Proceedings of Machine Learning Research*, pages 5547–5569. PMLR.

William Fedus, Barret Zoph, and Noam Shazeer. 2022. [Switch transformers: Scaling to trillion parameter](#)

[models with simple and efficient sparsity](#). *J. Mach. Learn. Res.*, 23:120:1–120:39.

Trevor Gale, Deepak Narayanan, Cliff Young, and Matei Zaharia. 2022. [Megablocks: Efficient sparse training with mixture-of-experts](#). *CoRR*, abs/2211.15841.

Leo Gao, Jonathan Tow, Baber Abbasi, Stella Biderman, Sid Black, Anthony DiPofi, Charles Foster, Laurence Golding, Jeffrey Hsu, Alain Le Noac’h, Haonan Li, Kyle McDonell, Niklas Muennighoff, Chris Ociepa, Jason Phang, Laria Reynolds, Hailey Schoelkopf, Aviya Skowron, Lintang Sutawika, Eric Tang, Anish Thite, Ben Wang, Kevin Wang, and Andy Zou. 2023. [A framework for few-shot language model evaluation](#).

Albert Q. Jiang, Alexandre Sablayrolles, Antoine Roux, Arthur Mensch, Blanche Savary, Chris Bamford, Devendra Singh Chaplot, Diego de Las Casas, Emma Bou Hanna, Florian Bressand, Gianna Lengyel, Guillaume Bour, Guillaume Lample, Léo Renard Lavaud, Lucile Saulnier, Marie-Anne Lachaux, Pierre Stock, Sandeep Subramanian, Sophia Yang, Szymon Antoniak, Teven Le Scao, Théophile Gervet, Thibaut Lavril, Thomas Wang, Timothée Lacroix, and William El Sayed. 2024. [Mixture of experts](#). *CoRR*, abs/2401.04088.

Diederik P. Kingma and Jimmy Ba. 2015. [Adam: A method for stochastic optimization](#). In *3rd International Conference on Learning Representations, ICLR 2015, San Diego, CA, USA, May 7-9, 2015, Conference Track Proceedings*.

Yeskendir Koishkenov, Vassilina Nikoulina, and Alexandre Berard. 2022. [Memory-efficient NLLB-200: language-specific expert pruning of a massively multilingual machine translation model](#). *CoRR*, abs/2212.09811.

Aran Komatsuzaki, Joan Puigcerver, James Lee-Thorp, Carlos Riquelme Ruiz, Basil Mustafa, Joshua Ainslie, Yi Tay, Mostafa Dehghani, and Neil Houlsby. 2023. [Sparse upcycling: Training mixture-of-experts from dense checkpoints](#). In *The Eleventh International Conference on Learning Representations, ICLR 2023, Kigali, Rwanda, May 1-5, 2023*. OpenReview.net.

Dmitry Lepikhin, Hyoungho Lee, Yuanzhong Xu, Dehao Chen, Orhan Firat, Yanping Huang, Maxim Krikun, Noam Shazeer, and Zhifeng Chen. 2021. [Gshard: Scaling giant models with conditional computation and automatic sharding](#). In *9th International Conference on Learning Representations, ICLR 2021, Virtual Event, Austria, May 3-7, 2021*. OpenReview.net.

Mike Lewis, Shruti Bhosale, Tim Dettmers, Naman Goyal, and Luke Zettlemoyer. 2021. [BASE layers: Simplifying training of large, sparse models](#). In *Proceedings of the 38th International Conference on Machine Learning, ICML 2021, 18-24 July 2021, Virtual Event, volume 139 of Proceedings of Machine Learning Research*, pages 6265–6274. PMLR.

709	Haonan Li, Yixuan Zhang, Fajri Koto, Yifei Yang,	Tim Brooks, Miles Brundage, Kevin Button, Trevor	767
710	Hai Zhao, Yeyun Gong, Nan Duan, and Timothy	Cai, Rosie Campbell, Andrew Cann, Brittany Carey,	768
711	Baldwin. 2023. <a href="#">CMMLU: measuring massive mul-</a>	Chelsea Carlson, Rory Carmichael, Brooke Chan,	769
712	<a href="#">titask language understanding in chinese.</a> <i>CoRR</i> ,	Che Chang, Fotis Chantzis, Derek Chen, Sully Chen,	770
713	<a href="#">abs/2306.09212</a> .	Ruby Chen, Jason Chen, Mark Chen, Ben Chess,	771
714	Wing Lian, Bleys Goodson, Eugene Pentland, Austin	Chester Cho, Casey Chu, Hyung Won Chung, Dave	772
715	Cook, Chanvichet Vong, and "Teknium". 2023.	Cummings, Jeremiah Currier, Yunxing Dai, Cory	773
716	Openorca: An open dataset of gpt augmented	Decareaux, Thomas Degry, Noah Deutsch, Damien	774
717	flan reasoning traces. <a href="https://https://huggingface.co/Open-Orca/OpenOrca">https://https://</a>	Deville, Arka Dhar, David Dohan, Steve Dowling,	775
718	<a href="https://https://huggingface.co/Open-Orca/OpenOrca">huggingface.co/Open-Orca/OpenOrca</a> .	Sheila Dunning, Adrien Ecoffet, Atty Eleti,	776
719	Stephanie Lin, Jacob Hilton, and Owain Evans. 2022.	Tyna Eloundou, David Farhi, Liam Fedus, Niko	777
720	<a href="#">Truthfulqa: Measuring how models mimic human</a>	Felix, Simón Posada Fishman, Juston Forte, Is-	778
721	<a href="#">falsehoods.</a> In <i>Proceedings of the 60th Annual Meet-</i>	abella Fulford, Leo Gao, Elie Georges, Christian	779
722	<i>ing of the Association for Computational Linguistics</i>	Gibson, Vik Goel, Tarun Gogineni, Gabriel Goh,	780
723	<i>(Volume 1: Long Papers), ACL 2022, Dublin, Ireland,</i>	Rapha Gontijo-Lopes, Jonathan Gordon, Morgan	781
724	<i>May 22-27, 2022</i> , pages 3214–3252. Association for	Grafstein, Scott Gray, Ryan Greene, Joshua Gross,	782
725	Computational Linguistics.	Shixiang Shane Gu, Yufei Guo, Chris Hallacy, Jesse	783
726	Jian Liu, Leyang Cui, Hanmeng Liu, Dandan Huang,	Han, Jeff Harris, Yuchen He, Mike Heaton, Jo-	784
727	Yile Wang, and Yue Zhang. 2020. <a href="#">Logiqa: A chal-</a>	hannes Heidecke, Chris Hesse, Alan Hickey, Wade	785
728	<a href="#">lenge dataset for machine reading comprehension</a>	Hickey, Peter Hoeschele, Brandon Houghton, Kenny	786
729	<a href="#">with logical reasoning.</a> In <i>Proceedings of the Twenty-</i>	Hsu, Shengli Hu, Xin Hu, Joost Huizinga, Shantanu	787
730	<i>Ninth International Joint Conference on Artificial</i>	Jain, Shawn Jain, Joanne Jang, Angela Jiang, Roger	788
731	<i>Intelligence, IJCAI 2020</i> , pages 3622–3628. <a href="#">ijcai.org</a> .	Jiang, Haozhun Jin, Denny Jin, Shino Jomoto, Billie	789
732	Rui Liu, Young Jin Kim, Alexandre Muzio, and Hany	Jonn, Heewoo Jun, Tomer Kaftan, Łukasz Kaiser,	790
733	Hassan. 2022. <a href="#">Gating dropout: Communication-</a>	Ali Kamali, Ingmar Kanitscheider, Nitish Shirish	791
734	<a href="#">efficient regularization for sparsely activated trans-</a>	Keskar, Tabarak Khan, Logan Kilpatrick, Jong Wook	792
735	<a href="#">formers.</a> In <i>International Conference on Machine</i>	Kim, Christina Kim, Yongjik Kim, Hendrik Kirchner,	793
736	<i>Learning, ICML 2022, 17-23 July 2022, Baltimore,</i>	Jamie Kiros, Matt Knight, Daniel Kokotajlo,	794
737	<i>Maryland, USA</i> , volume 162 of <i>Proceedings of</i>	Łukasz Kondraciuk, Andrew Kondrich, Aris Kon-	795
738	<i>Machine Learning Research</i> , pages 13782–13792.	stantinidis, Kyle Kopic, Gretchen Krueger, Vishal	796
739	PMLR.	Kuo, Michael Lampe, Ikai Lan, Teddy Lee, Jan	797
740	Shayne Longpre, Le Hou, Tu Vu, Albert Webson,	Leike, Jade Leung, Daniel Levy, Chak Ming Li,	798
741	Hyung Won Chung, Yi Tay, Denny Zhou, Quoc V. Le,	Rachel Lim, Molly Lin, Stephanie Lin, Mateusz	799
742	Barret Zoph, Jason Wei, and Adam Roberts. 2023.	Litwin, Theresa Lopez, Ryan Lowe, Patricia Lue,	800
743	<a href="#">The flan collection: Designing data and methods for</a>	Anna Makanju, Kim Malfacini, Sam Manning, Todor	801
744	<a href="#">effective instruction tuning.</a>	Markov, Yaniv Markovski, Bianca Martin, Katie	802
745	Subhabrata Mukherjee, Arindam Mitra, Ganesh Jawa-	Mayer, Andrew Mayne, Bob McGrew, Scott Mayer	803
746	har, Sahaj Agarwal, Hamid Palangi, and Ahmed	McKinney, Christine McLeavey, Paul McMillan,	804
747	Awadallah. 2023. <a href="#">Orca: Progressive learning from</a>	Jake McNeil, David Medina, Aalok Mehta, Jacob	805
748	<a href="#">complex explanation traces of GPT-4.</a> <i>CoRR</i> ,	Menick, Luke Metz, Andrey Mishchenko, Pamela	806
749	<a href="#">abs/2306.02707</a> .	Mishkin, Vinnie Monaco, Evan Morikawa, Daniel	807
750	Basil Mustafa, Carlos Riquelme, Joan Puigcerver,	Mossing, Tong Mu, Mira Murati, Oleg Murk, David	808
751	Rodolphe Jenatton, and Neil Houlsby. 2022. <a href="#">Multi-</a>	Mély, Ashvin Nair, Reiichiro Nakano, Rajeev Nayak,	809
752	<a href="#">modal contrastive learning with limoe: the language-</a>	Arvind Neelakantan, Richard Ngo, Hyeonwoo Noh,	810
753	<a href="#">image mixture of experts.</a> In <i>Advances in Neural</i>	Long Ouyang, Cullen O’Keefe, Jakub Pachocki, Alex	811
754	<i>Information Processing Systems 35: Annual Confer-</i>	Paino, Joe Palermo, Ashley Pantuliano, Giambat-	812
755	<i>ence on Neural Information Processing Systems 2022,</i>	tista Parascandolo, Joel Parish, Emy Parparita, Alex	813
756	<i>NeurIPS 2022, New Orleans, LA, USA, November 28</i>	Passos, Mikhail Pavlov, Andrew Peng, Adam Perel-	814
757	<i>- December 9, 2022.</i>	man, Filipe de Avila Belbute Peres, Michael Petrov,	815
758	OpenAI, :, Josh Achiam, Steven Adler, Sandhini Agar-	Henrique Ponde de Oliveira Pinto, Michael, Poko-	816
759	wal, Lama Ahmad, Ilge Akkaya, Florencia Leoni Ale-	rny, Michelle Pokrass, Vitchyr Pong, Tolly Pow-	817
760	man, Diogo Almeida, Janko Altschmidt, Sam Alt-	ell, Alethea Power, Boris Power, Elizabeth Proehl,	818
761	man, Shyamal Anadkat, Red Avila, Igor Babuschkin,	Raul Puri, Alec Radford, Jack Rae, Aditya Ramesh,	819
762	Suchir Balaji, Valerie Balcom, Paul Baltescu, Haim-	Cameron Raymond, Francis Real, Kendra Rimbach,	820
763	ing Bao, Mo Bavarian, Jeff Belgum, Irwan Bello,	Carl Ross, Bob Rotsted, Henri Roussez, Nick Ry-	821
764	Jake Berdine, Gabriel Bernadett-Shapiro, Christo-	der, Mario Saltarelli, Ted Sanders, Shibani Santurkar,	822
765	pher Berner, Lenny Bogdonoff, Oleg Boiko, Made-	Girish Sastry, Heather Schmidt, David Schnurr, John	823
766	laine Boyd, Anna-Luisa Brakman, Greg Brockman,	Schulman, Daniel Selsam, Kyla Sheppard, Toki	824
		Sherbakov, Jessica Shieh, Sarah Shoker, Pranav	825
		Shyam, Szymon Sidor, Eric Sigler, Maddie Simens,	826
		Jordan Sitkin, Katarina Slama, Ian Sohl, Benjamin	827
		Sokolowsky, Yang Song, Natalie Staudacher, Fe-	828
		lipe Petroski Such, Natalie Summers, Ilya Sutskever,	829

830	Jie Tang, Nikolas Tezak, Madeleine Thompson, Phil Tillet, Amin Tootoonchian, Elizabeth Tseng, Preston Tuggle, Nick Turley, Jerry Tworek, Juan Felipe Cerón Uribe, Andrea Vallone, Arun Vijayvergiya, Chelsea Voss, Carroll Wainwright, Justin Jay Wang, Alvin Wang, Ben Wang, Jonathan Ward, Jason Wei, CJ Weinmann, Akila Welihinda, Peter Welinder, Jiayi Weng, Lilian Weng, Matt Wiethoff, Dave Willner, Clemens Winter, Samuel Wolrich, Hannah Wong, Lauren Workman, Sherwin Wu, Jeff Wu, Michael Wu, Kai Xiao, Tao Xu, Sarah Yoo, Kevin Yu, Qiming Yuan, Wojciech Zaremba, Rowan Zellers, Chong Zhang, Marvin Zhang, Shengjia Zhao, Tianhao Zheng, Juntang Zhuang, William Zhuk, and Barret Zoph. 2023. <a href="#">Gpt-4 technical report</a> .	888
831		889
832		890
833		891
834		892
835		893
836		894
837		895
838		896
839		897
840		898
841		899
842		900
843		901
844		902
845	Myle Ott, Sergey Edunov, Alexei Baevski, Angela Fan, Sam Gross, Nathan Ng, David Grangier, and Michael Auli. 2019. <a href="#">fairseq: A fast, extensible toolkit for sequence modeling</a> . In <i>Proceedings of the 2019 Conference of the North American Chapter of the Association for Computational Linguistics: Human Language Technologies, NAACL-HLT 2019, Minneapolis, MN, USA, June 2-7, 2019, Demonstrations</i> , pages 48–53. Association for Computational Linguistics.	903
846		904
847		905
848		906
849		907
850		908
851		909
852		910
853		911
854	Joan Puigcerver, Carlos Riquelme, Basil Mustafa, and Neil Houlsby. 2023. <a href="#">From sparse to soft mixtures of experts</a> . <i>CoRR</i> , abs/2308.00951.	912
855		913
856		914
857	Samyam Rajbhandari, Conglong Li, Zhewei Yao, Minjia Zhang, Reza Yazdani Aminabadi, Ammar Ahmad Awan, Jeff Rasley, and Yuxiong He. 2022. <a href="#">DeepSpeed-MoE: Advancing mixture-of-experts inference and training to power next-generation AI scale</a> . In <i>International Conference on Machine Learning, ICML 2022, 17-23 July 2022, Baltimore, Maryland, USA</i> , volume 162 of <i>Proceedings of Machine Learning Research</i> , pages 18332–18346. PMLR.	915
858		916
859		917
860		918
861		919
862		920
863		921
864		922
865		923
866		924
867	Noam Shazeer, Azalia Mirhoseini, Krzysztof Maziarz, Andy Davis, Quoc V. Le, Geoffrey E. Hinton, and Jeff Dean. 2017. <a href="#">Outrageously large neural networks: The sparsely-gated mixture-of-experts layer</a> . In <i>5th International Conference on Learning Representations, ICLR 2017, Toulon, France, April 24-26, 2017, Conference Track Proceedings</i> . OpenReview.net.	925
868		926
869		927
870		928
871		929
872		930
873		931
874	Sheng Shen, Le Hou, Yanqi Zhou, Nan Du, Shayne Longpre, Jason Wei, Hyung Won Chung, Barret Zoph, William Fedus, Xinyun Chen, Tu Vu, Yuxin Wu, Wuyang Chen, Albert Webson, Yunxuan Li, Vincent Zhao, Hongkun Yu, Kurt Keutzer, Trevor Darrell, and Denny Zhou. 2023. <a href="#">Mixture-of-experts meets instruction tuning: a winning combination for large language models</a> .	932
875		933
876		934
877		935
878		936
879		937
880		938
881		939
882	Hugo Touvron, Louis Martin, Kevin Stone, Peter Albert, Amjad Almahairi, Yasmine Babaei, Nikolay Bashlykov, Soumya Batra, Prajwal Bhargava, Shruti Bhosale, Dan Bikel, Lukas Blecher, Cristian Canton-Ferrer, Moya Chen, Guillem Cucurull, David Esiobu, Jude Fernandes, Jeremy Fu, Wenyin Fu, Brian Fuller,	940
883		941
884		942
885		943
886		944
887		945
	Cynthia Gao, Vedanuj Goswami, Naman Goyal, Anthony Hartshorn, Saghar Hosseini, Rui Hou, Hakan Inan, Marcin Kardas, Viktor Kerkez, Madian Khabsa, Isabel Kloumann, Artem Korenev, Punit Singh Koura, Marie-Anne Lachaux, Thibaut Lavril, Jenya Lee, Diana Liskovich, Yinghai Lu, Yuning Mao, Xavier Martinet, Todor Mihaylov, Pushkar Mishra, Igor Molybog, Yixin Nie, Andrew Poulton, Jeremy Reizenstein, Rashi Rungta, Kalyan Saladi, Alan Schelten, Ruan Silva, Eric Michael Smith, Ranjan Subramanian, Xiaoqing Ellen Tan, Binh Tang, Ross Taylor, Adina Williams, Jian Xiang Kuan, Puxin Xu, Zheng Yan, Iliyan Zarov, Yuchen Zhang, Angela Fan, Melanie Kambadur, Sharan Narang, Aurélien Rodriguez, Robert Stojnic, Sergey Edunov, and Thomas Scialom. 2023. <a href="#">Llama 2: Open foundation and fine-tuned chat models</a> . <i>CoRR</i> , abs/2307.09288.	946
	Alex Wang, Yada Pruksachatkun, Nikita Nangia, Amanpreet Singh, Julian Michael, Felix Hill, Omer Levy, and Samuel R. Bowman. 2019. <a href="#">Superglue: A stickier benchmark for general-purpose language understanding systems</a> . In <i>Advances in Neural Information Processing Systems 32: Annual Conference on Neural Information Processing Systems 2019, NeurIPS 2019, December 8-14, 2019, Vancouver, BC, Canada</i> , pages 3261–3275.	947
	Ping Yu, Mikel Artetxe, Myle Ott, Sam Shleifer, Hongyu Gong, Ves Stoyanov, and Xian Li. 2022. <a href="#">Efficient language modeling with sparse all-mlp</a> . <i>CoRR</i> , abs/2203.06850.	948
	Zhiyuan Zeng and Deyi Xiong. 2023. <a href="#">Scomoe: Efficient mixtures of experts with structured communication</a> . In <i>The Eleventh International Conference on Learning Representations, ICLR 2023, Kigali, Rwanda, May 1-5, 2023</i> . OpenReview.net.	949
	Yanqi Zhou, Tao Lei, Hanxiao Liu, Nan Du, Yanping Huang, Vincent Zhao, Andrew M. Dai, Zhifeng Chen, Quoc V. Le, and James Laudon. 2022. <a href="#">Mixture-of-experts with expert choice routing</a> . In <i>Advances in Neural Information Processing Systems 35: Annual Conference on Neural Information Processing Systems 2022, NeurIPS 2022, New Orleans, LA, USA, November 28 - December 9, 2022</i> .	950
	Barret Zoph, Irwan Bello, Sameer Kumar, Nan Du, Yanping Huang, Jeff Dean, Noam Shazeer, and William Fedus. 2022. <a href="#">St-moe: Designing stable and transferable sparse expert models</a> .	951
	Simiao Zuo, Xiaodong Liu, Jian Jiao, Young Jin Kim, Hany Hassan, Ruofei Zhang, Jianfeng Gao, and Tuo Zhao. 2022. <a href="#">Taming sparsely activated transformer with stochastic experts</a> . In <i>The Tenth International Conference on Learning Representations, ICLR 2022, Virtual Event, April 25-29, 2022</i> . OpenReview.net.	952

## A Gradient Issues in Fill-in Rectification

There is a potential issue in the Fill-in Rectification, which stems from the implementation of the top- $k$  routing. According to Eq. (1), the routing scores of top- $k$  routing are normalized on the selected experts  $\mathbb{R}_i$ , rather than considering all expert choices. Several implementations like deepspeed-moe (Rajbhandari et al., 2022) and fairseq-moe (Ott et al., 2019) first normalize the routing scores on all experts and then re-normalize the scores specifically for the selected experts:

$$g_{ij} = \frac{e^{a_{ij}}}{\sum_j^m e^{a_{ij}}} \quad (4)$$
$$o_i = \sum_{j \in \mathbb{R}_i} \frac{g_{ij}}{\sum_j g_{ij}} E_j(x_i),$$

where  $g_{ij}$  represents the routing scores that are initially normalized across all experts and then further normalized specifically on the selected experts ( $\mathbb{R}_i$ ). However, their implementation is equivalent to directly normalizing the routing scores on  $\mathbb{R}_i$ .

There are two potential issues of normalizing routing scores on  $\mathbb{R}_i$ : 1) the routing scores of activated experts can not influence those of inactivated experts. For example, the increase of  $a_{ij}$  ( $j \in \mathbb{R}_i$ ) does not lead to the decrease of  $a_{il}$  ( $l \notin \mathbb{R}_i$ ). 2) In the case of top-2 routing, if the first routing of  $x_i$  is successful while the second routing fails due to the expert overflow, the gradients of all routing scores of  $x_i$  will be zero ( $\frac{\partial L}{\partial a_{ij}} = 0$ ). This is because that there is only one available expert choice for  $x_i$  ( $|\mathbb{R}_i| = 1$ ). Normalizing on  $|\mathbb{R}_i|$  would always yield a value of 1, regardless of the actual value of  $a_{ij}$ , leading to invalid gradients.

This problem is more prominent for the Fill-in Rectification, since it brings more dropped tokens, i.e., more unsuccessful routing. To address this problem, we utilize the straight-through trick to stop the gradient of normalization item in Eq. (4), which ensures that the gradient of routing scores remain valid:

$$\frac{\partial L}{\partial a_{ij}} \equiv \frac{\partial L}{\sum_j g_{ij} \frac{\partial g_{ij}}{\partial a_{ij}}} \quad (5)$$

No modifications have been made to the forward stage. But at the backward stage, the gradient of the routing score  $\frac{\partial L}{\partial g_{ij}}$  is calculated as  $\frac{\partial L}{\sum_j g_{ij} \frac{\partial g_{ij}}{\partial a_{ij}}}$  rather than 0, where the normalization item  $\sum_j g_{ij}$  is taken as a constant number without gradient.

## B Scaling to 32 Experts

In this experiment, we aimed to investigate the effectiveness of our methods when applied to a larger number of experts. We expanded the number of experts from 8 to 32. To reduce training costs, we only applied the Rectify-Routers (Intra-GPU Rectification and Fill-in Rectification) during evaluation. The results of this experiment are presented in Figure 3.

Interestingly, our findings indicate that increasing the number of experts from 8 to 32 does not necessarily result in improved model performance. In fact, in certain benchmarks, such as SuperGLUE, the performance of the model even declined. This observation aligns with previous research (Komatsuzaki et al., 2023), suggesting that increasing the number of experts can potentially be detrimental. One plausible explanation for this phenomenon is that a larger number of experts may lead to overfitting of the model. We believe that increasing the number of experts is helpful with enough training data. Notably, scaling from 8 to 32 experts only yielded notable benefits in the case of TruthfulQA.

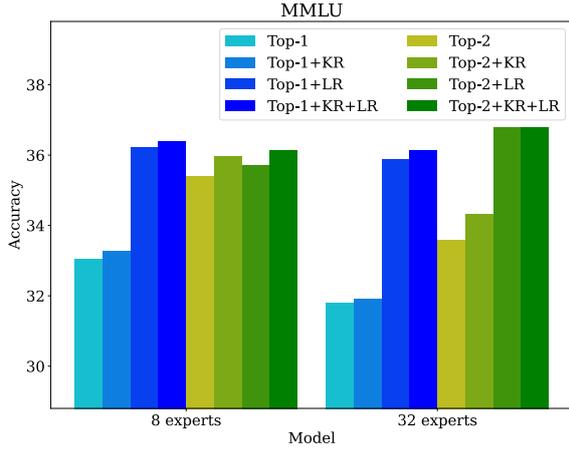
Despite the lack of consistent improvement when increasing the number of experts, our methods (Intra-GPU Rectification and Fill-in Rectification) still demonstrated significant enhancements compared to the vanilla top- $k$  routing approach in the context of 32 experts. For instance, while the vanilla top-1 and top-2 routers with 32 experts underperformed those with 8 experts on MMLU, our methods (Top-2+FR+IR) enabled the 32-expert models to outperform their 8-expert counterparts.

## C Analysis

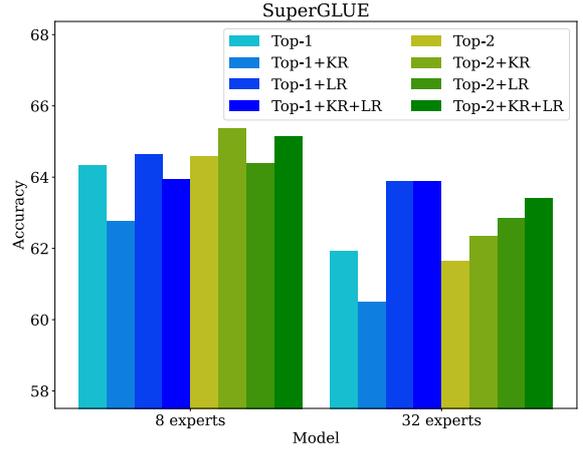
### C.1 Impact of Expert Distribution

Our Intra-GPU Rectification is a variant of the top-1 router, where tokens are assigned to the top-1 expert within GPU. When all experts are situated in the same GPU, the Intra-GPU Rectification essentially functions as the top-1 router. Therefore, the distribution of experts across GPUs can potentially influence the performance of the Intra-GPU Rectification. We conducted an investigation to explore this aspect and present the results in Table 5.

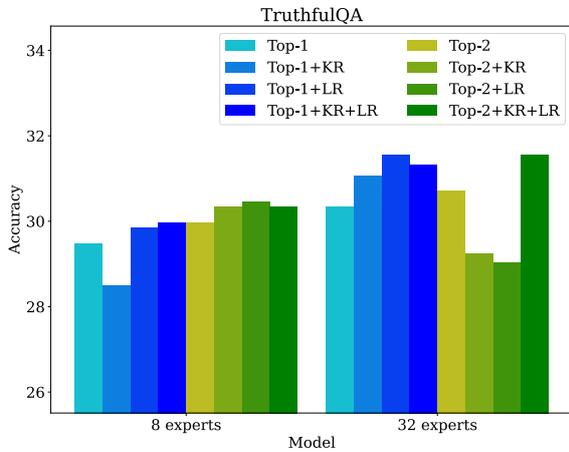
Interestingly, we found that increasing the number of experts per GPU did not yield significant improvements for either the top-1 router or the top-2 router. This suggests that the Intra-GPU Rec-



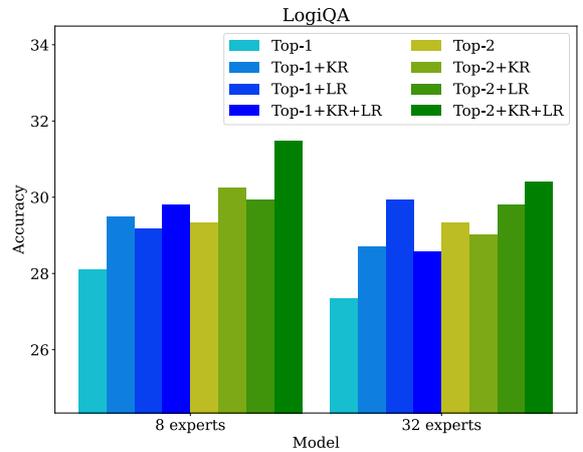
(a) The performance of 8-experts and 32-experts MoEs on MMLU



(b) The performance of 8-experts and 32-experts MoEs on SuperGLUE



(c) The performance of 8-experts and 32-experts MoEs on TruthfulQA



(d) The performance of 8-experts and 32-experts MoEs on LogiQA

Figure 3: The performance of 8-experts and 32-experts MoEs on MMLU, SuperGLUE, TruthfulQA and LogiQA.

tification demonstrates robustness to variations in the number of experts per GPU.

## C.2 Impact of Straight-through Trick

In Appendix A, we propose a solution to address the gradient issue associated with the Fill-in Rectification by utilizing the straight-through trick. To evaluate the effectiveness of this technique, we conducted an experiment comparing the performance of the Fill-in Rectification with versus without the straight-through trick. The results of this comparison are presented in Table 6.

Our findings indicate that the straight-through trick proves to be beneficial in improving the performance of the Fill-in Rectification (Top-1+FR). This suggests that the straight-through trick is necessary for the Fill-in Rectification to achieve optimal results. However, the application of the straight-

through trick does not yield a significant improvement in the performance of the top-2 router. This can be attributed to the fact that the proportion of unsuccessful routing is relatively small (5%) for the top-2 router, while it is considerably large (50%) when employing the Fill-in Rectification.

## C.3 Impact of Load-Balance Loss

The Rectify-Routers proposed in this paper were designed to address the issues of dropped tokens and padding resulting from unbalanced routing. In our previous experiments, we utilized the load-balanced loss introduced by Lepikhin et al. (2021) to enhance the balance of routing for all models, including those utilizing the Rectify-Routers. However, it is intriguing to investigate whether the Rectify-Routers remain effective in the absence of the load-balance loss. The results of this explo-

Router	Experts/GPU	MMLU	SuperGLUE	TruthfulQA	LogiQA	Avg
Top-1+IR	1	36.21	64.64	29.86	29.18	39.97
	2	36.17	64.38	30.23	29.03	39.95
	4	36.47	64.37	29.62	28.87	39.83
Top-2+IR	1	35.70	64.40	30.47	29.95	40.13
	2	35.79	64.73	30.35	29.18	40.01
	4	35.79	65.38	30.35	29.18	40.17

Table 5: The performance of Intra-GPU Rectification evaluated under various settings of the number of experts per GPU.

Model	ST	MMLU	SuperGLUE	TruthfulQA	LogiQA	Avg
Top-1+FR	Yes	<b>34.66</b>	<b>63.97</b>	28.51	<b>29.18</b>	<b>39.08</b>
Top-1+FR	No	33.96	62.75	<b>29.25</b>	28.57	38.63
Top-2	Yes	35.39	64.58	29.98	<b>29.33</b>	<b>39.82</b>
Top-2	No	<b>35.86</b>	<b>64.73</b>	29.98	28.26	39.70

Table 6: The performance of Top-1+FR and Top-2 router with and without straight-through trick. The second column (ST) denotes whether the straight-through trick is used.

ration are presented in Table 7.

Upon analyzing the results in Table 7, we observed a notable disparity in the performance of the vanilla top-1 router with and without the load-balance loss, particularly in the case of SuperGLUE and TruthfulQA. This discrepancy suggests that the load-balance loss plays a crucial role in improving the performance of the vanilla top-1 router. However, when considering our Rectify-Routers (Top-1+FR+IR), removing the load-balance loss does not result in a significant loss of performance. This finding indicates that our Rectify-Routers enhance the resilience of the top-1 router against the load-balance loss. Nevertheless, as a general trend, it is still preferable to employ a load-balance loss, even when utilizing the Rectify-Routers.

## D Complete Results of Capacity Factor Variation

In Section 5.4, we have discussed the performance of MoE models across various capacity factor settings. However, it is worth noting that only the average accuracy are reported in Table 3 and Table 4. For a comprehensive overview, we present the complete results in Table 8 and Table 9, which encompass the evaluation outcomes across all benchmarks.

Model	Aux-loss	MMLU	SuperGLUE	TruthfulQA	LogiQA	Avg
Top-1	yes	33.05	<b>64.34</b>	<b>29.49</b>	28.11	<b>38.74</b>
Top-1	no	<b>33.88</b>	61.60	27.41	<b>29.95</b>	38.21
Top-1+FR+IR	yes	<b>36.40</b>	63.94	29.98	<b>29.80</b>	<b>40.03</b>
Top-1+FR+IR	no	36.09	64.05	<b>31.21</b>	28.57	39.98

Table 7: The performance comparison of using vs. not using load-balance loss. Aux-loss represents whether load-balance loss is used.

Train Router	Test Router	Test CF	Test Speed	MMLU	SuperGLUE	TruthfulQA	LogiQA	Avg
Top-1	Top-1	1.0	9.4k	33.05	<b>64.34</b>	29.49	28.11	38.74
	Top-1+IR		9.2k	<b>36.21</b>	<b>64.64</b>	<b>29.86</b>	<b>29.18</b>	<b>39.97</b>
	Top-1	0.75	12.1k	30.88	62.68	29.37	28.41	37.83
	Top-1+IR		9.9k	<b>36.12</b>	<b>64.61</b>	<b>29.74</b>	<b>29.80</b>	<b>40.06</b>
	Top-1	0.5	16k	26.43	59.71	26.68	26.57	34.84
	Top-1+IR		10.6k	<b>36.32</b>	<b>65.23</b>	<b>29.98</b>	<b>30.10</b>	<b>40.40</b>
Top-2	Top-2	2.0	6.2k	35.39	<b>64.58</b>	29.98	29.33	39.82
	Top-2+IR		6.0k	<b>35.70</b>	64.40	<b>30.47</b>	<b>29.95</b>	<b>40.13</b>
	Top-2	1.5	7.4k	35.22	<b>65.00</b>	<b>30.47</b>	27.34	39.50
	Top-2+IR		6.6k	<b>35.71</b>	64.47	29.98	<b>28.26</b>	<b>39.60</b>
	Top-2	1.0	8.9k	33.28	62.76	28.51	29.49	38.51
	Top-2+IR		7.3k	<b>35.93</b>	<b>64.40</b>	<b>29.62</b>	<b>30.10</b>	<b>40.01</b>

Table 8: Performance of top- $k$  routers and their variants with low capacity factors ( $\leq k$ ). The difference between this table and Table 3 is that the evaluation results on all benchmarks are reported in this table, but only the average accuracy is reported in Table 3

Train Router	Test Router	Test CF	Test Speed	MMLU	SuperGLUE	TruthfulQA	LogiQA	Avg
Top-1	Top-1	1.0	9.4k	33.05	<b>64.34</b>	<b>29.49</b>	28.11	<b>38.74</b>
	Top-1+FR		8.9k	<b>33.28</b>	62.76	28.51	<b>29.49</b>	38.51
	Top-1	1.25	8.6k	34.51	62.94	29.13	<b>31.79</b>	39.59
	Top-1+FR		8.1k	<b>35.01</b>	<b>65.22</b>	<b>30.23</b>	29.95	<b>40.10</b>
	Top-1	1.5	7.9k	<b>36.40</b>	64.21	28.88	29.95	39.86
	Top-1+FR		7.3k	36.10	<b>64.89</b>	<b>30.23</b>	<b>30.10</b>	<b>40.33</b>
Top-2	Top-2	2.0	6.2k	35.39	64.58	29.98	29.33	39.82
	Top-2+FR		5.8k	<b>35.96</b>	<b>65.37</b>	<b>30.35</b>	<b>30.26</b>	<b>40.48</b>
	Top-2	2.5	5.4k	35.96	64.53	30.23	28.87	39.89
	Top-2+FR		5.1k	36.00	<b>64.92</b>	<b>30.59</b>	<b>30.56</b>	<b>40.51</b>
	Top-2	3.0	4.9k	35.72	64.65	<b>30.59</b>	29.18	40.03
	Top-2+FR		4.5k	<b>35.98</b>	<b>65.31</b>	30.23	<b>30.26</b>	<b>40.44</b>

Table 9: Performance of top- $k$  routers and their variants with high capacity factors ( $\geq k$ ). The difference between this table and Table 4 is that the evaluation results on all benchmarks are reported in this table, but only the average accuracy is reported in Table 4