# Expanding Sparse Tuning for Low Memory Usage

Shufan Shen<sup>1,2</sup> Junshu Sun<sup>1,2</sup> Xiangyang Ji<sup>3</sup> Qingming Huang<sup>1,2,4</sup> Shuhui Wang<sup>1,4</sup>\* <sup>1</sup>Key Lab of Intell. Info. Process., Inst. of Comput. Tech., CAS

<sup>2</sup>University of Chinese Academy of Sciences  $3$ Tsinghua University  $4$ Peng Cheng Laboratory {shenshufan22z, sunjunshu21s, wangshuhui}@ict.ac.cn xyji@tsinghua.edu.cn qmhuang@ucas.ac.cn

# Abstract

Parameter-efficient fine-tuning (PEFT) is an effective method for adapting pretrained vision models to downstream tasks by tuning a small subset of parameters. Among PEFT methods, sparse tuning achieves superior performance by only adjusting the weights most relevant to downstream tasks, rather than densely tuning the whole weight matrix. However, this performance improvement has been accompanied by increases in memory usage, which stems from two factors, *i.e.*, the storage of the whole weight matrix as learnable parameters in the optimizer and the additional storage of tunable weight indexes. In this paper, we propose a method named SNELL (Sparse tuning with kerNELized LoRA) for sparse tuning with low memory usage. To achieve low memory usage, SNELL decomposes the tunable matrix for sparsification into two learnable low-rank matrices, saving from the costly storage of the whole original matrix. A competition-based sparsification mechanism is further proposed to avoid the storage of tunable weight indexes. To maintain the effectiveness of sparse tuning with low-rank matrices, we extend the low-rank decomposition by applying nonlinear kernel functions to the whole-matrix merging. Consequently, we gain an increase in the rank of the merged matrix, enhancing the ability of SNELL in adapting the pre-trained models to downstream tasks. Extensive experiments on multiple downstream tasks show that SNELL achieves state-of-the-art performance with low memory usage, endowing PEFT with sparse tuning to large-scale models. Codes are available at [https://github.com/ssfgunner/SNELL.](https://github.com/ssfgunner/SNELL)

# 1 Introduction

Fine-tuning has become a predominant way for adapting large pre-trained models to downstream tasks with limited training samples [\[13,](#page-10-0) [9,](#page-10-1) [24,](#page-11-0) [23\]](#page-11-1). Nevertheless, fine-tuning all model parameters requires substantial memory usage and is susceptible to over-fitting, making it costly and infeasible for largescale models [\[58,](#page-14-0) [2,](#page-10-2) [11\]](#page-10-3). To address these limitations, parameter-efficient fine-tuning (PEFT) [\[64,](#page-14-1) [27,](#page-12-0) [62,](#page-14-2) [30,](#page-12-1) [8,](#page-10-4) [22\]](#page-11-2) has been proposed to tune a small subset of parameters while keeping other parameters frozen. PEFT methods can be categorized into addition-based and reparameterization-based methods. The former attaches additional parameters to a frozen pre-trained backbone, while the latter adjusts the original parameters in the pre-trained backbone.

Addition-based methods [\[53,](#page-13-0) [62,](#page-14-2) [30\]](#page-12-1) have achieved remarkable performance on vision tasks. However, adopting additional parameters incurs extra computational costs during the inference process. In contrast, reparameterization-based methods [\[5,](#page-10-5) [7,](#page-10-6) [27\]](#page-12-0) directly fine-tune the original parameters. These methods select specific parameters, involving reduced memory usage compared to full-parameter fine-tuning. Based on the granularity of parameter selection, one primary approach focuses on

<sup>∗</sup>Corresponding author.

<span id="page-1-0"></span>

Figure 1: (a) The high memory usage of sparse tuning arises from taking the whole weight matrix as learnable parameters, in addition to the storage of the tunable weight indexes (typically represented as a binary mask). (b) Our framework (SNELL) only stores the learnable low-rank matrices in the optimizer. (c) Memory usage comparison on pre-trained models with different depths.

specific parameter matrices. For example, Bitfit [\[5\]](#page-10-5) only adjusts bias to reduce the volume of tunable parameters while Partial-*k* [\[30\]](#page-12-1) fine-tunes the last few layers to avoid back-propagation through the entire pre-trained backbone. To further reduce memory usage, LoRA [\[27\]](#page-12-0) optimizes each selected weight matrix using two low-rank matrices to achieve memory-efficient fine-tuning. Although sufficient in reducing memory usage, these methods usually gain inferior performance compared to addition-based methods [\[30\]](#page-12-1). Recently, SPT [\[22\]](#page-11-2) and GPS [\[63\]](#page-14-3) found that combining existing PEFT methods with sparse tuning, which only adjusts the most task-related weights in a matrix, can achieve state-of-the-art performance on vision tasks. Concurrently, the effectiveness of sparse tuning has also been observed in NLP tasks [\[18\]](#page-11-3). By focusing on individual weights in a matrix, sparse tuning allows for more precise adjustments, thus achieving good performance and mitigated over-fitting risks [\[18\]](#page-11-3).

However, the performance gained from sparse tuning has been accompanied by high memory usage, as Figure [1\(](#page-1-0)a) shows. Although sparse tuning only updates part of weights in the pre-trained weight matrix, the whole matrix still needs to be stored as learnable parameters in the optimizer and computed for their corresponding gradients in practice. Additionally, sparse tuning necessitates storing the tunable weight indexes, further aggravating the memory demands. The above observation indicates that sparse tuning gains no advantage over full fine-tuning regarding memory usage, especially given the increasing parameter volumes in pre-trained models [\[58,](#page-14-0) [2\]](#page-10-2). A sparse tuning method with low memory usage is urgently required for applications on large-scale pre-trained models.

In this paper, we propose a method that conducts Sparse tuning with kerNELized LoRA (SNELL) shown in Figure [1\(](#page-1-0)b). SNELL can adapt pre-trained models to downstream tasks with both low memory usage and strong performance. To reduce memory usage, we decompose the tunable matrix for sparsification into low-rank learnable matrices to store fewer parameters in the optimizer and develop a competition-based method to avoid storing the tunable weight indexes. To improve the performance on downstream tasks, we extend LoRA from a kernel perspective and merge low-rank matrices with nonlinear kernel functions to obtain matrices with higher ranks.

Specifically, SNELL updates the pre-trained weight matrix using a sparse low-rank adaptation matrix. This adaptation matrix is first merged with two low-rank learnable matrices and then sparsified toward effective fine-tuning. Compared to storing the whole adaptation matrix, storing low-rank matrices in the optimizer results in lower memory usage. For the sparsification process, we propose a competition-based method inspired by the neuron competition phenomenon in neuroscience [\[49\]](#page-13-1), avoiding the storage of the tunable weight indexes that incur additional memory usage. The proposed method promotes competition among weights based on their absolute values. Most task-relevant weights are encouraged to have larger absolute values and survive during the fine-tuning process. By setting a sparsity ratio as the hyperparameter and determining tunable weights based on their absolute values in an end-to-end manner, we can eliminate the storage of the tunable weight indexes.

In addition to low memory usage, the performance is also critical for model fine-tuning. However, directly merging two low-rank matrices through the inner product leads to the low-rank structure of the adaptation matrix, which narrows the optimization scope of tunable matrices and further limits the expressiveness of sparse tuning. To overcome this bottleneck, we draw inspiration from DyN [\[45\]](#page-13-2)

on weight matrix interpretation based on low-dimensional dynamical systems, and reformulate the merging process with nonlinear kernel functions that increase the rank of the merged adaptation matrix. This new formulation enables a more expressive sparse tuning while maintaining a compact representation with low memory.

Extensive experiments are conducted on 24 downstream visual recognition tasks with both plain and hierarchical vision Transformer backbones under supervised and self-supervised pre-training. Results show that SNELL can gain the performance improvement of sparse tuning and the low memory usage of LoRA concurrently. SNELL obtains the state-of-the-art performance on FGVC (91.8% *vs.* 90.7%) and VTAB-1k (74.6% *vs.* 74.1%) benchmark with LoRA-level memory usage. Moreover, as Figure [1\(](#page-1-0)c) shows, the low memory-usage advantage of SNELL becomes increasingly apparent as the model size grows, enabling sparse tuning on larger models.

# 2 Related Work

**Parameter Sparsity.** In early work, the parameter sparsity usually serves as an optimization objective in model pruning [\[21,](#page-11-4) [42\]](#page-13-3). These pruning methods remove the weights from pre-trained models irrelevant to a specific task, without significantly degrading model performance. The relevance of individual weights can be estimated based on activations [\[28\]](#page-12-2), redundancy [\[48\]](#page-13-4), per-layer second derivatives [\[15\]](#page-11-5), and energy efficiency [\[57\]](#page-14-4). Except for the post-training pruning strategy, sparse networks [\[4,](#page-10-7) [41,](#page-13-5) [17\]](#page-11-6) directly introduce parameter sparsity into the training process, removing redundant weights more precisely [\[17\]](#page-11-6). Motivated by the advantage of parameter sparsity in model optimization, recent studies introduce sparsity to the fine-tuning of pre-trained models and achieve enhanced model performance on downstream tasks [\[1,](#page-10-8) [21,](#page-11-4) [56\]](#page-14-5). The parameter sparsity gives rise to a reduced number of trainable parameters and serves as a regularization constraint during fine-tuning [\[18\]](#page-11-3). Among sparse tuning, pre-pruning methods adopt model pruning for fine-tuning. These methods sparsify the weight matrix [\[22,](#page-11-2) [63\]](#page-14-3) or adapter [\[1\]](#page-10-8) through pruning metrics [\[43,](#page-13-6) [17\]](#page-11-6) to identify learnable parameters for the fine-tuning process. Other methods select trainable parameters during fine-tuning, including learnable mask [\[64\]](#page-14-1) or diff vectors [\[20\]](#page-11-7) with sparsity constraints. However, the parameter sparsification methods need to store the indexes of tunable weights, which incurs additional memory usage. For sparse tuning under low memory budget, our competition-based mechanism selects weights relevant to downstream tasks in a learnable manner without storing the tunable weight indexes.

Parameter-efficient Fine-tuning. Fine-tuning is the most predominant approach for adapting a pre-trained model to downstream tasks. However, for large pre-trained models, fine-tuning all parameters is costly and prone to overfit downstream datasets. To tackle these problems, parameterefficient fine-tuning (PEFT) [\[8,](#page-10-4) [30,](#page-12-1) [62\]](#page-14-2), which tunes only a tiny portion of parameters, becomes a desirable choice. Following the taxonomy of SPT [\[22\]](#page-11-2), PEFT methods can be categorized into *addition-based* [\[3,](#page-10-9) [26,](#page-11-8) [46,](#page-13-7) [50,](#page-13-8) [14,](#page-11-9) [31,](#page-12-3) [37,](#page-12-4) [62\]](#page-14-2) and *reparameterization-based* [\[5,](#page-10-5) [7,](#page-10-6) [20,](#page-11-7) [64,](#page-14-1) [27\]](#page-12-0) methods.

*Addition-based* methods attach additional trainable parameters to a frozen pre-trained backbone. Adapters [\[3,](#page-10-9) [26,](#page-11-8) [46,](#page-13-7) [50,](#page-13-8) [61\]](#page-14-6) adopt a residual pathway and learn a bottleneck layer including two linear projections and a non-linear activation. Prompt-tuning methods [\[14,](#page-11-9) [31,](#page-12-3) [37,](#page-12-4) [34\]](#page-12-5) add trainable parameters to the input and keep the entire pre-trained model unchanged during training. Recent work [\[62\]](#page-14-2) attempts to find the optimal configurations to combine multiple addition-based methods. Despite of the popularity and effectiveness of *addition-based* methods, the additional trainable parameters incur excess computational costs during the inference process [\[3,](#page-10-9) [33\]](#page-12-6).

*Reparametization-based* methods adjust the inherent parameters in the pre-trained backbone to avoid excess computational costs during inference. Early work directly selects parameters with low memory usage for fine-tuning, such as the bias terms [\[5\]](#page-10-5) and the final few layers of the pre-trained model [\[7\]](#page-10-6). To further reduce the memory usage of the selected matrices, LoRA [\[27\]](#page-12-0) optimizes low-rank matrices that can be reparameterized into the pre-trained weight matrices to reduce memory usage. Exploring finer-grained parameter selection, some studies [\[20,](#page-11-7) [64\]](#page-14-1) propose sparse tuning, which involves selecting and tuning individual weights sparsely within the weight matrices. Recently, SPT [\[22\]](#page-11-2) combines sparse tuning and LoRA in a hybrid framework that achieves state-of-the-art performances on visual PEFT tasks. SPT has revealed that optimizing the weights most relevant to the downstream task through sparse tuning can significantly enhance the performance, which is also supported by SAM [\[18\]](#page-11-3) and GPS [\[63\]](#page-14-3). However, existing sparse tuning framework still faces the challenge of high memory usage brought by sparse tuning. Unlike existing methods, our SNELL inherits high performance and low memory usage concurrently by sparsifying the adaptation matrix merged with low-rank matrices through nonlinear kernels.

# 3 Methodology

We first introduce the definitions of sparse tuning [\[64,](#page-14-1) [20,](#page-11-7) [1\]](#page-10-8), LoRA [\[27\]](#page-12-0) and kernel trick [\[32\]](#page-12-7) (Section [3.1\)](#page-3-0). Then we propose SNELL, a sparse tuning method including kernelized LoRA that enables high-performance tuning with low-rank learnable matrices (Section [3.2\)](#page-3-1) and a competition-based mechanism that sparsifies weights without additional memory usage (Section [3.3\)](#page-4-0).

#### <span id="page-3-0"></span>3.1 Preliminaries

**Sparse Tuning**. Given a downstream training set  $\mathcal{D} = \{x^{(n)}, y^{(n)}\}_{n=1}^N$ , the objective of sparse tuning is to minimize the model's empirical risk on the downstream task, with the sparsity constraints on the volume of tunable weights in weight matrix  $\mathbf{W} \in \mathbb{R}^{m \times n}$ . The sparsification is usually achieved through a binary mask  $\mathbf{M} \in \{0,1\}^{m \times n}$ . The objective function can be formulated as

<span id="page-3-2"></span>
$$
\min_{\mathbf{W}\odot\mathbf{M}}\frac{1}{N}\sum_{n=1}^{N}\mathcal{L}\left(f(x^{(n)};\mathbf{W}),y^{(n)}\right)
$$
\n(1)

where  $f(\cdot; \cdot)$  is a parameterized function over the input (*e.g.*, a neural network),  $\mathcal{L}(\cdot, \cdot)$  is a loss function (*e.g.*, cross-entropy), and  $\odot$  denotes element-wise multiplication. The binary mask M can be either a fixed hyperparameter, pre-computed with heuristics such as pre-pruning [\[22\]](#page-11-2), or a learnable parameter obtained through end-to-end fine-tuning [\[64\]](#page-14-1). All these methods require storing M to determine the tunable weights, which results in additional memory usage. More importantly, the tunable parameters  $W \odot M$  occupy the same amount of memory as the weight matrix W in practice. As a result, the memory usage of sparse tuning is even higher than that of full fine-tuning.

**LoRA**. Given a pre-trained weight matrix  $W_0$ , LoRA [\[27\]](#page-12-0) optimizes two low-rank matrices  $B \in$  $\mathbb{R}^{m \times r}$ ,  $\mathbf{A} \in \mathbb{R}^{n \times r}$  to reduce the memory usage during fine-tuning. The low-rank matrices A and B can be reparameterized into the pre-trained weight  $W_0$ ,

$$
\mathbf{W} = \mathbf{W}_0 + \Delta \mathbf{W} = \mathbf{W}_0 + \mathbf{B} \mathbf{A}^\top
$$
 (2)

With  $r \ll \min(m, n)$ , LoRA can achieve high training efficiency and low memory usage by only optimizing the smaller low-rank matrices.

Kernel Trick [\[32\]](#page-12-7). In many machine learning tasks, mapping the vectors into higher dimensions is frequently used to achieve linear separability [\[51\]](#page-13-9). However, the explicit mapping process incurs significant computational costs. To address this problem, the kernel trick is proposed to efficiently model data relationships in high-dimensional spaces, without the need to explicitly formulate the space. According to Mercer's theorem [\[6\]](#page-10-10), a kernel function  $\kappa : \mathbb{R}^r \times \mathbb{R}^r \to \mathbb{R}$  can express an inner product in some space as  $\kappa(\mathbf{x}, \mathbf{x}') = \phi(\mathbf{x})^\top \phi(\mathbf{x}')$ , if and only if  $\kappa$  is positive semi-definite (Appendix [B\)](#page-16-0).  $\mathbf{x}, \mathbf{x}' \in \mathbb{R}^r$ , and  $\phi : \mathbb{R}^r \to \mathbb{R}^d$  is an implicit feature map. By selecting an appropriate kernel function  $\kappa$ , we can obtain the inner product of two vectors in higher-dimensional space  $\mathbb{R}^d$  ( $d \geq r$ ) without explicitly formulating the feature map  $\phi$ .

#### <span id="page-3-1"></span>3.2 Kernelized LoRA

We leverage LoRA to reduce the memory usage of sparse tuning in light of its low memory usage. An intuitive solution is to sparsify the adaptation matrix  $\Delta W$  composed of the two low-rank matrices. However, the low-rank property of  $\Delta W$  can lead to the performance degradation of sparse tuning. For the original sparse tuning, the weight matrix  $W$  is free of the rank constraint, and weights are independent of each other. Therefore, we can independently select and optimize weights most relevant to the downstream task. For sparse tuning with LoRA, the adaptation matrix  $\Delta W$  with rank r is constrained in  $\mathbb{R}^{r \times (m+n-r)}$ , a subspace of  $\mathbb{R}^{m \times n}$ . When  $r \ll \min(m, n)$ , the weight optimization scope of sparse tuning contracts, hindering its performance on downstream tasks.

To achieve sparse tuning with both strong performance and low memory usage, we propose to construct a high-rank matrix using low-rank matrices. Inspired by DyN [\[45\]](#page-13-2) that fits a high-rank



Figure 2: Overview of our SNELL strategy. Given two learnable low-rank matrices, we merge them using a non-linear kernel function (*left*). This merging process is equivalent to mapping the matrices to higher-rank matrices and then performing matrix multiplication. Then we sparsified this merged adaptation matrix using a competition-based sparsification mechanism (*right*). This mechanism zeros out weights with small absolute values based on the specified percentage of s.

matrix using the distance matrix of a low-dimension dynamical system, we extend the distance function to general kernel functions and investigate LoRA in the kernel perspective. Given two vectors  $x, x' \in \mathbb{R}^r$ , the kernel function  $\kappa(x, x')$  can be formulated as an inner product  $\phi(x)^\top \phi(x')$ with an implicit feature map  $\phi : \mathbb{R}^r \to \mathbb{R}^d$ . The merging process of LoRA can be seen as applying linear kernel function  $\kappa_l(\cdot, \cdot)$  on the rows of the learnable parameters **A** and **B**,

$$
\Delta \mathbf{W}_{ij} = \kappa_l(\mathbf{A}_{i,\cdot}, \mathbf{B}_{j,\cdot}) = \phi_l(\mathbf{B}_{j,\cdot})\phi_l(\mathbf{A}_{i,\cdot})^\top = \mathbf{B}_{j,\cdot}\mathbf{A}_{i,\cdot}^\top,\tag{3}
$$

where  $\mathbf{A}_{i,\cdot}, \mathbf{B}_{j,\cdot} \in \mathbb{R}^r$ ,  $\phi_l : \mathbb{R}^r \to \mathbb{R}^r$  denotes the identity mapping. By replacing  $\kappa_l(\cdot, \cdot)$  with more complex non-linear kernel functions, we can approximate relations in higher-dimensional spaces  $\mathbb{R}^d$  and obtain matrices with rank larger than r. The merged adaptation matrix in SNELL can be represented by

$$
\Delta \mathbf{W} = (\kappa(\mathbf{A}_{i,\cdot}, \mathbf{B}_{j,\cdot}))_{m \times n} = [\phi(\mathbf{B}_{1,\cdot})^{\top}, ..., \phi(\mathbf{B}_{n,\cdot})^{\top}]^{\top} [\phi(\mathbf{A}_{1,\cdot})^{\top}, ..., \phi(\mathbf{A}_{n,\cdot})^{\top}] = \mathbf{B}_{\phi} \mathbf{A}_{\phi}^{\top}.
$$
\n(4)

Note that in practice, explicit computation of  $A_{\phi} \in \mathbb{R}^{n \times d}$  and  $B_{\phi} \in \mathbb{R}^{m \times d}$  is unnecessary.  $\Delta W$  can be directly derived based on A and B with the kernel function  $\kappa$ . By extending LoRA in a kernel perspective, SNELL can build high-rank adaptation matrices based on low-rank learnable matrices, empowering strong sparse tuning with low memory usage. We utilize the piecewise linear kernel introduced in Appendix [B](#page-16-0) without a specific statement.

#### <span id="page-4-0"></span>3.3 Competition-based Sparsification Mechanism

Existing methods store tunable weight indexes  $\mathbf{M} \in \{0,1\}^{m \times n}$  for sparsifying the update of the weight matrix  $\mathbf{W} \in \mathbb{R}^{m \times n}$ . The storage of M leads to additional memory usage. Inspired by the neuron competition phenomenon in neuroscience [\[49\]](#page-13-1), we design a competition-based parameter sparsification mechanism to avoid this additional storage. Instead of determining the learnable weights in the optimization process based on M, our objective is to encourage the weights to compete based on their contributions to performance improvement. Weights with stronger contributions survive in the sparsification while the remaining low-contributed weights are zeroed out. The weight contribution is reflected in their absolute values during the end-to-end optimization. During optimization, weights contributing more to the loss reduction are encouraged to have more significant values, while weights contributing less approach zero. By retaining higher importance to significant weights and zeroing out the less impactful weights, we can achieve end-to-end tunable parameter selection by solely relying on the absolute values of weights, avoiding the storage of M.

Specifically, given a merged adaptation matrix  $\Delta W$  and a sparsity ratio  $s \in [0, 1]$ , we sparsify weights with a soft-threshold function. To induce weight competition during end-to-end fine-tuning, we propose a dynamic threshold  $\Delta w_s$ , *i.e.*, the weight having the  $\lceil smn \rceil$ -th smallest absolute value in  $\Delta$ W. This threshold ensures that only a fixed proportion ( $s \times 100\%$ ) of weights remain non-zero. Therefore, the weights have to compete with each other to be selected instead of just having a larger absolute value than a fixed threshold.

$$
\Delta \mathbf{W}_{ij}^s = \Delta \mathbf{W}_{ij} \max(|\Delta \mathbf{W}_{ij}| - |\Delta w_s|, 0), \tag{5}
$$

where  $\Delta W^s = (\Delta W^s_{ij})_{m \times n}$  denotes the sparse matrix with sparsity ratio s. In practice, the sparsity ratio  $s$  is manually determined regarding specific downstream tasks. Given a sparsity ratio  $s$ , the training objective in Equation [1](#page-3-2) can be reformulated as

$$
\min_{\mathbf{A}, \mathbf{B}} \frac{1}{N} \sum_{n=1}^{N} \mathcal{L}\left(f(x^{(n)}; \mathbf{W}_0 + \Delta \mathbf{W}^s), y^{(n)}\right).
$$
 (6)

This objective encourages weights that are most relevant to the downstream task to gain more significant values for survival. Adjusting the sparsity ratio  $s$  allows us to control the sparsification process precisely and identify the optimal number of tunable parameters for different tasks.

# 4 Experiments

#### <span id="page-5-0"></span>4.1 Experimental Setup

Datasets and Metrics. We evaluate our methods on 24 downstream tasks categorized into two groups following SPT [\[22\]](#page-11-2). *(i) FGVC* [\[30\]](#page-12-1) is a benchmark for fine-grained image classification. This benchmark includes 5 downstream tasks, which are CUB-200-2011 [\[55\]](#page-14-7), NABirds [\[25\]](#page-11-10), Oxford Flowers [\[44\]](#page-13-10), Stanford Dogs [\[19\]](#page-11-11) and Stanford Cars [\[12\]](#page-10-11). We follow the validation splits in [\[22\]](#page-11-2) if the official validation set is unavailable. *(ii) VTAB-1k* [\[59\]](#page-14-8) is a large-scale transfer learning benchmark consisting of 19 visual classification tasks. VTAB-1k can be further divided into three groups, *i.e.*, natural tasks with natural images, specialized tasks with images captured by specialized equipment, and structured tasks with images mostly generated from synthetic environments. We use top-1 accuracy averaged within each group as our main metric following [\[22\]](#page-11-2).

Pre-trained Backbones. We conduct experiments on the plain vision Transformer backbone ViT-B/16 [\[16\]](#page-11-12) that is pre-trained on ImageNet [\[47\]](#page-13-11) with different pre-training strategies following [\[22\]](#page-11-2), including supervised pre-training and self-supervised pre-training with MAE [\[23\]](#page-11-1) and MoCo v3 [\[10\]](#page-10-12). We also conduct experiments on the representative hierarchical vision Transformer backbone Swin-B [\[38\]](#page-12-8) and CNN backbone ConvNeXt-Base [\[39\]](#page-12-9) under supervised pre-training. In addition, we fine-tune the supervised pre-trained large-scale models (ViT-L/16 [\[16\]](#page-11-12), ViT-H/14 [\[16\]](#page-11-12)) on VTAB-1k to demonstrate the memory-efficiency and high-performance of SNELL.

Competitors. We compare our methods with addition-based methods including MLP-*k*, VPT-Shallow [\[30\]](#page-12-1), VPT-Deep [\[30\]](#page-12-1), Adapter-r [\[26\]](#page-11-8), and SPT-Adapter [\[22\]](#page-11-2). For reparameterization-based methods, we compare with Linear, Partial-1, Bias [\[5\]](#page-10-5), LoRA-r [\[27\]](#page-12-0), SSF [\[35\]](#page-12-10), and SPT-LoRA [\[22\]](#page-11-2). Here r represents the number of bottleneck dimensions in Adapter-r and the value of rank in  $LoRA-r$ and our proposed SNELL-r. Details of the competitors are presented in Appendix [A.1.](#page-15-0) We also provide additional comparisons with other approaches [\[63,](#page-14-3) [53\]](#page-13-0) in Appendix [C.1.](#page-17-0)

Implementation Details. Following SPT [\[22\]](#page-11-2), we use the AdamW optimizer [\[40\]](#page-12-11) with cosine learning rate decay. The batch size, learning rate, and weight decay are 32,  $1e - 3$ , and  $1e - 4$ , respectively. We also follow SPT [\[22\]](#page-11-2) to implement the standard data augmentation pipeline for VTAB-1K and follow SSF [\[35\]](#page-12-10) for FGVC as well. SNELL is applied on the pre-trained weight matrix of all linear layers. For each task, we fine-tune the model with different sparsity ratios  $s$  to search the optimal volume of tunable parameters for this task. Without specific stating, we adopt the piecewise linear kernel (introduced in Appendix [B\)](#page-16-0) as the kernel function for SNELL. Ablation studies on different kernel functions are presented in Figure [4.](#page-9-0)

#### 4.2 Performance on Downstream Tasks

Performance on Different Benchmarks. Experiments on FGVC and VTAB-1k benchmarks indicate that SNELL achieves the best performance with supervised pre-trained ViT-B/16 backbone as shown in Table [1.](#page-6-0) SNELL gains large performance improvements over LoRA variants, *e.g.*, SNELL-8 surpasses LoRA-8 significantly by 5.5% in terms of mean accuracy on the FGVC benchmark. Moreover, SNELL outperforms the state-of-the-art method SPT-LoRA by a clear margin of 0.5% in terms of mean top-1 accuracy on the VTAB-1k benchmark. This stems from the fact that SPT-LoRA only performs sparse tuning on a portion of the weight matrices while employing LoRA for the remaining part. In contrast, the low memory property of SNELL empowers sparse tuning on all the weight matrices, allowing for more precise adjustments and giving rise to superior performance.

Method				<b>FGVC</b>				VTAB-1k			
	<b>CUB-200</b>	<b>NABirds</b>	Oxford Flowers	Stanford Dogs	Stanford Cars	Mean Acc.	Natural	Specialized Structured Mean Acc.			
Full	87.3	82.7	98.8	89.4	84.5	88.5	75.9	83.4	47.6	69.0	
					Additional-based methods						
MLP-3 [30] VPT-Shallow [30] VPT-Deep [30] Adapter-8 [26] Adapter-32 [26] SPT-Adapter [22] <b>MoSA</b> [60]	85.1 86.7 88.5 87.3 87.2 89.1 89.3	77.3 78.8 84.2 84.3 84.3 83.3 85.7	97.9 98.4 99.0 98.4 98.5 99.2 99.2	84.9 90.7 90.2 88.8 89.6 91.1 91.9 Reparameter-based methods	53.8 68.7 83.6 68.4 68.4 86.2 83.4	79.8 84.6 89.1 85.5 85.6 89.8 89.9	67.8 76.8 78.5 79.0 79.6 82.0 79.9	72.8 79.7 82.4 84.1 84.0 85.8 84.0	30.6 47.0 55.0 58.5 58.3 61.4 50.3	57.1 67.8 72.0 73.9 74.0 76.4 71.4	
Linear $[30]$ Partial-1 [30] Bias $[5]$ LoRA-8 [27] LoRA-16 [27] SPT-LoRA [22] SSF [35] SNELL-8 (ours)	85.3 85.6 88.4 84.9 85.6 88.6 89.5 89.6	75.9 77.8 84.2 79.0 79.8 83.4 85.7 86.8	97.9 98.2 98.8 98.1 98.9 99.5 99.6 99.3	86.2 85.5 91.2 88.1 87.6 91.4 89.6 92.1	51.3 66.2 79.4 79.8 72.0 87.3 89.2 89.9	79.3 82.6 88.4 86.0 84.8 90.1 90.7 91.5	68.9 69.4 73.3 79.5 79.8 81.9 81.6 82.0	77.2 78.5 78.3 84.6 84.9 85.9 86.6 85.7	26.8 34.2 44.1 60.5 60.2 61.3 59.0 61.6	57.6 60.7 65.2 74.9 75.0 76.4 75.7 76.4	
SNELL-16 (ours) SNELL-32 (ours)	89.9 89.9	87.0 87.0	99.3 99.4	92.2 92.0	90.3 90.5	91.7 91.8	82.4 82.7	86.1 86.1	61.7 61.8	76.7 76.9	

<span id="page-6-0"></span>Table 1: Top-1 accuracy (%) on FGVC and VTAB-1k benchmarks using ViT-B/16 pre-trained on ImageNet-21k supervisedly. The best result is in bold, and the second-best result is underlined.

<span id="page-6-1"></span>Table 2: Top-1 accuracy (%) on VTAB-1k benchmarks using ViT-B/16 backbone pre-trained on ImageNet using MAE and MoCo v3 strategies. The best result is in bold.

Methods			<b>VTAB-1k MAE</b>			VTAB-1k MoCo v3				
	Natural	Specialized	Structured	Mean Acc.	Natural	Specialized	Structured	Mean Acc.		
Full	59.3	79.7	53.8	64.3	72.0	84.7	42.0	69.6		
				Additional-based methods						
Adapter-8 <sup>[26]</sup>	57.2	78.4	54.7	63.4	27.6	70.9	48.4	49.0		
Adapter-32 [26]	55.3	78.8	53.3	62.5	74.2	82.7	47.7	68.2		
VPT-Shallow [30]	40.0	69.7	27.5	45.7	67.3	82.3	37.6	62.4		
VPT-Deep [30]	36.0	60.6	26.6	41.1	70.3	83.0	42.4	65.2		
SPT-Adapter [22]	65.6	82.7	60.7	69.7	76.6	85.0	61.7	74.4		
				Reparameterization-based methods						
Linear $[30]$	18.9	52.7	23.7	32.1	67.5	81.1	30.3	59.6		
Partial-1 [30]	58.4	78.3	47.6	61.5	72.3	84.6	47.9	68.3		
Bias $[5]$	54.6	75.7	47.7	59.3	72.9	81.1	53.4	69.2		
LoRA-8 [27]	57.5	77.7	57.7	64.3	21.2	66.7	45.1	44.3		
LoRA-16 [27]	57.3	77.1	59.9	64.8	16.0	64.0	48.7	42.9		
SPT-LoRA [22]	65.4	82.4	61.5	69.8	76.5	86.0	63.6	75.3		
SNELL-8 (ours)	68.3	83.8	63.5	71.8	76.8	86.0	63.7	75.5		

Performance on Different Pre-training Strategies. Experimental results on models pre-trained using different strategies are presented in Table [2.](#page-6-1) SNELL outperforms the state-of-the-art performances on models pre-trained with MAE (71.8% *vs.* 69.8%) and MoCo v3 (75.5% *vs.* 75.3%). Furthermore, SNELL consistently outperforms other PEFT methods on every group of downstream datasets. This demonstrates the general effectiveness of SNELL under different pre-training strategies.

Performance on Different Architectures. Following VPT [\[30\]](#page-12-1) and SPT [\[22\]](#page-11-2), we apply SNELL to the hierarchal vision transformer Swin-B and the CNN architecture ConvNeXt-Base. Experimental results are shown in Table [3.](#page-7-0) Results on Swin-B demonstrate that SNELL-8 outperforms existing reparameterization-based PEFT methods by 0.3% and achieves comparable performance to the stateof-the-art addition-based method SPT-Adapter. For ConvNeXt-Base, SNELL achieves a performance improvement of 0.4% compared to the best-reported result. These results obtained on different architectures further validate the versatility and effectiveness of our SNELL approach.

Memory Usage Comparison. We illustrate the effectiveness of SNELL in terms of memory usage by comparing it with various PEFT methods. Figure [3\(](#page-7-1)a) shows the accuracy and memory usage of different methods on ViT-B/16. Although some methods achieve satisfactory performance, their memory usage is excessively large, even surpassing that of full fine-tuning (*e.g.* SPT-Adapter and VPT-Deep). In comparison, SNELL achieves superior performance on downstream tasks with memory usage comparable to memory-efficient methods, including LoRA and Adapter.

Methods			VTAB-1k Swin-B				VTAB-1k ConvNeXt-B	
	Natural	Specialized	Structured	Mean Acc.	Natural	Specialized	Structured	Mean Acc.
Full	79.1	86.2	59.7	75.0	78.0	83.7	60.4	74.0
				Additional-based methods				
MLP-3 [30]	73.6	75.2	35.7	61.5	73.8	81.4	35.7	63.6
VPT-Deep [30]	76.8	84.5	53.4	71.6	78.5	83.0	44.6	68.7
Adapter-8 <sup>[26]</sup>	81.7	87.3	61.2	76.7	83.1	84.9	64.6	77.5
SPT-Adapter [22]	83.0	87.3	62.1	77.5	83.7	86.2	65.3	78.4
				Reparameterization-based methods				
Linear $[30]$	73.5	80.8	33.5	62.6	74.5	81.5	34.8	63.6
Partial-1 [30]	73.1	81.7	35.0	63.3	73.8	81.6	39.6	65.0
LoRA-8 [27]	81.7	87.2	60.1	76.3	82.2	84.7	64.1	77.0
SPT-LoRA [22]	83.1	87.4	60.4	77.2	83.4	86.7	65.9	78.7
SNELL-8 (ours)	83.3	87.7	61.4	77.5	84.5	87.4	65.6	79.1

<span id="page-7-0"></span>Table 3: Comparisons on VTAB-1k benchmark with supervised pre-trained Swin-B and ConvNeXt-B. Top-1 accuracy (%) is reported. The best result is in bold.

<span id="page-7-1"></span>

Figure 3: (a) Accuracy *vs.* memory usage (batchsize=64) with supervised pre-trained ViT-B/16 on VTAB-1k. (b) Memory usage evolutions of full fine-tuning, SNELL, and SNELL storing the merged adaptation matrix (SNELL storing  $\Delta W$ ) on ViT-H/14 during fine-tuning (batchsize=8). (c) Model parameter volumes *vs.* memory usage (batchsize=8). As the model gets larger, SNELL's advantage of low memory usage over full fine-tuning becomes more obvious.

Additionally, we present the memory usage evolutions during the fine-tuning process in Figure [3\(](#page-7-1)b) to provide a detailed explanation of how SNELL can save memory. In the model initialization stage, SNELL exhibits a significantly smaller memory usage compared to full fine-tuning. This is because full fine-tuning stores all weight matrices as learnable parameters in the optimizer, whereas SNELL only stores low-rank matrices with smaller parameter volumes. In the feed-forward phase, the memory usage increases with the storage of intermediate variables for backpropagation. Unlike other intermediate variables, the adaptation matrix  $\Delta W$  in SNELL solely relies on the low-rank parameter matrices, which are already stored in the optimizer. Therefore, it can be dumped in the feed-forward phase and recovered in backpropagation immediately, saving from a large amount of memory usage (SNELL *vs.* SNELL storing  $\Delta W$ ).

Scaling to Larger Models. To investigate the scalability of SNELL to large models, we apply it to ViT models of varying sizes (ViT-B/16, ViT-L/16, and ViT-H/16 pre-trained on ImageNet21K). We follow the experimental setup presented in Section [4.1,](#page-5-0) except for modifying the batch size for experiments on ViT-H/14 to 8 and changing the search scope of sparsity ratios  $s \in \{0, 0.9\}$ .

As depicted in Figure [3\(](#page-7-1)c), the memory usage of full fine-tuning increases rapidly as the model size grows. This observation highlights that existing PEFT methods like VPT and SPT, despite their advanced performances, incur substantial memory costs when applied to large-scale models due to even higher memory usage than full fine-tuning. In contrast, SNELL exhibits a notable advantage in terms of memory usage for larger models (similar to LoRA-8). When applied to ViT-H/14, the memory usage of SNELL is only approximately 50% of that required for full fine-tuning, exemplifying its significant memory-saving capability on large models.

Regarding the performance, as shown in Table [4,](#page-8-0) SNELL-8 outperforms LoRA-8 on all dataset groups (Natural, Specialized, and Structured) as well as the mean accuracy for both ViT-L and ViT-H on the VTAB-1k benchmark. This demonstrates the effectiveness of SNELL for adapting large pre-trained models to downstream tasks.

<span id="page-8-0"></span>

<b>Methods</b>	$V1AD-IRV1I-L/10$				$V1AD-IRV1I-IVI+$					
		Natural Specialized Structured Mean Acc.   Natural Specialized Structured Mean Acc.								
LoRA-8 SNELL-8	81.2 82.3	86.6 86.9	53.4 56.6	73.7 75.3	77.9 79.5	84.8 85.1	55.9 56.9	72.9 73.8		

Table 5: (a) Performance on VTAB-1k of sparsifying a full-rank matrix, the merged adaptation matrix of LoRA-8 and kernelized LoRA-8 (KLoRA-8) with sparsity ratio  $s = 0.9$ . (b) The mean accuracy on VTAB-1k of kernelized LoRA (KLoRA) and SNELL (KLoRA+sparsifying) with different ranks of learnable matrices. Perf. Imp. denotes the performance improvement of SNELL over KLoRA.  $(a)$ (b)

<span id="page-8-1"></span>

#### 4.3 Ablation Studies

Effect of Kernelized LoRA. We explore the effectiveness of kernelized LoRA by comparing the performance of sparsifying a full-rank matrix, the merged adaptation matrix of LoRA, and the merged adaptation matrix of kernelized LoRA. Experimental results are presented in Table [5\(](#page-8-1)a). We can see that sparsifying the merged adaptation matrix of LoRA significantly underperforms a full-rank matrix. This reveals that the low-rank property of the merged adaptation matrix in LoRA greatly compromises the weight selection scope, leading to performance degradation for sparse tuning. However, when we replace LoRA with kernelized LoRA, the performance becomes notably comparable to that of the full-rank matrix under the strong sparsity constraint ( $s = 0.9$ ). This indicates that kernelized LoRA can effectively leverage sparse tuning while maintaining a low memory usage.

Effect of Sparse Tuning. Table [5\(](#page-8-1)b) shows the performance comparison between SNELL and kernelized LoRA to explore the effectiveness of sparse tuning. For kernelized LoRA with different ranks, applying sparse tuning can consistently improve their performance. Moreover, as the rank of the learnable matrix increases, the performance of kernelized LoRA decreases while that of SNELL increases. This difference stems from the model regularization. Similar to sparse regularization, the low-rank property of LoRA that constrains the dependence between individual weights, can also be taken as a form of regularization. As the rank of the learnable matrix increases, the effect of low-rank regularization diminishes. Consequently, kernelized LoRA becomes more susceptible to over-fitting and encounters performance degradation. In contrast, SNELL employs both low-rank and sparse regularization. Higher ranks enable better sparsification towards downstream tasks, boosting sparse regularization that counteracts the diminished low-rank regularization. Therefore, a higher rank may lead to over-fitting in kernelized LoRA, but it can further enhance performance with sparse tuning.

Effect of Different Kernel Function. We investigate the effectiveness of different kernel functions in kernelized LoRA. First, we explore the ability of different kernel functions to fit randomly generated full-rank matrices based on low-rank matrices using the gradient descent algorithm (introduced in Appendix [A.3\)](#page-15-1). As shown in Figure [4\(](#page-9-0)a), we explored four kinds of kernel functions. Compared with the linear kernel function, nonlinear kernel functions can reconstruct the full-rank target matrix more accurately based on the low-rank matrices. Subsequently, we explore the performance of kernelized LoRA with different kernel functions for pre-trained model fine-tuning in Figure [4\(](#page-9-0)b). We find that using piecewise linear distance as the kernel function can achieve better results compared to linear kernel function (LoRA), while using Sigmoid and RBF kernels leads to severe performance degradation. This is because the complex non-linear kernel functions such as the exponential function increase the optimization difficulty in deep networks shown in Figure [4\(](#page-9-0)c). More comparisons between LoRA and kernelized LoRA (with piecewise linear kernel) are presented in Appendix [C.3.2.](#page-18-0)

Optimal Sparsity Ratio for Different Downstream Tasks. We provide the optimal sparsity ratio of SNELL-8 on tasks from VTAB-1k benchmarks in Figure [5.](#page-9-1) The optimal sparsity ratio varies

<span id="page-9-0"></span>

Figure 4: (a) The fitting ability of different kernel functions. We fit random sparse matrices by merging two learnable low-rank matrices with different kernel functions and compute the MSE loss. (b) Performance comparison on groups of datasets in VTAB-1k. (c) Training loss on CIFAR-100 dataset in VTAB-1k benchmark of kernelized LoRA with different kernel functions.

<span id="page-9-1"></span>

Figure 5: The optimal sparsity ratio of SNELL-8 on different downstream tasks (*left*) and the average optimal sparsity ratio within each group (*right*) in VTAB-1k benchmark. The pre-trained model is the ConvNeXt-B pre-trained on ImageNet-21k.

significantly across different downstream tasks within the same group (*e.g.*, Cifar *vs.* Sun397, dSprloc *vs.* Clevr-Dist). Furthermore, we can observe that the Natural task group exhibits a higher average optimal sparsity ratio compared to the Specialized group, while the Structured group demonstrates the lowest ratio. This observation aligns with the example illustrated in Figure [A6,](#page-15-2) where cross-domain adaptation from a model pre-trained on natural images (ImageNet) to images of Specialized and Structured groups necessitates a larger number of tunable parameters.

# 5 Conclusion

In this work, we proposed a PEFT method named SNELL (Sparse tuning with kerNELized LoRA) to conduct high-performance sparse tuning with low memory usage. To reduce memory usage, we sparsified the adaptation matrix merged with low-rank matrices rather than the pre-trained weight matrix to reduce the volume of learnable parameters stored in the optimizer. Then we designed a competition-based sparsification mechanism to avoid the additional memory usage of storing the tunable weight indexes. To reveal the effectiveness of sparse tuning, we utilize nonlinear kernel functions to merge the adaptation matrix, increasing the rank of the merged matrix to maintain a compact representation suitable for sparse tuning with low memory usage. Extensive experiments demonstrated the ability of SNELL to leverage the high performance of sparse tuning and the low memory usage of LoRA. For future work, we will apply SNELL on larger models such as LLMs and improve its training efficiency. For limitations discussion, please refer to Appendix [E.2.](#page-20-0)

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<span id="page-15-2"></span>

Figure A6: Samples of different downstream tasks in VTAB-1k.

# <span id="page-15-3"></span>A More Details of Experimental Setup

#### <span id="page-15-0"></span>A.1 More Details of Contenders.

- Full: fully tunes all the model parameters (including backbone and classification head).
- Linear: freezes all the backbone parameters and only tunes the linear classification head.
- Bias [\[5\]](#page-10-5): freezes all model parameters except for the bias term and the linear classification head.
- Partial-1: freezes the backbone except for the last 1 layer and also tunes the classification head as described in [\[30\]](#page-12-1).
- MLP-3: freezes the backbone and tunes the classification head implemented by a trainable 3-layer multi-layer percepton as described in [\[30\]](#page-12-1).
- VPT-Shallow [\[30\]](#page-12-1): freezes all the backbone parameters while introducing additional trainable prompts to the input space of the pretrained ViT.
- VPT-Deep [\[30\]](#page-12-1): freezes the backbone while appending additional trainable prompts to the sequence in the multi-head self-attention layer of each ViT block.
- Adapter-r [\[26\]](#page-11-8): freezes all the backbone parameters while adding a down projection, a ReLU non-linearity, and an up projection layer sequentially in the feed-forward network (FFN) of each visual Transformer block. We report the performance implemented by [\[22\]](#page-11-2) for comparison.
- Lora-r [\[27\]](#page-12-0): freezes all the backbone parameters while adding a concurrent branch including two low-rank matrices to the weight matrices in the multi-head self-attention layers to approximate efficiently updating them. The low-rank matrices can be merged into the backbone weights after fine-tuning. We report the performance implemented by [\[22\]](#page-11-2) for comparison.
- SPT [\[22\]](#page-11-2): identifies the tunable parameters for a given task in a data-dependent way, and utilizes LoRA (SPT-LoRA) or Adapter (SPT-Adapter) for weight matrices with a large number of tunable parameters and sparse tuning for weight matrices with a small number of tunable parameters.
- VQT [\[53\]](#page-13-0): introduces a handful of learnable query tokens to each layer for adaptation.
- DoRA [\[36\]](#page-12-12): decomposes the pre-trained weight into two components, *i.e.*, magnitude and direction, for fine-tuning. It specifically employs LoRA for directional updates to efficiently minimize the number of trainable parameters.
- GPS [\[63\]](#page-14-3): identifies task-dependent tunable weights and applies sparse tuning to these weights.

#### A.2 Dataset Samples for the Downstream Tasks

We visualize some sampled images from different downstream tasks of VTAB-1k [\[59\]](#page-14-8)) in Figure [A6.](#page-15-2) The VTAB-1k benchmark encompasses a diverse range of tasks, including natural images, remote sensing, medical images, etc. Notably, our SNELL has achieved state-of-the-art (SOTA) performance on these datasets, demonstrating its general effectiveness.

#### <span id="page-15-1"></span>A.3 Implementation details of Figure [4\(](#page-9-0)a)

Given a random matrix  $\mathbf{W}^{(gt)} \in \mathbb{R}^{m \times n}$ , we fit this matrix by merging two low-rank learnable matrices  $\mathbf{B} \in \mathbb{R}^{m \times r}$ ,  $\mathbf{A} \in \mathbb{R}^{n \times r}$  with different kernel functions  $\kappa$ ,

$$
\min_{\mathbf{A}, \mathbf{B}} \frac{1}{mn} \sum_{i=1}^{m} \sum_{j=1}^{n} (\mathbf{W}_{ij}^{(gt)} - \kappa(\mathbf{B}_{i,\cdot}, \mathbf{A}_{j,\cdot}))^2.
$$
 (A7)

Table A6: Expression of kernel function utilized in the main text.

<span id="page-16-1"></span>

<b>Kernel Function</b>	Expression
Linear	$\kappa(\mathbf{x}, \mathbf{x}') = \mathbf{x}^\top \mathbf{x}'$
	Piecewise Linear $\kappa(\mathbf{x}, \mathbf{x}') = \sum \alpha_p   \mathbf{x}_{[rp/P]: [r(p+1)/P]} - \mathbf{x}'_{[rp/P]: [r(p+1)/P]}  _2$
Sigmoid <b>RBF</b>	$\kappa(\mathbf{x}, \mathbf{x}') = \alpha (1 + exp(-\beta \mathbf{x}^\top \mathbf{x}'))^{-1} + \gamma$ $\kappa(\mathbf{x}, \mathbf{x}') = \alpha(exp(-\beta \ \mathbf{x} - \mathbf{x}'\ _2^2)) + \gamma$

<span id="page-16-2"></span>Table A7: Top-1 accuracy (%) on VTAB-1k benchmarks using ViT-B/16 backbone pre-trained on ImageNet-21k supervisedly. The best result is in bold, and the second-best result is underlined.



We use gradient descent for 1e5 optimization steps, employing the Adam optimizer with a learning rate of  $1e - 4$ . We fit 10 randomly generated matrices for each kernel function presented in Table [A6](#page-16-1) and report the average MSE Loss in Figure [4\(](#page-9-0)a).

# <span id="page-16-0"></span>B Introduction of Utilized Kernel Functions

**Kernel Function Definition (positive semi-definite).** Consider a vector space  $\mathbb{R}^r$ , a kernel function  $\kappa : \mathbb{R}^r \times \mathbb{R}^r \to \mathbb{R}$  is called a positive semi-definite kernel on  $\mathbb{R}^r$  if

$$
\sum_{i=1}^{n} \sum_{j=1}^{n} c_i c_j \kappa(\mathbf{x}_i, \mathbf{x}_j) \ge 0
$$
 (A8)

holds for all  $\mathbf{x}_1, ..., \mathbf{x}_n \in \mathbb{R}^r, c_1, ..., c_n \in \mathbb{R}, n \in \mathbb{N}$ .

Given two vectors  $x, x' \in \mathbb{R}^r$ , we show the utilized kernel functions in Table [A6.](#page-16-1) We introduce additional learnable parameters (the *e.g.*  $\alpha$  for Sigmoid and RBF kernel,  $\alpha_p$  for piecewise linear kernel) that enable the merged adaptation matrix  $\Delta W$  to accommodate both positive and negative values. The additional parameters select certain elements in the matrix and assign them negative values, without compromising the high-rank property of the merged adaptation matrix ∆W. We set  $P = 2$  for the piecewise linear kernel.

Table A8: (a) Performance comparisons on FGVC between SNELL and GPS. (b) Memory usage comparison between SNELL and GPS (batchsize=8) on different pre-trained models.  $(a)$  $(h)$ 

	$\cdots$									
Method		CUB NABirds		$\overline{\text{Oxford}}$ Stanford Stanford Mean			Method		Memory Usage (Mb)	
			Flowers	Dogs	Cars				ViT-B ViT-L ViT-H	
<b>GPS</b>	89.9	86.7	99.7	92.2	90.4	91.8	<b>GPS</b>		2428 7522	16119
SNELL-32 89.9		87.0	99.4	92.0	90.5	91.8	SNELL-32   1673		4519	9692

<span id="page-17-2"></span>Table A9: Performance comparisons on VTAB-1k between SNELL and VQT with ViT-B/16 pretrained on ImageNet-21K. The best result is in bold.

<span id="page-17-1"></span>

# C Additional Experiments

#### <span id="page-17-0"></span>C.1 More Comparisons with Existing Methods

Given that some methods do not provide performance or implementation details on both FGVC and VTAB benchmarks, we present a comparison between SNELL and these methods in the appendix rather than in Table [1.](#page-6-0) First, we provide comparisons with GPS [\[63\]](#page-14-3) on the FGVC benchmark in terms of performance and memory usage in Table [A8.](#page-17-1) With comparable performance, SNELL has a significant advantage over GPS in terms of memory usage. Then, we compare SNELL with VQT [\[53\]](#page-13-0) on VTAB-1k dataset in Table [A9.](#page-17-2) SNELL significantly outperforms VQT (76.9% *vs.* 68.8%).

#### C.2 Per-task results on the VTAB-1k benchmark

We provide the per-tasks results on the VTAB-1k benchmark using ViT-B/16 supervised pre-trained on ImageNet21K in Table [A7.](#page-16-2) Our SNELL has demonstrated superior performance by achieving SOTA performance on 13 downstream tasks. Additionally, SNELL achieves SOTA performance on the mean accuracy across all tasks (74.6% *vs.* 74.1%), indicating its effectiveness in various domains.

#### C.3 More ablation studies.

#### C.3.1 Comparison between Competition-based Sparsification and Pre-defined Weight Mask

To verify the effectiveness of the proposed competition-based sparsification mechanism, we compare the performance on FGVC datasets between kernelized LoRA (KLoRA-8-Fixed) with pre-defined fixed masks, and SNELL in Table [A10.](#page-17-3) The weight masks are generated by SPT [\[22\]](#page-11-2). For a fair comparison, we utilize the same data augmentation as SPT. Compared to our dynamic masking strategy, pre-defined fixed masking can hardly identify and adjust the most task-relevant weights in an end-to-end fashion, which leads to performance degradation (89.4 vs. 90.3).

<span id="page-17-3"></span>Table A10: Performance comparisons on FGVC benchmark between kernelized LoRA with fixed weight masks (KLoRA-8-Fixed) and our dynamical masks (SNELL-8).

Method	$CUB-200$	<b>NABirds</b>	Oxford Flowers	Stanford Dogs Stanford Cars		Mean
KLoRA-8-Fixed	88.0	82.1	99.0	89.4	88.4	89.4
SNELL-8	89.0	83.9	99.3	90.6	88.6	90.3

Method	Natural	Specialized	Structured	Mean Acc.
LoRA-8	80.8	84.9	59.6	75.1
KL <sub>O</sub> R <sub>A</sub> -8	80.8	85.5	60.5	75.6
$L_0RA-16$	80.6	85.6	58.5	74.9
$KLoRA-16$	80.9	85.7	59.7	75.4
$L_0RA-32$	79.4	85.4	57.8	74.2
$KLoRA-32$	80.8	85.4	59.4	75.2

<span id="page-18-1"></span>Table A11: Comparisons between LoRA and kernelized LoRA (KLoRA) on VTAB-1k using ViT-B/16 pre-trained on ImageNet21k supervisedly. Better performance for the same rank is in bold.

<span id="page-18-2"></span>Table A12: Memory usage comparison between SNELL and LoRA.  $\Delta$  Mem. denotes the incremental memory usage of SNELL in comparison to LoRA.

Pre-trained Model	LoRA-8	SNELL-8	$\Delta$ Mem. / Mem. (MB) Mem. (MB) SNELL-8 Mem.
$ViT-B/16$	1546	1673	0.076
$ViT-L/16$	4325	4519	0.043
$ViT-H/16$	9325	9692	0.038

#### <span id="page-18-0"></span>C.3.2 Comparison between Kernelized LoRA and LoRA

We compare the performance of LoRA and kernelized LoRA on the VTAB-1k benchmark, where all weight matrices of the pre-trained models are fine-tuned to ensure a fair comparison. The experimental results are presented in Table [A11.](#page-18-1) Through experiments with different ranks, we observed that kernelized consistently outperforms LoRA across various task groups. The replacement of the inner product with nonlinear kernel functions leads to stronger expressive ability, which in turn contributes to improved performance on downstream tasks.

## C.3.3 Additional Memory Usage from Nonlinear Kernel Functions

In Figure  $3(a)$ , we observe that SNELL requires additional memory usage compared to LoRA due to the incorporation of nonlinear kernel functions. To explore whether the impact of this additional usage hinders the usability of SNELL on large models, we compare the memory usage between SNELL and LoRA on models as the model size grows (in Table [A12\)](#page-18-2). As the model size expands, the incremental memory usage of SNELL becomes negligible.

#### C.4 Experiments on Large Language Models

We apply SNELL on LLaMA2-7B [\[52\]](#page-13-12) to adapt to the commonsense reasoning benchmark. As Table [A13](#page-18-3) shows, SNELL achieves a better performance than LoRA. This shows the applicability of SNELL to NLP tasks. Many other vision PEFT approaches lack this capability, as they necessitate a full level of memory usage for fine-tuning as Figure [3\(](#page-7-1)a) shows.

# <span id="page-18-4"></span>C.5 Training Time Analysis

Table [A14](#page-19-0) provides a comparison of training time costs between SNELL and other PEFT methods using NVIDIA GeForce RTX 4090 GPU. The training time of SNELL-8 is slightly higher than LoRA-8 (0.557 *vs.* 0.443). By further comparing the training time of SNELL-8 and SNELL-8 (saving  $\Delta W$ ), it becomes apparent that the increase in time cost primarily stems from the recomputation

Table A13: Performance on commonsense reasoning benchmark with LLaMA-2-7B.

<span id="page-18-3"></span>

Model			BoolQ PIQA SIQA HellaSwag WinoGrande ARC-e ARC-c OBQA Average				
LoRA-32 SNELL-32 71.4 82.9 80.7	69.8 79.9 79.5	83.6 82.1	82.6 80.9	79.8 82.6	64.7 68.0	81.0 80.8	77.6 78.7

<span id="page-19-1"></span><span id="page-19-0"></span>

Table A14: Training time cost on ViT-B/16 of different PEFT methods.

Figure A7: Accuracy of different sparsity ratios using SNELL-8. The pre-trained model is the ConvNeXt-B pre-trained on ImageNet-21k.

of the merged adaptation matrix  $\Delta W$ . Conversely, the time cost associated with sparsification (KLoRA-8 *vs.* SNELL-8) and kernelization (LoRA-8 *vs.* KLoRA-8 (saving  $\Delta W$ )) is relatively small. Indeed, despite the slight increase in time cost due to the recomputation, one significant advantage is the significant performance improvement and memory efficiency shown in Figure [3.](#page-7-1)

# D Additional Visualization

#### D.1 Performance of Different Sparsity Ratio

Figure [A7](#page-19-1) depicts the accuracy of different sparsity ratios on datasets in VTAB-1k. Different downstream tasks exhibit diverse preferences for the sparsity ratio. For instance, CIFAR-100 tends to favor a smaller sparsity ratio (0.2), while DTD prefers a larger sparsity ratio (0.8). Both Sun397 and Retinopathy tasks also lean towards a larger sparsity ratio (0.99). This highlights the need to consider the specific characteristics of each task when determining the optimal sparsity ratio.

#### D.2 Analysis of Tunable Parameters

We analyze the tunable weights of SNELL for different downstream tasks. In Figure [A8,](#page-19-2) we compute the number of weights in the weight matrix  $W_Q$  of self-attention [\[54\]](#page-13-13) selected by multiple tasks. We find that most of the weights are only selected by a single downstream task (Tuned Times=1). Moreover, we find that in blocks of different depths, there will be a small part of weights that are selected by multiple downstream tasks, which indicates that there exists a small number of crucial parameters to improve the model's performance on downstream tasks.

<span id="page-19-2"></span>

Figure A8: Number of weights in  $W_Q$  selected by multiple tasks with sparsity ratio  $s = 0.99$ .

# E Discussion

## E.1 Tunable Parameter Volume Computing

We justify our choice to omit to report the volume of learnable parameters. First, computing the volume of tunable parameters in SNELL is difficult. In the case of LoRA, the volume corresponds to the shape of the learnable low-rank matrices. Conversely, for sparse tuning, the volume is determined by the number of updated weights. However, SNELL employs low-rank matrices as learnable parameters and achieves additional updated weight reduction by sparsifying the merged matrices. When using the parameter volume computation method of LoRA, calculating the reduction in parameters due to sparsification becomes challenging. Conversely, applying the parameter volume computation method of sparse tuning would be inherently unfair, given that SNELL is specifically optimized using low-rank matrices. Second, the parameter efficiency is a pathway to achieve high performance and low memory usage rather than an objective for model improvement, because performance improvement and memory usage reduction hold practical value. In our experiments, SNELL has demonstrated its advantages in terms of high performance and low memory usage, which we consider more valuable than the pursuit of fewer learnable parameters.

# <span id="page-20-0"></span>E.2 Limitation Discussion

Despite achieving state-of-the-art performance with low memory usage, SNELL requires more training time than LoRA. The additional training time cost comes from the recomputation of the merged matrix  $\Delta W$  in the backpropagation process presented in Appendix [C.5.](#page-18-4) However, it is crucial to note that this limitation can be solved. Firstly, given the unique characteristics of the kernel matrix, more efficient methods [\[29\]](#page-12-13) can be employed to calculate the merged adaptation matrix  $\Delta W$ . Secondly, by designing appropriate GPU operators, it is possible to avoid explicitly calculating ∆W [\[45\]](#page-13-2) during the fine-tuning process like LoRA and reparameterize the learnable low-rank matrices into the pre-trained weight matrices after the fine-tuning process.

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