CAN TRANSFORMERS REASON LOGICALLY? A STUDY IN SAT SOLVING

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ABSTRACT

We theoretically and empirically study the logical reasoning capabilities of LLMs in the context of the Boolean satisfiability (SAT) problem. First, we construct a non-uniform class of decoder-only Transformers that can solve 3-SAT using backtracking and deduction via Chain-of-Thought (CoT). We prove its correctness by showing trace equivalence to the well-known DPLL SAT-solving algorithm. Second, to support the implementation of this abstract construction, we design a compiler PARAT that takes as input a procedural specification and outputs a transformer model implementing this specification. Third, rather than *programming* a transformer to reason, we evaluate empirically whether it can be *trained* to do so by learning directly from algorithmic traces ("reasoning paths") of the DPLL algorithm.

1 Introduction

Transformer-based Language Models (LLMs, Vaswani et al. (2017)) have demonstrated remarkable success in a wide range of tasks framed in natural language, especially when using prompting techniques such as Chain-of-Thought (CoT, Wei et al. (2022)). On the other hand, even the most advanced LLMs face challenges in reliable multi-step reasoning, frequently hallucinating towards nonsensical conclusions (Kambhampati et al. (2024)). Evaluating progress on logical deduction in language models remains an ongoing challenge as researchers have continued to disagree on even a reasonable definition of what constitutes "reasoning."

This paper focuses on the question of LLM reasoning capability in what we believe is the simplest and most mathematically precise setting: the Boolean satisfiability problem (SAT, Cook (1971)). SAT problems provide an excellent starting point for studying the reasoning ability of LLMs given that (a) natural language often encodes Boolean logic, and (b) we already have many useful algorithms that implement logical deduction to solve SAT problems Biere et al. (2009). Notably, notwithstanding the NP-completeness of SAT, humans implicitly solve simple boolean satisfaction problems in their daily lives; scheduling a multi-person meeting across time zones, for example.

In this work we aim to rigorously investigate Transformers' multi-step reasoning and backtracking capability in solving formal logical reasoning problems, and we demonstrate through a theoretical construction that decoder-only Transformers can reliably decide SAT instances.

Theorem 1.1 (Informal version of Theorem 4.5). For any $p, c \in \mathbb{N}^+$, there exist a decoder-only Transformer with $O(p^2)$ parameters that can decide all 3-SAT instances of at most p variables and c clauses using Chain-of-Thought reasoning.

To investigate the properties of our construction empirically, we design a compiler that converts computational graphs of abstract sequence operations used in our construction into Transformer model weights. We implemented the construction in PyTorch and empirically validated its correctness on random 3-SAT instances. We also investigated its empirical properties such as the number of generated CoT tokens.

Additionally, we perform training experiments to demonstrate that Transformers can effectively learn from deductive reasoning and the backtracking process of the DPLL algorithm encoded as Chain-of-Thought. We show that Transformers equipped with CoT can generalize between SAT instances generated from different distributions within the same number of variables p. However,

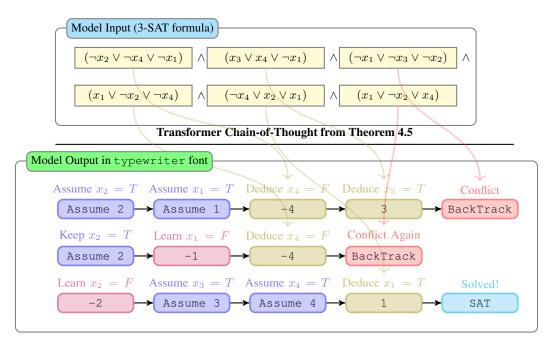


Figure 1: Visualization of the Chain-of-Thought (CoT) process used by our model to solve the SAT formula described in Theorem 4.5. The model autonomously performs trial-and-error reasoning, making multiple attempts and backtracking upon encountering conflicts. Here, T represents T rue and F represents F also. Tokens in typewriter font denote the CoT generated by the model.

LLMs trained on SAT instances with CoT still struggle to solve instances with unseen number of variables, demonstrating challenges in learning length-generalizable reasoning and opportunities to incorporate compiled reasoning components in Transformer LLMs to improve reasoning capabilities.

Contributions We prove by theoretical construction that decoder-only Transformers can solve 3-SAT, a fundamental NP-Complete logical reasoning problem, by performing logical deduction and backtracking using Chain-of-Thought (CoT). We show that Transformers can perform logical deduction on all conditions (clauses) in parallel instead of checking each condition sequentially. Nevertheless, the construction requires exponentially many CoT steps in the worst case, although it is much faster on most typical examples.

We design PARAT, a compiler of high-level sequence operations written in Numpy-like syntax into Transformer model weights, to empirically validate and analyze theoretical constructions of Transformer algorithms.

We empirically demonstrate that the compiled SAT-solver model can solve SAT formulas up to 20 propositions and 88 clauses with perfect accuracy. Note that our goal is not to compete with modern state-of-the-art SAT solvers. Rather, we answer a fundamental question about whether LLMs can perform propositional reasoning. Finally, our training experiments suggest that Chain-of-Thought allows Transformer-LLMs to achieve out-of-distribution generalization for the same input lengths.

2 Related Work

Theoretical Expressiveness of Transformers and Chain-of-Thought (CoT): Owing to the empirical success of Transformer-based models, many researchers have investigated the capabilities of the Transformer architecture from a theoretical perspective. This line of research focuses on what types of computation can Transformer models simulate by providing theoretical constructions of Transformer models with idealized assumptions. The seminal work of Liu et al. (2023) showed that Transformers can simulate automata using a single pass over only a logarithmic number of layers w.r.t. the number of states. Yao et al. (2021) demonstrated that transformers can perform parentheses matching of at most k types of parentheses and D appearance of each $(\mathrm{Dyck}_{k,D})$ with D+1 layers.

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However, the computation power of one pass of the Transformer model is fundamentally limited (Merrill & Sabharwal (2023)), and the success of Chain-of-Thought (CoT) reasoning (Wei et al. (2022)) has sparked more recent research on how CoT can improve upon the expressiveness of Transformer models. Pérez et al. (2019) proved that Transformers can emulate the execution of single-tape Turing machines if each output vector is appended to the input vector sequence at the next iteration. Giannou et al. (2023) showed that Transformers can recurrently simulate arbitrary programs written in a one-instruction-set language if the output vector at every position of the Transformer is passed as input to the model at the next iteration. Li et al. (2024) proved that Transformers can simulate arbitrary boolean circuits using CoT by representing the circuit in the positional encoding and is commonly perceived to have shown that Transformers with CoT can "solve all problems". In particular, transformers can decide all problems in $P/poly \supseteq P$ with polynomial steps of CoT. Merrill & Sabharwal (2024) showed that Transformers with averaging hard attention can decide all regular languages with a linear number of CoT tokens and decide all problems in P with a polynomial number of CoT tokens. Feng et al. (2023) shows that Transformer CoT can perform integer arithmetic, solve linear equations, and perform dynamic programming for the longest increasing subsequence and edit distance problems. These seminal works profoundly advanced our understanding of the capabilities of Transformer models from a theoretical perspective.

How our work differs from the above-mentioned results: Many of the above papers are focused on problems in P or P/poly, while 3-SAT is an NP-complete problem. It is widely believed that P is a strict subset of NP, and it is not known whether NP is a subset of P/poly. In other words, our results are not comparable to these earlier results.

Meanwhile, Pérez et al. (2019), Li et al. (2024), and Merrill & Sabharwal (2024) also show that Transformers can simulate single-tape Turing Machines (TM) with CoT and can theoretically be extended to arbitrary decidable languages. However, these constructions require at least one CoT token for every step of TM execution. By contrast, our theoretical construction demonstrates that, for certain classes of formal reasoning problems, Transformers can simulate algorithmic reasoning traces at an abstract level with *drastically reduced number of CoT tokens* compared to step-wise emulation of a single-tape TM. At each CoT Step, our construction performs deductive reasoning over the full input in parallel while any single-tape TM must process each input token sequentially. Furthermore, the CoT produced by our theoretical construction abstractly represents the human reasoning process of trial and error, as demonstrated in Figure 1.

Compilation of Transformer Weights. Further, prior work on the theoretical construction of Transformer models rarely provide practical implementations. Notably, Giannou et al. (2023) provide an implementation of their construction and demonstrate its execution on several programs. However, the model is initialized "manually" using prolonged sequences of array assignments, limiting its extensibility to other theoretical frameworks.

More recently, Lindner et al. (2023) released Tracr, which compiles RASP (Weiss et al. (2021)) programs into decoder-only Transformer models. The "Restricted Access Sequence Processing Language" (RASP, Weiss et al. (2021)) is a human-readable representation of a subset of operations that Transformers can perform via self-attention and MLP layers. In our preliminary attempt to implement a SAT solver model with Tracr, we identified several implementation inconveniences and limitations of Tracr when scaling to more complex algorithms, which motivated the development of our compiler. In particular: (1) Every "variable" (termed sop in Lindner et al. (2023)) in Tracr must be either a one-hot categorical encoding or a single numerical value. This constraint makes representing more complex vector structures highly inconvenient. Furthermore, each select operation (i.e., self-attention) accepts only a single sop as the query and key vectors, whereas our theoretical construction often requires incorporating multiple sops as queries and keys. (2) Tracr represents position indices and many other discrete sops with a one-hot encoding, allocating a residual stream dimension for each possible value of the sop. In particular, compiling models with a context length of n requires O(n) additional embedding dimensions for each SOp that represents a position index. (3) For each binary operation between one-hot encoded sops (such as position indices), Tracr creates an MLP layer that first creates a lookup table of all possible value combinations of the input sops. This results in an MLP layer of $O(n^3)$ parameters.

3 Preliminaries

The Boolean satisfiability problem (SAT) is the problem of determining whether there exists an assignment A of the variables in a Boolean formula F such that F is true under A. In this paper we only consider 3-SAT instances in $conjunctive\ normal\ form\ (CNF)$, where groups of at most 3 variables and their negations (literals) can be joined by OR operators into clauses, and these clauses can then be joined by AND operators. In our implementations we use the well-known DIMACS encoding for CNF formulae whereby each literal is converted to a positive or negative integer corresponding to its index, and clauses are separated by a 0.

3.1 AUTOREGRESSIVE DECODER-ONLY TRANSFORMER ARCHITECTURE

The Transformer architecture Vaswani et al. (2017) is a foundational model in deep learning for sequence modeling tasks. In our work, we focus on the autoregressive decoder-only Transformer, which generates sequences by predicting the next token based on previously generated tokens. It is a relatively complex architecture, and here we only give a precise but quite concise description, and we refer the reader Vaswani et al. (2017) among many others for additional details. Given an input sequence of tokens $\mathbf{s} = (s_1, s_2, \dots, s_n) \in \mathcal{V}^n$, where \mathcal{V} is a *vocabulary*, a Transformer model $M: \mathcal{V}^* \to \mathcal{V}$ maps \mathbf{s} to an output token $s_{n+1} \in \mathcal{V}$ by composing a sequence of parameterized intermediate operations. These begin with a token embedding layer, following by L transformer blocks (layers), each block consisting of H attention heads, with embedding dimension d_{emb} , head dimension d_h , and MLP hidden dimension d_{mlp} . Let us now describe each of these maps in detail.

Token Embedding and Positional Encoding. Each input token s_i is converted into a continuous vector representation $\operatorname{Embed}(s_i) \in \mathbb{R}^d$ using a fixed embedding map $\operatorname{Embed}(\cdot)$. To incorporate positional information, a positional encoding vector $\boldsymbol{p}_i \in \mathbb{R}^d$ is added to each token embedding. The initial input to the first Transformer block is

$$\boldsymbol{x}^{(0)} \leftarrow (\text{Embed}(s_1) + \boldsymbol{p}_1, \text{ Embed}(s_2) + \boldsymbol{p}_2, \dots, \text{ Embed}(s_n) + \boldsymbol{p}_n) \in \mathbb{R}^{n \times d}.$$

Transformer Blocks. For $l=1,\ldots,L$, each block l of the transformer processes an embedded sequence $\boldsymbol{x}^{(l-1)} \in \mathbb{R}^{n \times d}$ to produce another embedded sequence $\boldsymbol{x}^{(l)} \in \mathbb{R}^{n \times d}$. Each block consists of a multi-head self-attention (MHA) mechanism and a position-wise feed-forward network (MLP). We have a set of parameter tensors that includes MLP parameters $\boldsymbol{W}_1^{(l)} \in \mathbb{R}^{d_{\text{emb}} \times d_{\text{mlp}}^*}$, $\boldsymbol{b}_1^{(l)} \in \mathbb{R}^{d_{\text{mlp}}}$, $\boldsymbol{b}_1^{(l)} \in \mathbb{R}^{d_{\text{mlp}}}$, and $\boldsymbol{b}_2^{(l)} \in \mathbb{R}^d$, self-attention parameters $\boldsymbol{W}_Q^{(l,h)}$, $\boldsymbol{W}_Q^{(l,h)}$, $\boldsymbol{W}_Q^{(l,h)}$, $\boldsymbol{W}_Q^{(l,h)}$, $\boldsymbol{W}_Q^{(l,h)}$, $\boldsymbol{W}_Q^{(l,h)}$, and multi-head projection matrix $\boldsymbol{W}_Q^{(l)} \in \mathbb{R}^{(Hd_h) \times d_{\text{emb}}}$. We will collectively refer to all such parameters at layer l as $\Gamma^{(l)}$, whereas the self-attention parameters for attention head l at layer l will be referred to as $\Gamma^{(l,h)}$. We can now process the embedded sequence $\boldsymbol{x}^{(l-1)}$ to obtain $\boldsymbol{x}^{(l)}$ in two stages:

$$m{h}^{(l)} \leftarrow m{x}^{(l-1)} + ext{MHA}\left(m{x}^{(l-1)}; \Gamma^{(l)}
ight), \qquad ext{and} \qquad m{x}^{(l)} \leftarrow m{h}^{(l)} + ext{MLP}\left(m{h}^{(l)}; \Gamma^{(l)}
ight),$$

where

$$\begin{split} & \text{MHA}\left(\boldsymbol{x}; \boldsymbol{\Gamma}^{(l)}\right) := \text{Concat}\left(\text{Attention}(\boldsymbol{x}; \boldsymbol{\Gamma}^{(l,1)}), \dots, \text{Attention}(\boldsymbol{x}; \boldsymbol{\Gamma}^{(l,H)})\right) \boldsymbol{W}_O^{(l)} \\ & \text{Attention}(\boldsymbol{x}; \boldsymbol{\Gamma}^{(l,h)}) := \text{softmax}\left(d_h^{-1/2} \boldsymbol{x} \boldsymbol{W}_Q^{(l,h)} (\boldsymbol{W}_K^{(l,h)} \boldsymbol{x})^\top + \boldsymbol{M}\right) \boldsymbol{x} \boldsymbol{W}_V^{(l,h)} \\ & \text{MLP}\left(\boldsymbol{h}; \boldsymbol{\Gamma}^{(l)}\right) := \sigma\left(\boldsymbol{h} \boldsymbol{W}_1^{(l)} + \boldsymbol{b}_1^{(l)}\right) \boldsymbol{W}_2^{(l)} + \boldsymbol{b}_2^{(l)}. \end{split}$$

The $n \times n$ matrix M is used as a "mask" to ensure self-attention is only backward looking, so we set $M[i,j] = \infty$ for $i \geq j$ and M[i,j] = 0 otherwise. Finally, we use the $\operatorname{ReGLU}(\cdot) : \mathbb{R}^{2d_{\operatorname{mlp}}} \to \mathbb{R}^{d_{\operatorname{mlp}}}$ activation function $\sigma(\cdot)$ at each position. Tiven input $u \in \mathbb{R}^{n \times 2d_{\operatorname{mlp}}}$, for each position i we split u_i into two halves $u_{i,1}$, $u_{i,2} \in \mathbb{R}^d$ and, using \otimes denotes element-wise multiplication, we define

$$\sigma_{\text{ReGLU}}(\boldsymbol{u}_i) = \boldsymbol{u}_{i,1} \otimes \text{ReLU}(\boldsymbol{u}_{i,2}).$$
 (1)

Output Layer. After the final Transformer block, the output representations are projected onto the vocabulary space to obtain a score for each token. We assume that we're using the greedy decoding strategy, where the token with the highest score at the last input position is the model output.

Algorithm 1: Greedy Decoding

```
Input: Model M: \mathcal{V}^* \to \mathcal{V}, prompt s_{1:n} = (s_1, s_2, \dots, s_n), stop tokens \mathcal{E} \subseteq \mathcal{V}, t \leftarrow n

1 while t \leftarrow t+1 do

2 |s_t \leftarrow M(s_{1:t-1}); // Obtain model output and append to string

3 | if s_t \in \mathcal{E} return s_{1:t}

4 end
```

$$o = x^{(L)}W_{\text{out}} + b_{\text{out}} \in \mathbb{R}^{n \times V}, s_{n+1} = \arg\max_{v} o_{n,v} \in \mathcal{V}$$
 (2)

where $W_{\text{out}} \in \mathbb{R}^{d \times V}$, $b_{\text{out}} \in \mathbb{R}^{V}$, V is the size of the vocabulary, $o_{n,v}$ is the score for token v at the last input position n.

Autoregressive Decoding and Chain-of-Thought. During generation, the Transformer model is repeatedly invoked to generate the next token and appended to the input tokens, described in Algorithm 1. In this paper, we refer to the full generated sequence of tokens as the **Chain-of-Thought**, and the number of chain-of-thought tokens in Algorithm 1 is t-n.

4 Transformers and SAT: Logical Deduction and Backtracking

This section presents and explains our main results on Transformers' capability in deductive reasoning and backtracking with CoT. To rigorously state our results, we first formally define decision problems, decision procedures, and what it means for a model to "solve" a decision problem using CoT:

Definition 4.1 (Decision Problem). Let \mathcal{V} be a vocabulary, $\Sigma \subseteq \mathcal{V}$ be an alphabet, $L \subseteq \Sigma^*$ be a set of valid input strings. We say that a mapping $f: L \to \{0,1\}$ is a *decision problem* defined on L.

Definition 4.2 (Decision Procedure). We say that an algorithm \mathcal{A} is a decision procedure for the decision problem f, if given any input string x from L, \mathcal{A} halts and outputs 1 if f(x) = 1, and halts and outputs 0 if f(x) = 0.

Definition 4.3 (Autoregressive Decision Procedure). For any map $M: \mathcal{V}^* \to \mathcal{V}$, which we refer to as an *auto-regressive next-token prediction model*, and $\mathcal{E} = \{\mathcal{E}_0, \mathcal{E}_1\} \subset \mathcal{V}$, define procedure $\mathcal{A}_{M,\mathcal{E}}$ as follows: For any input $s_{1:n}$, run Algorithm 1 with stop tokens \mathcal{E} . $\mathcal{A}_{M,\mathcal{E}}$ outputs 0 if $s_{1:t}$ ends with \mathcal{E}_0 and $\mathcal{A}_{M,\mathcal{E}}$ output 1 otherwise. We say M autoregressively decides decision problem f if there is some $\mathcal{E} \subset \mathcal{V}$ for which $\mathcal{A}_{M,\mathcal{E}}$ decides f.

Definition 4.4 (3-SAT $_{p,c}$). Let DIMACS(p,c) denote the set of valid DIMACS encodings of 3-SAT instances with at most p variables and c clauses with a prepended <code>[BOS]</code> token and an appended <code>[SEP]</code> token. Define 3-SAT $_{p,c}$: DIMACS $(p,c) \rightarrow \{0,1\}$ as the problem of deciding whether the 3-SAT formula encoded in the input in DIMACS(p,c) encoding is satisfiable.

With the above definition, we're ready to present a formal statement of our theoretical construction of a Transformer model that performs SAT Solving:

Theorem 4.5 (Decoder-only Transformers can solve SAT). For any $p, c \in \mathbb{N}^+$, there exists a Transformer model $M: \mathcal{V}^* \to \mathcal{V}$ that autoregressively decides $3\text{-SAT}_{p,c}$ in no more than $p \cdot 2^{p+1}$ CoT iterations. M requires L = 7 layers, H = 5 heads, $d_{emb} = O(p)$, and $O(p^2)$ parameters.

Remarks on Theorem 4.5

- The upper bound on the CoT length $p \cdot 2^{p+1}$ is a worst-case upper bound which assumes that the model is unable to make any logical deductions have to try all 2^p assignments. However, this upper bound is never reached in practice, and in Figure 3 we show that the number of CoT tokens is no greater than $8p \cdot 2^{0.08p}$ for most formulas. If the number of backtracking steps is bounded by T then the CoT is no longer than (2p+1)(T+1)
- The worst-case CoT length is independent of the number of clauses c, which is due to the parallel deduction over all clauses within the Transformer construction. Otherwise, sequentially processing each clause would take at least $c \cdot 2^{O(p)}$ number of steps.

- Positional encodings are not included in the number of parameters. The positional encoding
 at position i is the numerical value i at a particular dimension.
- Each parameter can be represented with $O(p + \log c)$ bits

We show our full proof via trace equivalence with DPLL in Appendix C. The construction uses adapted versions of lemmas from Feng et al. (2023) as basic building blocks.

Here we describe our design of the algorithm and CoT process for SAT-Solving and a high-level description on the internal mechanisms of the Transformer model.

Logical Deduction and Backtracking in CoT. In Figure 1, we illustrate the Chain-of-Thought of our model when solving SAT instances. The model performs SAT-solving through trial-and-error reasoning by iteratively exploring different solution paths while "learning" lessons from failed attempts, inspired by the "abstract DPLL" algorithm (Nieuwenhuis et al. (2005)). Specifically, at each step, the model: (1) attempts a solving direction by assigning a truth value to a variable; (2) deduces new variable assignments based on the clauses and current assignments; and (3) backtracks and negates the last assumed assignment if a conflict arises. The chain of thought keeps track of the current "assignment trace", which includes the order of current assignments and whether each assignment is an assumption or a deduction. This iterative process continues until a satisfying assignment is found, which mirrors the human problem-solving process of trying different possible solutions before reaching an answer.

Parallel Processing of Constraints. In our construction, we demonstrate that Transformers can process all clauses in parallel when checking for satisfaction, conflicts, and finding possible deductions.

Essentially, each clause in the input formula as well as the current state can be "summarized" as a single binary vector, and the query and key transformations ensure that the dot product between the current state (assignment) and each clause provides information on whether deduction is possible between them. The attention operation averages over clauses where deductions are possible and the following MLP layer filters out the result of deductions.

5 Compiler for Complex Transformer Algorithms

In the previous section, we presented a theoretical construction of a Transformer capable of solving SAT instances through backtracking and parallel deduction. However, relying solely on theorems and proofs can make it challenging to gain practical insights and verify correctness. To address this, we introduce ParametricTransformer (PARAT), which provides a framework for converting theoretical constructions of Transformers into practical models to facilitate empirical analysis and validation.

5.1 SUPPORTED FEATURES AND OPERATIONS

Our compiler is designed to provide an intuitive syntax resembling standard numerical array manipulation, akin to NumPy, while supporting a diverse and extensible set of abstract operations. PARAT is capable of implementing

• NumPy-like Array Syntax for indexing, arithmetic, and comparison.

Multi-Level Abstraction to enable low-level customization.
 Multi-stage Evaluation Mechanisms to facilitate debugging and error localization

• **High Extensibility** through structured class inheritance, promoting the addition of new features and operations.

Each intermediate "variable" is an instance of the SOp base class (name adapted from Lindner et al. (2023)), and each instance sop of SOp is assigned a dimension $d_{\text{sop}} \in \mathbb{N}^+$ and can be viewed as an abstract representation of an $\mathbb{R}^{n \times d_{\text{sop}}}$ array where n is the number of tokens in the input to the Transformer model. A PARAT "program" is basically a sequence of array operations over SOps.

Throughout this section, we refer to the indices along the first dimension of an SOp as "position" and refer to indices along the second dimension as "dimension".

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The "inputs" to a program are arbitrary positional encoding and token embedding SOps, represented by the base class names PosEncSOp and TokEmbSOp respectively. For example, the OneHotTokEmb class represents the one-hot embedding of tokens and Indices represents the numerical value of the index of each position.

The rest of the program performs various operations that compute new SOps based on existing ones. We provide implementations of basic building block operations including (but not limited to) the following:

- Mean (q, k, v) Represents the "Averaging Hard Attention" operation. At each position i, this operation identifies all target positions j with the maximal value of $q_i^{\top}k_j$ for $j \leq i$ and computes the average of the corresponding v_j values.
- sop[idx, :] Performs indexing using a one-dimensional index array idx, producing an SOp out such that out[i,j] = sop[idx[i], j] for $i \in [n]$ and $j \in [d_{sop}]$. This mirrors NumPy's array indexing semantics.
- sop[:, start:end] Extracts a slice of dimensions from sop, where start, end $\in [d_{\text{sop}}]$, resulting in a new SOp of dimension end start. This operation is analogous to NumPy slicing.
- Element-wise operations such as sop1 + sop2, sop1 sop2, sop1 * sop2, logical operations (& for AND, | for OR), and comparison operations (\geq , \leq , >, <), following standard broadcasting rules.

As an illustrative example, the following function returns a one-dimensional SOp representing the position index of the closest token within a set of target tokens:

```
348 1
       def nearest_token_id(tok_emb: OneHotTokEmb, vocab: List[str],
                             targets: List[str], indices: Indices=indices):
349 2
350 <sup>3</sup>
           # Get the token ids of the target tokens
           target_tok_ids = [vocab.index(target) for target in targets]
351
            # Get whether the current token is one of the target tokens
352
            # by summing the one-hot embedding
353 7
           target_token_embs = Concat([tok_emb[:, target_tok_id]
                                         for target_tok_id in target_tok_ids])
354 8
           in_targets = target_token_embs.sum(axis=1)
355 <sup>9</sup>
            # Filter the indices to only include the target tokens
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           filtered_index = indices * in_targets
357 12
           return filtered_index.max()
```

We present our full code implementing our construction for Theorem 4.5 using PARAT in Appendix D.

5.2 THE COMPILATION PROCESS

PARAT takes in a SOp that contains the computational graph of the algorithm and outputs a PyTorch (Paszke et al. (2017)) model. The compilation process follows stages similar to those of Tracr:

- 1. **Computational Graph Construction**: When a user writes sop operations, each operation automatically creates a dependency tree of all operations required for computing the resulting sop value.
- 2. **Reduction to Base Operations:** Each sop operation is reduced to one of 5 base classes: SelfAttention for operation that requires information from other token positions, GLUMLP for non-linear local operations, Linear for linear local operations, PosEncSOp for positional encodings, or TokEmbSOp for token embeddings. Sequential Linear operations are reduced to a single operation through matrix multiplication and dependency merging.
- 3. **Allocation of Layers and Residual Stream:** The computational graph is topologically sorted such that each sop appears later than its dependencies. This sorting is then used to assign SelfAttention and GLUMLP sops to Transformer layer numbers that comply

with dependency constraints. Furthermore, each non-Linear sop is also allocated a portion of the residual stream equal to their d_{sop} size.

4. **Model Construction and Weight Assignment:** A PyTorch model is initialized based on the number of required layers, hidden size, and embedding size inferred from the previous steps. The computed weights for each sop are assigned to different model components based on their types. Notably, each SelfAttention sop corresponds to an attention head, and each GLUMLP sops corresponds to part of a MLP layer with ReGLU activation.

Soft vs Hard Attention The reduction of Mean to SelfAttention induces inevitable numerical errors due to Mean representing averaging a strict subset of previous positions while SelfAttention computes a weighted average over all previous positions via softmax. This error also affects other operations based on Mean such as position indexing. We control this error via an "exactness" parameter β that scales the attention logits, and Lemma C.6 shows that the error decreases exponentially w.r.t. β .

Multi-Stage Evaluation To facilitate debugging, PARAT allows 3 types of evaluations for every sop at different stages of compilation.

• sop.abstract_eval (tokens) evaluates sop on a sequence of input tokens without any numerical errors. This can be used to validate the correctness of the algorithm implementation as sop operations.

• sop.concrete_eval (tokens) evaluates sop on an input sequence after reducing to the base classes at step 2 of the compilation process. This helps localize errors stemming from incorrect reduction of high-level operations to base classes.

 Model evaluation This corresponds to evaluating the Pytorch model after the full compilation process.

5.3 Analysis of the Compiled SAT-Solving Model

With PARAT, we successfully compiled our theoretical construction in Theorem 4.5 using the code in Appendix D. For p=20 number of variables, the resulting Transformer has 7 layers, 5 attention heads, 502 embedding dimensions, and 5011862 parameters. With a concrete implementation of our theoretical construction in PyTorch, we empirically investigate 3 questions (1) Does the compiled model correctly decide SAT instances? (2) How many steps does the model take to solve actual 3-SAT instances? (3) How does error induced by soft attention affect reasoning accuracy? These questions reveal further insights that are not available by observing the theoretical constructions alone and demonstrate the additional values provided by PARAT.

Evaluation Datasets We evaluate our models on randomly sampled DIMACS encoding of 3-SAT formulas. We focus on SAT formulas with exactly 3 literals in each clause, with the number of clauses c between 4.1p and 4.4p, where p is the number of variables.

It is well-known that the satisfiability of such random 3-SAT formulas highly depends on the clause/variable ratio, where a formula is very likely satisfiable if $c/p \ll 4.26$ and unsatisfiable if $c/p \gg 4.26$ (Crawford & Auton (1996)). This potentially allows a model to obtain high accuracy just by observing the statistical properties such as the c/p ratio. To address this, we constrain this ratio for all formulas to be near the critical ratio 4.26. Furthermore, our "marginal" datasets contain pairs of SAT vs UNSAT formulas that differ from each other by only a single literal. This means that the SAT and UNSAT formulas in the dataset have almost no statistical difference in terms of c/p ratio, variable distribution, etc., ruling out the possibility of obtaining SAT vs UNSAT information solely via statistical properties.

We also use 3 different sampling methods to generate formulas of different solving difficulties to evaluate our model:

• Marginal: Composed of pairs of formulas that differ by only one token.

• Random: Formulas are not paired by differing tokens and each clause is randomly generated.

• **Skewed:** Formulas where polarity and variable sampling are not uniform; For each literal, one polarity is preferred over the other. Some literals are also preferred over others.

We generate the above 3 datasets for each variable number $4 \le p \le 20$, resulting in 51 total datasets of 2000 samples each. Each sample with p variables contains 16.4p to 17.6p input tokens, which is at least 320 for p=20.

Model Unless otherwise stated, the model we experiment with is compiled from the code in D using PARAT with max number of variables p=20, max number of clauses c=88, and exactness parameter $\beta=20$. The model uses greedy decoding during generation.

Accuracy Our compiled model achieves perfect accuracy on all evaluation datasets described above. This provides empirical justification for our theoretical construction for Theorem 4.5 as well as PARAT. This result is included in Figure 2 to compare with trained models.

How many steps? We perform experiments to measure the empirical Chain-of-Thought length required for solving SAT formulas of different sizes. For all formulas we evaluated, the maximum CoT length is bounded by $8p \cdot 2^{0.08p}$, which is significantly less than the theoretical bound of $p \cdot 2^{(p+1)}$. This indicates that the model can use deduction to reduce the search space significantly. The figure illustrating the results is in Appendix Figure 3.

Effect of Soft Attention In our previous evaluations, we used a sufficiently large "exactness" value β to ensure that the error from MEAN based operations does not affect the final output of greedy sampling. The use of "Averaging Hard Attention" is prevalent in previous works on theoretical construction. However, how exactly does soft-attention affect the final reasoning output?

In Figure 4 we present the SAT/UNSAT prediction accuracy for models under 8 different "mean exactness" β values on our "marginal" datasets ranging from 2.5 to 20. Recall that β controls how the well soft attention approximates "hard" attention in each self-attention layer. Our results demonstrate that longer inputs generally require larger β values to achieve high accuracy. This may explain why Transformers fail to learn generalizable algorithmic procedures, as the attention learned on smaller formulas may be too "soft" to generalize to larger inputs.

6 CAN TRANSFORMER LEARN SAT SOLVING FROM DATA?

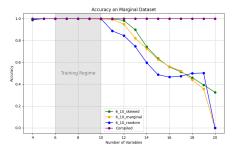
Our previous sections showed that Transformer and weights exist for solving SAT instances using CoT with backtracking and deduction. However, it is unclear to what extent Transformers can learn such formal reasoning procedures by training on SAT formulas. Previously, Zhang et al. (2023) showed that when using a single pass of a Transformer model (without CoT), Transformers fail to generalize to logical puzzles sampled from different distributions even when they have the same number of propositions.

This section provides proof-of-concept evidence that training on the Chain-of-Thought procedure with deduction and backtracking described in Figure 1 can facilitate Out-of-Distribution generalization within the same number of variables.

Datasets In Section 5.3 we introduced 3 different distributions over random 3-SAT formulas of varying difficulties. For training data, we use the same sampling methods, but instead of having a separate dataset for each variable number p, we pick 2 ranges $p \in [6,10]$ and $p \in [11,15]$, where for each sample a random p value is picked uniformly random from the range. Each formula with p variables contains 16.4p to 17.6p tokens. This results in 2×3 training datasets, each containing 5×10^5 training samples¹, with balanced SAT vs UNSAT samples. For each formula, we generate the corresponding chain of thought in the same format as Figure 1 using a custom SAT Solver. The evaluation data is exactly the same as Section 5.3.

Model and Training We use the LLaMa (Touvron et al. (2023)) architecture with 70M and 160M parameters for the training experiments, which uses Rotary Positional Encodings (RoPE) and SwiGLU as the activation function for MLP layers. Following prior works (Feng et al. (2023)), we compute cross-entropy loss on every token in the CoT but not the DIMACS encoding in the prompt tokens. We provide further training details in Appendix A. We also permute the variable IDs for training samples to ensure that the model sees all possible input tokens for up to 20 variables.

¹The number of training samples is negligible compared to the total number of possible formulas. Note that the number of clauses is at least 4p, each clause contains 3 literals and each literal has at least p choices. This results in p^{12p} possibilities, which is $> 10^{56}$ for p=6



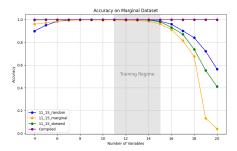


Figure 2: Result of the Length generalization experiments, showing SAT/UNSAT prediction accuracy of Transformer-LLM trained on the marginal, random, and skewed dataset on the marginal dataset over 4-20 variables. Left: model trained on 6-10 variables. Right: model trained on 11-15 variables.

6.1 Intra-length OOD Generalization

Table 1: Average accuracies (%) of SAT/UNSAT prediction for models trained and tested on different datasets in the training regime for number of variables $p \in [6, 10]$ and $p \in [11, 15]$. Columns denote train datasets, and rows denote test datasets. Each accuracy is computed over 10000 total samples.

	$p \in [6, 10]$			$p \in [11, 15]$		
	Marginal	Random	Skewed	Marginal	Random	Skewed
Marginal	99.88%	99.99%	99.99%	98.66%	99.70%	99.57%
Random	99.96%	100.00%	100.00%	99.11%	99.75%	99.55%
Skewed	99.96%	100.00%	99.99%	99.41%	99.74%	99.48%

Our first set of experiments evaluates the model's performance on SAT formulas sampled from different distributions from training, but the number of variables in formulas remains the same $(p \in [6, 10] \text{ and } p \in [11, 15] \text{ for both train and test datasets}).$

As shown in Table 1, our trained models achieve near-perfect SAT vs UNSAT prediction accuracy when tested on the same number of variables as the training data, even when on formulas sampled from different distributions. Recall that the "marginal" dataset has SAT vs UNSAT samples differing by a single token (out of at least 16p tokens in the input formula), which minimizes statistical evidence that can be used for SAT/UNSAT prediction. Our experiments suggest that the LLM have very likely learned general reasoning procedures using CoT that can be applied to all formulas with the same number of variables as the data they are trained on.

6.2 LIMITATIONS IN LENGTH GENERALIZATION

The second experiment evaluates the model's ability to generalize to formulas with a different number of variables than seen during training. We use the model trained on the Marginal dataset from section 6.1 and evaluate datasets with 4-20 variables, generated using the three methods described, with 2,000 samples each. For this experiment, we evaluate the binary SAT vs UNSAT prediction accuracy.

Results In Figure 2, our results indicate that performance degrades drastically beyond the training regime when the number of variables increases. This shows that the model is unable to learn a general SAT-solving algorithm that works for all inputs of arbitrary lengths, which corroborates our theoretical result where the size of the Transformer for SAT-solving depends on the number of variables. This further demonstrates the value of having a compiled Transformer that provably works well on all inputs up to p variables for any given p.

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A TRAINING DETAILS

We use Llama Touvron et al. (2023) models in the HuggingFace library. For the 70M model, we use models with 6 layers, 512 embedding dimensions, 8 heads, 512 attention hidden dimensions, and 2048 MLP hidden dimensions. For the 140M model, we use 12 layers, 768 embedding dimensions, 12 heads, 768 attention hidden dimensions, and 3072 MLP hidden dimensions. Both models have 850 context size. We trained for 5 epochs on both datasets using the Adam optimizer with a scheduled cosine learning rate decaying from 6×10^{-4} to 6×10^{-5} with $\beta_1 = 0.9$ and $\beta_2 = 0.95$.

B ADDITIONAL EXPERIMENT RESULTS

In Figure 3 we provide results on the number of Chain-of-Thought tokens required to solve randomly generated SAT instances. In Figure 4 we provide results on how the SAT/UNSAT prediction accuracy is affected by numerical errors introduced by softmax.

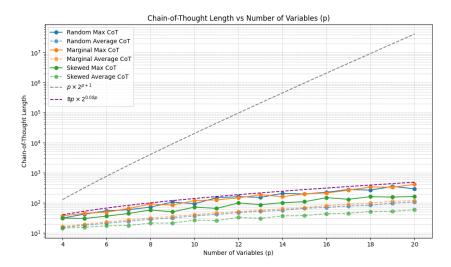


Figure 3: Chain-of-Thought Lengths generated by the compiled SAT-Solver Model vs the number of boolean variables in sampled SAT formulas, y-axis in log scale. Solid lines denote the maximum CoT length for each dataset while opaque, dashed lines denote the average CoT length. The empirical maximum CoT length in our datasets is bounded by $8p \cdot 2^{0.08p}$

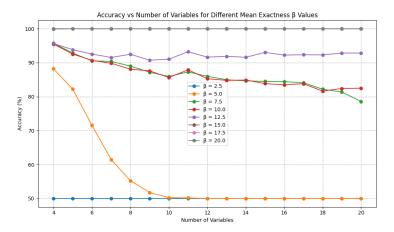


Figure 4: The impact of soft attention in Transformer layers on the SAT/UNSAT prediction accuracy. β is a scaling factor that allows the soft attention operation to better simulate hard attention at the cost of larger model parameter values in attention layers. The model achieves perfect accuracy on all "marginal" datasets starting at $\beta=17.5$, and for lower β values, accuracy is negatively correlated with the number of variables in the datasets.

C PROOFS

C.1 NOTATION DETAILS

3-SAT SAT problems where the Boolean formula is expressed in conjunctive normal form (CNF) with three literals per clause will be referred to as *3-SAT*. A formula in CNF is a conjunction (i.e. "AND") of clauses, a **clause** is a disjunction (i.e. "OR") of several **literals**, and each literal is either a variable or its negation. In the case of 3-SAT, each clause contains at most three literals. An example 3-SAT formula with 4 variables and 6 clauses is:

$$(x_1 \vee \neg x_2) \wedge (\neg x_1 \vee x_2 \vee \neg x_3) \wedge (x_2 \vee x_4 \vee \neg x_1) \wedge (x_1 \vee \neg x_3 \vee x_4) \wedge (\neg x_2 \vee \neg x_3 \vee \neg x_4) \wedge (\neg x_4 \vee \neg x_1)$$

In the above formula, $(x_1 \vee \neg x_2)$ is a clause, which contains the literals x_1 and $\neg x_2$.

The 3-SAT problem refers to determining if any assignment of truth values to the variables allows the formula ϕ to evaluate as true. It is well-known that 3-SAT is NP-hard and is widely believed to be unsolvable in polynomial time.

DIMACS Encoding The DIMACS format is a standardized encoding scheme for representing Boolean formulas in conjunctive normal form (CNF) for SAT problems. Each clause in the formula is represented as a sequence of integers followed by a terminating "0" (i.e. "0" represents \land symbols and parentheses). Positive integers correspond to variables, while negative integers represent the negations of variables. For instance, if a clause includes the literals x_1 , $\neg x_2$, and x_3 , it would be represented as "1 -2 3 0" in the DIMACS format.

For the 3-SAT example in the previous paragraph, the corresponding DIMACS representation would be:

C.2 USEFUL LEMMAS FOR TRANSFORMERS

In this section, we present adapted versions of several lemmas from Feng et al. (2023). Specifically, an MLP with ReGLU can exactly simulate ReLU, linear operations, and multiplication without error. For Self-attention lemmas, we directly adapt from Feng et al. (2023).

C.2.1 LEMMAS FOR MLP WITH REGLU ACTIVATION

This section shows several lemmas showing the capabilities of the self-attention operation and MLP layers to approximate high-level vector operations. These high-level operations are later used as building blocks for the Transformer SAT-solver. Specifically, with appropriate weight configurations, a 2-layer MLP with ReGLU activation $f(x) = W_2[(W_1x + b) \otimes \operatorname{relu}(Vx + c)]$ can approximate the following vector operations for arbitrary input x:

- Simulate a 2-layer MLP with ReLU activation: $W_2 \operatorname{ReLU}(W_1'x + b_1') + b_2'$
- Simulate any linear operation $\boldsymbol{W} \boldsymbol{x}$
- Simulate element-wise multiplication: $oldsymbol{x}_1 \otimes oldsymbol{x}_2$

Lemma C.1 (Simulating a 2-Layer ReLU MLP with ReGLU Activation). A 2-layer MLP with ReGLU activation function can simulate any 2-layer MLP with ReLU activation function.

Proof. Let the ReLU MLP be defined as:

$$g(\mathbf{x}) = \mathbf{W}_2' \operatorname{ReLU}(\mathbf{W}_1'\mathbf{x} + \mathbf{b}_1') + \mathbf{b}_2'.$$

Set the weights and biases of the ReGLU MLP as follows:

$$egin{aligned} m{W}_1 &= m{0}, & m{b}_1 &= m{1}, \ m{V} &= m{W}_1', & m{b}_2 &= m{b}_1', \ m{W}_2 &= m{W}_2', & m{b} &= m{b}_2'. \end{aligned}$$

Then, the ReGLU MLP computes:

$$f(x) = W_2' [(\mathbf{0} \cdot x + \mathbf{1}) \otimes \text{ReLU}(W_1'x + b_1')] + b_2'.$$

Simplifying:

$$f(x) = W_2' [1 \otimes \text{ReLU}(W_1'x + b_1')] + b_2' = W_2' \text{ReLU}(W_1'x + b_1') + b_2' = g(x).$$

Thus, the ReGLU MLP computes the same function as the ReLU MLP.

Lemma C.2 (Simulating Linear Operations with ReGLU MLP). A 2-layer MLP with ReGLU activation can compute any linear operation f(x) = Wx + b.

Proof. To compute a linear function using the ReGLU MLP, we can set the activation to act as a scalar multiplier of one. Set the weights and biases as:

$$egin{aligned} m{W}_1 &= m{W}, & m{b}_1 &= m{b}, \ m{V} &= m{0}, & m{b}_2 &= m{1}, \ m{W}_2 &= m{I}, & m{b} &= m{0}. \end{aligned}$$

Here, I is the identity matrix.

Since $Vx + b_2 = b_2 = 1$, we have:

$$ReLU(Vx + b_2) = ReLU(1) = 1.$$

Then, the ReGLU MLP computes:

$$f(x) = I[(Wx + b) \otimes 1] = Wx + b.$$

Thus, any linear operation can be represented by appropriately setting W_1 , b_1 , and W_2 .

Lemma C.3 (Element-wise Multiplication via ReGLU MLP). A 2-layer MLP with ReGLU activation can compute the element-wise multiplication of two input vectors \mathbf{x}_1 and \mathbf{x}_2 , that is,

$$f(\boldsymbol{x}) = \boldsymbol{x}_1 \otimes \boldsymbol{x}_2,$$

where $\mathbf{x} = [\mathbf{x}_1; \mathbf{x}_2]$ denotes the concatenation of \mathbf{x}_1 and \mathbf{x}_2 .

Proof. Let $\boldsymbol{x} = [\boldsymbol{x}_1; \boldsymbol{x}_2] \in \mathbb{R}^{2n}$, where $\boldsymbol{x}_1, \boldsymbol{x}_2 \in \mathbb{R}^n$.

Set the weights and biases:

$$egin{aligned} m{W}_1 &= egin{bmatrix} m{I}_n \ m{I}_n \end{bmatrix}, & m{b}_1 &= m{0}_{2n}, \ m{V} &= egin{bmatrix} m{I}_n \ -m{I}_n \end{bmatrix}, & m{b}_2 &= m{0}_{2n}, \ m{W}_2 &= m{I}_n & -m{I}_n \end{bmatrix}, & m{b} &= m{0}_n. \end{aligned}$$

Compute:

$$egin{aligned} oldsymbol{W}_1 oldsymbol{x} + oldsymbol{b}_1 &= egin{bmatrix} oldsymbol{x}_1 \ oldsymbol{x} + oldsymbol{b}_2 &= egin{bmatrix} oldsymbol{x}_2 \ -oldsymbol{x}_2 \end{bmatrix}, \ & ext{ReLU}(oldsymbol{V} oldsymbol{x} + oldsymbol{b}_2) &= egin{bmatrix} ext{ReLU}(oldsymbol{x}_2) \ ext{ReLU}(-oldsymbol{x}_2) \end{bmatrix}. \end{aligned}$$

The element-wise product:

$$(oldsymbol{W}_1oldsymbol{x}$$
 -

 $(\boldsymbol{W}_1 \boldsymbol{x} + \boldsymbol{b}_1) \otimes \operatorname{ReLU}(\boldsymbol{V} \boldsymbol{x} + \boldsymbol{b}_2) = \begin{bmatrix} \boldsymbol{x}_1 \otimes \operatorname{ReLU}(\boldsymbol{x}_2) \\ \boldsymbol{x}_1 \otimes \operatorname{ReLU}(-\boldsymbol{x}_2) \end{bmatrix}.$

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Compute the output:

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 $f(\boldsymbol{x}) = \boldsymbol{W}_2 \left[(\boldsymbol{W}_1 \boldsymbol{x} + \boldsymbol{b}_1) \otimes \text{ReLU}(\boldsymbol{V} \boldsymbol{x} + \boldsymbol{b}_2) \right] + \boldsymbol{b}$ $= \boldsymbol{x}_1 \otimes \operatorname{ReLU}(\boldsymbol{x}_2) - \boldsymbol{x}_1 \otimes \operatorname{ReLU}(-\boldsymbol{x}_2)$ $= \boldsymbol{x}_1 \otimes (\text{ReLU}(\boldsymbol{x}_2) - \text{ReLU}(-\boldsymbol{x}_2))$

 $= \boldsymbol{x}_1 \otimes \boldsymbol{x}_2.$

Thus, the ReGLU MLP computes $f(x) = x_1 \otimes x_2$ without restrictions on x_2 .

C.2.2 Capabilities of the Self-Attention Layer

In this subsection, we provide 2 core lemmas on the capabilities of the self-attention layer from Feng et al. (2023).

Let $n \in \mathbb{N}$ be an integer and let x_1, x_2, \dots, x_n be a sequence of vectors where $x_i = (\tilde{x}_i, r_i, 1) \in$ $[-M,M]^{d+2}, \tilde{x}_i \in \mathbb{R}^d, r_i \in \mathbb{R}$, and M is a large constant. Let $K, Q, V \in \mathbb{R}^{d' \times (d+2)}$ be any matrices with $\|V\|_{\infty} \le 1$, and let $0 < \rho, \delta < M$ be any real numbers. Denote $q_i = Qx_i$, $k_i = Kx_i, v_j = Vx_j$, and define the matching set $S_i = \{j \leq i : |q_i \cdot k_j| \leq \rho\}$. Equipped with these notations, we define two basic operations as follows:

- COPY: The output is a sequence of vectors u_1, \dots, u_n with $u_i = v_{pos(i)}$, where $pos(i) = v_{pos(i)}$ $\operatorname{argmax}_{j \in \mathcal{S}_i} r_j$.
- MEAN: The output is a sequence of vectors u_1, \dots, u_n with $u_i = \text{mean}_{i \in S_i} v_i$.

Assumption C.4. [Assumption C.6 from Feng et al. (2023)] The matrices Q, K, V and scalars ρ, δ satisfy that for all considered sequences x_1, x_2, \dots, x_n , the following hold:

- For any $i, j \in [n]$, either $|\mathbf{q}_i \cdot \mathbf{k}_j| \le \rho$ or $\mathbf{q}_i \cdot \mathbf{k}_j \le -\delta$.
- For any $i, j \in [n]$, either i = j or $|r_i r_j| > \delta$.

Assumption C.4 says that there are sufficient gaps between the attended position (e.g., pos(i)) and other positions. The two lemmas below show that the attention layer with casual mask can implement both COPY operation and MEAN operation efficiently.

Lemma C.5 (Lemma C.7 from Feng et al. (2023)). Assume Assumption C.4 holds with $\rho \leq \frac{\delta^2}{8M}$. For any $\epsilon > 0$, there exists an attention layer with embedding size O(d) and one causal attention head that can approximate the COPY operation defined above. Formally, for any considered sequence of vectors x_1, x_2, \ldots, x_n , denote the corresponding attention output as o_1, o_2, \ldots, o_n . Then, we have $\|o_i - u_i\|_{\infty} \le \epsilon$ for all $i \in [n]$ with $S_i \ne \emptyset$. Moreover, the ℓ_{∞} norm of attention parameters is bounded by $O(\text{poly}(M, 1/\delta, \log(n), \log(1/\epsilon)))$.

Lemma C.6 (Lemma C.8 from Feng et al. (2023)). Assume Assumption C.4 holds with $\rho \leq$ $\frac{\delta \epsilon}{16M \ln(\frac{4Mn}{\epsilon})}$. For any $0 < \epsilon \le M$, there exists an attention layer with embedding size O(d) and one causal attention head that can approximate the MEAN operation defined above. Formally, for any considered sequence of vectors x_1, x_2, \ldots, x_n , denote the attention output as o_1, o_2, \ldots, o_n . Then, we have $\|\mathbf{o}_i - \mathbf{u}_i\|_{\infty} \le \epsilon$ for all $i \in [n]$ with $\mathcal{S}_i \ne \emptyset$. Moreover, the ℓ_{∞} norm of attention parameters is bounded by $O(\text{poly}(M, 1/\delta, \log(n), \log(1/\epsilon)))$.

C.3 LEMMAS ON SAT SOLVING OPERATIONS IN TRANSFORMERS

In this section, we describe the implementation of critical operations in SAT solving as Transformer Operations. For simplicity, we state these operations using self-attention operations with "Average Hard Attention"

Definition C.7 (Average-Hard Attention (AHA), adapted from Merrill et al. (2022); Strobl (2023)). $s = s_1 \cdots s_n \in \mathbb{R}^+$, let $\mathcal{M}(s) = \{i \in [n] \mid s_i = \max_{j \in [n]} s_j \}$. The average-hard attention function ξ maps s to the probability distribution $\xi(s): [n] \to [0,1]$ given by

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$$\xi(s)_i = \begin{cases} 1/\mathcal{M}(s) & \text{if } i \in \mathcal{M}(s) \\ 0 & \text{otherwise.} \end{cases}$$

Leyan TODO: Add AHA head definition after finishing theorems! For the following theorems and definitions, we consider 3-SAT formulas with at most p variables:

Here we describe binary vector "Encodings" of clauses and partial assignments with informative dot products:

Definition C.8 (Encoding of clause). Let C be a clause with L(C) literals. Define encoding $E(C) \in \{0,1\}^{2p}$ of clause C as the following: For $i \in [p]$, $E(C)_i = 1$ iff x_i is a literal in C, and $E(C)_{p+i} = 1$ iff $\neg x_i$ is a literal in C. All positions in E(C) are 0 otherwise.

Definition C.9 (Encoding of partial assignment). Let $A:\{x_1,\ldots,x_p\}\to\{\text{True},\text{False},\text{None}\}$ be a partial assignment. Define encoding $E(A)\in\{0,1\}^{2p}$ of clause C as the following: For $i\in[p]$, $E(A)_i=1$ iff $A(x_i)=\text{True}$, and $E(A)_{p+i}=1$ iff $A(x_i)=\text{False}$. All positions in E(C) are 0 otherwise.

Assuming that we have successfully obtained encodings of every clause $E(C_1), E(C_2), \ldots, E(C_c)$ as a part of the residual stream as well as the current partial assignment E(A), we show that a single attention head can perform model checking, conflict detection, and unit propagation in parallel

Lemma C.10.

C.4 THEORETICAL CONSTRUCTION

Preprint Note: We're in the process of reformatting the construction and proof for better organization

Notations

- p denotes the number of variables
- t_i denotes the token at position i
- T_{vars} denotes the set of tokens that denote variables and their negations. i.e. '1', '2', ..., 'n', '-1', '-2', ..., '-n'
- b denotes boolean variables

Proof. We first describe the encoding format of the formulas and the solution trace format before going into the details of model construction.

Input Format. We consider 3-CNF-SAT formulas in the DIMACS representation, with an initial [BOS] token and an ending [SEP] token. Each variable x_i for $i \in [n]$ has 2 associated tokens: i and -i (e.g., 1 and -1), where the positive token indicates that the i-th variable appears in the clause while the negative token indicates that the negation of the i-th variable appears in the clause are separated using the 0 token. For example, the formula

$$(\neg x_2 \lor \neg x_4 \lor \neg x_1) \land (x_3 \lor x_4 \lor \neg x_1) \land (\neg x_1 \lor \neg x_3 \lor \neg x_2)$$
$$\land (x_1 \lor \neg x_2 \lor \neg x_4) \land (\neg x_4 \lor x_2 \lor x_1) \land (x_1 \lor \neg x_2 \lor x_4)$$

would be represented as:

Solution Trace Format. The trace keeps track of the order of the assignments made and whether each assignment is a decision (assumption) or a unit propagation (deduction). Literals with a preceding D token are decision literals while other literals are from unit propagation. When the model encounters a conflict between the current assignment and the formula, it performs a backtrack operation denoted by [BT] and performs another attempt with the last decision literal negated. In particular, compared to Figure 1, we used D to abbreviate Assume and use [BT] to abbreviate Backtrack

As an example, the solution trace for the above SAT formula would be:

927 [

```
[SEP] D 2 D 1 -4 3 [BT] D 2 D -1 -4 [BT] -2 D 3 D 4 -1 SAT
```

Embedding Layer. Our token set consists of one token for each variable and its negation, the separator token , and a special token D to denote where decisions are made. The positional encoding occupies a single dimension and contains the numerical value of the position of the token in the string. (i.e. there exists a dimension pos such that the position embedding of position i is $i \cdot e_{pos}$)

Layer 1. The first layer prepares for finding the nearest separator token and D token. Let i denote the position index of tokens:

- 1. Compute i_{sep} where $i_{\text{sep}} = i$ if the corresponding token $t_i \in \{\text{`0'}, \text{`[SEP]'}, \text{`[BT]'}\}$ and $i_{\text{sep}} = 0$ otherwise
- 2. Similarly, compute i_D where $i_D = i$ if the corresponding token $t_i = D$ and $i_{sep} = 0$ otherwise.
- 3. Compute $(i-1)^2$, i^2 for index equality comparison

The first 2 operations can both be computed using a single MLP layer that multiplies between i from the positional encoding using Lemma C.3. Similarly, the 3rd operation is a multiplication operation that can be performed with Lemma C.3.

Layer 2. This layer uses 2 heads to perform the following tasks:

1. Copy the index and type of the last separator token and stores

$$\begin{aligned} p_i^{sep\prime} &= \max\{j: j \leq i, t_j \in \{\text{`O', `[SEP]', `[BT]'}\}\} \\ b_0 &= (t_j = \text{`O'}) \\ b_{\text{[SEP]}} &= (t_j = \text{`[SEP]'}) \\ b_{\text{[BT]}} &= (t_j = \text{`[BT]'}) \end{aligned}$$

for
$$j = p_i^{sep}$$

- 2. (Backtrack) Compute the position of the nearest D token $p_i^{\rm D}=\max\{j:j\leq i,t_j=\text{`D'}\}$

3. Compute
$$(p_i^{sep_I})^2$$
 for index operation

Task 1 can be achieved via the COPY operation from Lemma C.5 with $\boldsymbol{q}_i=1,\,\boldsymbol{k}_i=i_{\mathrm{sep}},\,\boldsymbol{v}_j=(j,\mathbb{I}[t_j=\text{`[]']},\mathbb{I}[t_j=\text{`[]UP]']},\mathbb{I}[t_j=\text{`[BackTrack]']}).$

Task 2 is highly similar to task 1 and can be achieved using COPY with $m{q}_i=1, m{k}_i=i_{\scriptscriptstyle \mathbb{D}}, m{v}_j=(j)$

Task 3 is a multiplication operation that can be performed using Lemma C.3.

 Layer 3 This layer uses 1 head to copy the several values from the previous token to the current token. Specifically, this layer computes:

1. The position of the *previous* separator token, not including the current position:

$$p_i^{sep} = \max\{j: j < i, t_j \in \{\texttt{`0'}, \texttt{`[SEP]',`[UP]'}, \texttt{`[BackTrack]'}\}\}$$

- 2. Dermine if the previous token is $D: b_{decision} = (t_{i-1} = `D')$ i.e., whether the current token is a decision variable
- 3. (Induction) Compute the offset of the current token to the previous separator token $d_i^{sep} = i p_i^{sep\prime}$
- 4. Compute $(p_i^{sep})^2$, for equality comparison at the next layer.

Task 1 and 2 is done by copying $p_i^{sep\prime}$ and $\mathbb{I}[t_i=\text{`D'}]$ from the previous token. Specifially, we use the COPY operation from Lemma C.5 with $\mathbf{q}_i=((i-1)^2,i-1,1)$ and $\mathbf{k}_j=(-1,2j,-j^2)$ which determines i-1=j via $-((i-1)-j)^2=0$ and $\mathbf{v}_j=(p_i^{sep\prime},\mathbb{I}[t_i=\text{`D'}])$. Task 4 is a local multiplication operation that can be implemented via Lemma C.3.

Layer 4. This layer uses 2 heads to perform the following tasks:

1. Compute the sum of all variable token embeddings after the previous separator to encode a vector representation of assignments and clauses at their following separator token.

$$\mathbf{r}_i = \sum_{j > p_i^{sep}, t_j \in T_{vars}} \mathbf{e}_{id(t_j)} = \sum_{p_j^{sep} = p_i^{sep}, t_j \in T_{vars}} \mathbf{e}_{id(t_j)}$$

- 2. (Induction) Compute the position of the second-to-last separator $p_i^{sep} = \max\{j: j < p_i^{sep}, t_j \in \{\text{`0', `[SEP]', `[UP]', `[BackTrack]'}\}\} = p_{p_i^{sep}}^{sep}$, and the corresponding current position in the previous state $p_i^- = p_i^{sep} + d_i^{sep}$. As a special case for the first state, we also add 4 to p_i^- if $b_{\text{[SEP]}}$ is true, i.e. $p_i^- = p_i^{sep} + d_i^{sep} + 4 \cdot b_{\text{[SEP]}}$. The additional 4 is the number of variables per clause + 1 to ensure that we don't consider the last clause as an assignment.
- 3. (Backtrack) Compute the position of the nearest D token to the last separator token $p_i^{\mathbb{D}^-}=p_{p_i^{\mathrm{Sep}},}^{\mathbb{D}}$
- 4. Compute $b_{exceed} = (p_i^- > p_i^{--} + 1)$, this denotes whether we're beyond the last decision of the previous state.
- 5. Compare $(p_i^{\mathbb{D}^-} \leq p_i^-)$ for $b_{\text{BT_finished}}$ at the next layer.
- 6. Compare if $p_i^{\text{D-}}=p_i^-$ for the $b_{backtrack}$ operator.
- 7. Compute $b'_{copy} = (p_i^- < p_i^{sep} 1)$

Task 1 is achieved using a MEAN operation with $\mathbf{q}_i = ((p_i^{sep})^2, p_i^{sep}, 1), \ \mathbf{k}_j = ((-1, 2p_j^{sep}, -(p_j^{sep})^2), \ \mathbf{v}_j = \mathbf{e}_{id(t_j)} \text{ for } t_j \in T_{vars}.$ This attention operations results in $\frac{\mathbf{r}_i}{i-p_i^{sep}}$ The MLP layer then uses Lemma C.3 to multiply the mean result by $i-p_i^{sep}$ to obtain the \mathbf{r}_i .

Task 2 is achieved using the COPY operation with $q_i = ((p_i^{sep})^2, p_i^{sep}, 1)$, $k_j = (-1, 2j, -j^2)$ and $v_j = p_i^{sep}$. The MLP layer then performs the addition operation the computes p_i^- by Lemma C.2

Similarly, Task 3 is achieved using the COPY operation with $q_i = ((p_i^{sep})^2, p_i^{sep}, 1), k_j = (-1, 2j, -j^2)$ and $v_j = p_i^{\mathbb{D}}$.

Layer 5. The third layer uses 5 heads to perform the following tasks:

- 1. Determine whether the current assignment \mathbf{r}_i satisfies the formula. b_{sat}
- Determine whether the current assignment r_i results in a contradiction with a clause of the formula b_{cont}
- 3. Find clauses with at least 2 False literals and sum up the unassigned literals in these clauses. This would result in all the variables that can be currently determined via unit propagation. \mathbf{e}_{UP}

4. Compute $b_{final} = b_{exceed} \wedge b_{decision}$

- 5. Compare $b_{no.decision} = (p_i^{\text{D}} \leq p_i^{sep})$, which denotes whether the current state contains no decision variables
- 6. Compute $b_{\text{BT_finished}} = (p_i^{\mathbb{D}^-} \leq p_i^-) \wedge b_{[\text{BackTrack}]}$
- 7. Compare p_i^- with $p_i^{\text{D}-}-1$ by storing $p_i^- \leq p_i^{\text{D}-}-1$ and $p_i^- \geq p_i^{\text{D}-}-1$ (to check for equality at the next layer)
- 8. Compare $b_{\text{backtrack}} = (p_i^- = p_i^{D-} 1)$

To describe the operations performed in this layer, we interpret the \mathbf{r}_i vectors computed in the previous layer as a 2n-dimensional binary encoding of the clause/assignment preceding token i. The value at dimension 2j-1 is 1 if the clause/assignment contains variable j (1-indexed) in positive polarity and the value at dimension 2j is one iff the clause/assignment contains variable j is in negative polarity. For example, the clause 1-2-4 is represented as the binary vector $\mathbf{r}=[1,0,0,1,0,0,1,0]$ when the number of variables is n=4. The \mathbf{r} representation for each clause is at the '0' separator following the clause in the input format.

We now define the linear transformation $T[\mathbf{v}_{true}, \mathbf{v}_{false}, \mathbf{v}_{none}] \in \mathbb{R}^{2n \times 2n}$ where $\mathbf{v}_{true}, \mathbf{v}_{false}, \mathbf{v}_{none} \in \{(0,0), (0,1), (1,0), (1,1)\}$. The transformation takes every pair of values in \mathbf{r} corresponding to each variable, determines whether the variable is true, false, or none-existent in the clause/assignment represented by \mathbf{r} , and replaces each pair with the corresponding $\mathbf{v}_{true}, \mathbf{v}_{false}, \mathbf{v}_{none}$ value.

For example, when $\mathbf{r}=[1,0,0,1,0,0,1,0]$, applying T[(1,1),(1,0),(0,1)] will result in [1,1,1,0,0,1,1]. Also, T[(1,0),(0,1),(0,0)] is equivalent to the identity operation. Intuitively, the transformation changes 2-element binary vectors representing true, false, and non-existence within the clause/assignment. The transformation is used to construct query and key matrices to satisfy the desired properties of the assignment-clause dot product.

Parallel Deduction over Clauses Task 1 (checking satisfiability) is achieved via an MEAN Lemma C.6 with $\mathbf{q}_i = (\mathbf{r}_i, 1)$ and $\mathbf{k}_j = M(-\mathbf{r}_j, c_j^{(1)})$ and $v_j = \mathbf{1}[t_j = \text{`[BOS]']}$, where

$$c_j^{(1)} = \begin{cases} 0 & t_j = \text{`0'}, \\ -0.5 & t_j = \text{`[BOS]'}, \\ -M & otherwise \end{cases}$$

and M is a sufficiently large constant to approximate hard-max with the softmax operation.

Correctness: Consider the case where \mathbf{r}_i denotes the binary encoding of the current assignment and \mathbf{r}_j denotes the binary encoding of a clause at a '0' separator position. Then $\mathbf{r}_i \cdot \mathbf{r}_j$ denotes the number of common literals in the assignment and the clause, i.e. how many literals in the clause are True according to the assignment \mathbf{r}_i . Therefore, the clause is satisfied by the assignment ending at position i as long as $\mathbf{r}_i \cdot \mathbf{r}_j \geq 1$. Since we only consider the \mathbf{r}_j values at the '0' separators as the binary encoding of the clause, all these positions have $c_j^{(1)} = 0$. Therefore, $\mathbf{q}_i \cdot \mathbf{k}_j = -M\mathbf{r}_i \cdot \mathbf{r}_j$, which is 0 for non-satisfied clauses and <-M for satisfied clauses. Also notice that since $c_j^{(1)} = -0.5$ for j=1 (i.e. the first <code>[BOS]</code> token), so $\mathbf{q}_i \cdot \mathbf{k}_1 = -\frac{M}{2}$. If the formula is satisfied, all clauses must be satisfied, and each clause must have attention score $\mathbf{q}_i \cdot \mathbf{k}_j < -M$ while the <code>[BOS]</code> token has attention score $-\frac{M}{2}$. For sufficiently large M, we can view the softmax operation as selecting the value vector of the largest attention score item, which is <code>[BOS]</code>. Since $v_1 = \mathbf{1}[t_1 = \text{`[BOS]'} = 1$, the result of the attention head will be 1. Conversely, if at least one clause is not satisfied, then $\mathbf{q}_i \cdot \mathbf{k}_j = 0$ for that particular clause. As such, the <code>[BOS]</code> token will not be selected and the result of the attention operation will be 0.

Similarly, task 2 (Detecting Conflict) is also achieved via MEAN(Lemma C.6) with $\mathbf{q}_i = (T[(1,0),(0,1),(1,1)]\mathbf{r}_i,1)$, $\mathbf{k}_j = M(-\mathbf{r}_j,c_j^{(1)})$, $v_j = 1 - \mathbf{1}[t_j = \text{`[BOS]']}$, where the definition of M and $c_j^{(1)}$ is the same as Task 1.

For task 3 (unit propagation), apply MEAN (Lemma C.6) with $\mathbf{q}_i = (T[(0,1),(1,0),(0,0)]\mathbf{r}_i,1)$, $\mathbf{k}_j = M(\mathbf{r}_j,c_i^{(2)})$, $\mathbf{v}_j = c\mathbf{r}_j$ where

$$c_j^{(2)} = \begin{cases} 0 & t_j = \text{`0'}, \\ 1.5 & t_j = \text{`[BOS]'}, \\ -M & otherwise \end{cases}$$

Let the attention result be \mathbf{o}_{UP} . The MLP layer then computes $\mathbf{e}_{UP} = ReLU(\mathbf{o}_{UP}) - ReLU(\mathbf{o}_{UP} - 1) - T[(1,1),(1,1),(0,0)]\mathbf{r}_i$ via Lemma C.1.

Correctness: Here we show that, if the assignment at position i does not make the formula unsatisfied, then the resulting vector is approximately a binary encoding of all literals that can be unit-propagated. Consider again the case where \mathbf{r}_i denotes the binary encoding of the current assignment and \mathbf{r}_j denotes the binary encoding of a clause at a '0' separator position. Here $\mathbf{q}_i \cdot \mathbf{k}_j$ denotes M times the number of false literals in clause j according to the current assignment i. Since each clause has three variables, if a clause has three false assignments, then the formula is unsatisfied by the assignment and thus requires no further unit propagation. Therefore, we consider the case where each clause has at most 2 opposing assignments.

If there are no clauses with 2 opposing assignments, then all clause attention logits $\mathbf{q}_i \cdot \mathbf{k}_j$ will be at most M, while the attention logit to the [BOS] token will be $c_1^{(2)} = 1.5M$. Since M is a large number, most attention weights will be assigned to [BOS] after the softmax operation and result in a zero embedding vector.

If at least one clause has 2 opposing assignments, all these clauses will have attention logits $\mathbf{q}_i \cdot \mathbf{k} \approx 2M$. Therefore, the attention value will be evenly distributed on all clauses with 2 opposing assignments. The resulting attention output $\mathbf{o}_{[UP]}$ will be the *average of embedding* of all clauses with 2 opposing assignments, multiplied by c since $\mathbf{v}_j = c \cdot \mathbf{r}_j$. Since there are at most c clauses, the number of attended clauses is at most c, and the divisor when computing the average is at most c. Therefore, the resulting $\mathbf{o}_{[UP]}$ will be a embedding vector where every literal that appeared in at least one clause with 2 false literals have their corresponding position assigned to $a \ge 1$ value.

Layer 6 This layer does the remaining boolean operators required for the output. In particular,

- $b_{unsat} = b_{no_decision} \wedge b_{cont}$
- $b_{\text{[BT]}} = b_{cont} \land \neg (t_i = \text{[BT]})$
- Compute a vector that is equal to $b_{backtrack} \cdot \mathbf{e}_{BT}$, which is equal to \mathbf{e}_{BT} if $b_{backtrack}$ is True and $\mathbf{0}$ otherwise. This is to allow the operation at the output layer for backtracking

Note that \land can be implemented as a single ReLU operation for tasks 1 and 2 that can be implemented with Lemma C.1, and task 3 is a multiplication operation implemented with Lemma C.3

Layer 7 This layer performs a single operation with the MLP layer: Compute $b_{copy} \cdot e_{copy}$, which gates whether e_{copy} should be predicted based on b_{copy} . This enables condition 5 at the output layer.

Output Projection The final layer is responsible for producing the output of the model based on the computed output of the pervious layers. We constructed prioritized conditional outputs, where the model outputs the token according to the first satisfied conditional in the order below:

- 1. If b_{sat} output SAT
- 2. If $b_{cont} \wedge b_{no_decision}$ output UNSAT
- 3. If $b_{cont} \land \neg (t_i = [BackTrack])$ output '[BackTrack]'
- 4. (BackTrack) If $b_{backtrack}$, output the negation of the token from position $p_i^{D-} + 1$
- 5. (Induction) If b_{copy} , copy token from position $p_i^- + 1$ as output (e_{copy})

- 6. output a unit propagation variable, if any.
- 7. output D if the current token is not D
- 8. output a unassigned variable

For the output layer, we use $l_{\texttt{[TOKEN]}}$ to denote the output logit of <code>[TOKEN]</code>. Since the final output of the model is the token with the highest logit, we can implement output priority by assigning outputs of higher priority rules with higher logits than lower priority rules. Specifically, we compute the output logits vector using the output layer linear transformation as:

$$2^7 \cdot b_{sat} \cdot \mathbf{e}_{\text{SAT}} + 2^6 \cdot b_{cont} \cdot \mathbf{e}_{\text{[BackTrack]}} + 2^5 \cdot b_{unsat} \cdot \mathbf{e}_{\text{UNSAT}}$$

$$+2^4 \cdot b_{backtrack} \cdot \mathbf{e}_{BT} + 2^3 \cdot b_{copy} \cdot \mathbf{e}_{copy} + 2^2 \cdot \mathbf{e}_{\text{UnitPropVar}} + 2^1 \cdot (1 - \mathbf{1}[t_i = \text{`D'}]) \cdot \mathbf{e}_{\text{D}} + 2^0 \cdot T[(0, 0), (0, 0), (1, 1)] \mathbf{r}_i$$

Proposition C.11. There exists a transformer with 7 layers, 5 heads, O(p) embedding dimension, and $O(p^2)$ weights that, on all inputs $s \in \text{DIMACS}(p,c)$, predicts the same token as the output as the above operations. Furthermore, let $l_{ctx} = 4c + p \cdot 2^p$ be the worst-case maximum context length required to complete SAT-solving, then all weights are within $\text{poly}(l_{ctx})$ and can be represented within $O(p + \log c)$ bits.

We only argue from a high level why this is true due to the complexity of the construction. In the above construction, we demonstrate how each operation can be approximated by a Self-attention or MLP layer. We can set the embedding dimension to the sum of dimensions of all the intermediate values and allocate for every intermediate values a range of dimensions that's equal to the dimension of the variables. All dimensions are initialized to 0 in the positional encoding of the transformer except for the dimensions assigned to the positional index i. Similarly, only the dimensions assigned to the one-hot token representation are initialized in the token embeddings. At each layer, the self-attention heads and MLP layers extract the variable values from the residual stream and perform the operations assigned to them at each layer.

The only intermediate values whose dimensions are dependent on p are the vectors for one-hot encodings and storing binary encodings of clauses and assignments. They all have size 2p. Therefore, the number of total allocated embedding sizes is also O(p).

Furthermore, shows that all parameter values are polynomial with respect to the context length and the inverse of approximation errors. Note that we need only guarantee the final error is less than 1 to prevent affecting the output token. Furthermore, we can choose all parameter values so that they are multiples of 0.5. As such, all parameters are within $poly(l_{ctx})$ and can be represented by $O(\log(l_{ctx})) = O(p + \log c)$

C.5 CORRECTNESS

Note: This section assumes prior knowledge in propositional logic and SAT solving, including an understanding of the DPLL algorithm. For a brief explanation of the notations in this section, please refer to (Nieuwenhuis et al. (2005)). For more general knowledge, please refer to (Biere et al. (2009)).

We prove that the above model autoregressive solves 3-SAT $_{p,c}$ by showing that it uses the CoT to simulate the "Abstract DPLL Procedure".

C.5.1 ABSTRACT DPLL

In this section, we provide a description of abstract DPLL. Since the focus of this paper is not to show the correctness of the DPLL algorithm but rather how our model's CoT is equivalent to it, we only present the main results from Nieuwenhuis et al. (2005) and refer readers to the original work for proof of the theorems.

Let M be an ordered trace of variable assignments with information on whether each assignment is an *decision literal* (i.e. assumption) or an *unit propagation* (i.e., deduction).

For example, the ordered trace 3^d 1 $\overline{2}$ 4^d 5 denotes the following sequence of operations:

Assume $x_3 = T \to \text{Deduce } x_1 = T \to \text{Deduce } x_2 = F \to \text{Assume } x_4 = T \to \text{Deduce } x_5 = T.$

Let F denote the a SAT formula in CNF format (which includes 3-SAT), C denote a clause (e.g., $x_1 \vee \neg x_2 \vee x_3$), l denote a single literal (e.g., $\neg x_2$), and l^d denote a decision literal. Let $M \models F$ denote that the assignment in M satisfies the formula F.

Definition C.12 (State in the DPLL Transition System). A *state* $S \in \mathbb{S}$ in the DPLL transition system is either:

- The special states SAT, UNSAT, indicating that the formula satisfiable or unsatisfiable
- A pair $M \parallel F$, where:
 - F is a finite set of clauses $C_1 \wedge C_2 \cdots \wedge C_c$ (a conjunctive normal form (CNF) formula), and
 - M is a sequence of annotated literals $l_1 \circ l_2 \cdots \circ l_i$ for some $i \in [n]$ representing variable assignments, where \circ denotes concatenation. Annotations indicate whether a literal is a decision literal (denoted by l^d) or derived through unit propagation.

We denote the empty sequence of literals by \emptyset , unit sequences by their only literal, and the concatenation of two sequences by simple juxtaposition. While M is a sequence, it can also be viewed as a set of variable assignments by ignoring annotations and order.

Definition C.13 (Adapted from Definition 1 of Nieuwenhuis et al. (2005)). The Basic DPLL system consists of the following transition rules $\mathbb{S} \Longrightarrow \mathbb{S}$:

UnitPropagate:

$$M \, \| \, F \wedge (C \vee l) \qquad \Longrightarrow \quad M \circ l \, \| \, F \wedge (C \vee l) \qquad \text{if} \quad \begin{cases} M \models \neg C, \\ l \text{ is undefined in } M. \end{cases}$$

Decide:

$$M \parallel F \qquad \qquad \Longrightarrow \quad M \circ l^{\operatorname{d}} \parallel F \qquad \qquad \text{if} \quad \begin{cases} l \text{ or } \neg l \text{ occurs in a clause of } F, \\ l \text{ is undefined in } M. \end{cases}$$

Backjump:

$$M \circ l^{\mathrm{d}} \circ N \parallel F \qquad \Longrightarrow M \circ l' \parallel F \qquad \qquad \text{if} \quad \begin{cases} F \models C \lor l', & M \models \neg C, \\ l' \text{ is undefined in } M, \\ l' \text{ or } \neg l' \text{ occurs in a clause of } F. \end{cases}$$

Fail:

$$M \parallel F \wedge C$$
 \Longrightarrow UNSAT if $\begin{cases} M \models \neg C, \\ M \text{ contains no decision literals.} \end{cases}$

Success:

$$M \parallel F$$
 \Longrightarrow SAT **if** $M \models F$

We also use $S \Longrightarrow^* S'$ to denote that there exist S_1, S_2, \dots, S_i such that $S \Longrightarrow S_1 \Longrightarrow \dots \Longrightarrow S_i \Longrightarrow S'$. Also $S \Longrightarrow^! S'$ denote that $S \Longrightarrow^* S'$ and S' is a final state (SAT or UNSAT).

Explanation of the Backjump *Operation:*

The Backjump operation allows the DPLL algorithm to backtrack to a previous decision and learn a new literal. In particular, $F \models C \lor l'$ means that, for some clause C, every assignment that satisfies F must either satisfy C (i.e., contain the negation of each literal in C) or contain l' as an assignment. However, if $M \models \neg C$, which means that M conflicts with C and thus contains the negation of each literal in C, then if we want some assignment containing M to still satisfy F, then the assignment must also include the literal l' as an assignment to ensure that it satisfies $C \lor l'$, a requirement for satisfying F.

In our construction, we only consider the narrower set of BackTrack operations that find the last decision and negate it:

Lemma C.14. [Corrollary of Lemma 6 from Nieuwenhuis et al. (2005)] Assume that $\emptyset \parallel F \Longrightarrow^* M \circ l^d \circ N \parallel F$, the BackTrack operation:

$$M \circ l^{\mathrm{d}} \circ N \parallel F \implies M \circ \neg l \parallel F$$
 if
$$\begin{cases} \textit{There exists clause C in F such that} \\ M \circ l^{\mathrm{d}} \circ N \models \neg C \\ \textit{N contains no decision literals} \end{cases}$$

is always a valid Backjump operation in Definition C.13.

Definition C.15 (Run of the DPLL Algorithm). A *run* of the DPLL algorithm on formula F is a sequence of states $S_0 \Longrightarrow S_1 \Longrightarrow \cdots \Longrightarrow S_T$ such that:

- S_0 is the initial state $\emptyset \parallel F$
- For each i = 0, 1, ..., n-1, the transition $S_i \Longrightarrow S_{i+1}$ is valid according to the transition rules of the DPLL system in Definition C.13 (e.g., UnitPropagate, Decide, Backjump, or Fail);
- S_n is a final state that is either SAT or UNSAT

Note that the above definition is simply the expansion of $\emptyset \parallel F \Longrightarrow^! S_T$.

The following theorem states that the DPLL procedure always decides the satisfiability of CNF formulas:

Lemma C.16. [Theorem 5 and Theorem 9 Combined from Nieuwenhuis et al. (2005)] The Basic DPLL system provides a decision procedure for the satisfiability of CNF formulas F. Specifically:

- 1. $\emptyset \parallel F \Longrightarrow$! UNSAT if and only if F is unsatisfiable.
- 2. $\emptyset \parallel F \Longrightarrow$! SAT if and only if F is satisfiable.
- 3. There exist no infinite sequences of the form $\emptyset \parallel F \Longrightarrow S_1 \Longrightarrow \cdots$

C.5.2 TRACE EQUIVALENCE AND INDUCTIVE PROOF

We demonstrate that our Transformer in Theorem 4.5 solves SAT by showing that the CoT produced by the Transformer is "trace equivalent" to an abstract DPLL algorithm with some heuristic. We first provide definition of "trace equivalence":

Definition C.17 (Trace Equivalence of Algorithms). Let A and B be two algorithms. Let Σ_A and Σ_B be the sets of possible states of A and B, respectively. We say that algorithms A and B are *trace equivalent* if there exists a bijective mapping $\phi: \Sigma_A \to \Sigma_B$, independent of the input, such that for every input s, the traces produced by S and S satisfy the following:

If the execution of A on input s produces the trace $\operatorname{Tr}_A(s) = [\sigma_1^A, \sigma_2^A, \dots, \sigma_n^A]$, and the execution of B on the same input s produces the trace $\operatorname{Tr}_B(s) = [\sigma_1^B, \sigma_2^B, \dots, \sigma_n^B]$, then for all $i \in \{1, 2, \dots, n\}$,

$$\sigma_i^B = \phi(\sigma_i^A).$$

That is, the sequences of states of A and B are in one-to-one correspondence via the fixed mapping ϕ , and corresponding states are related by this mapping for every input s.

We first show how to convert a chain of thought of the model into a state in the abstract DPLL algorithm. Consider the following model input and Chain-of-Thought trace:

Recall that [BT] denotes backtracking and D denotes that the next token is a decision literal.

Note that the prompt input ends at [SEP] and the rest is the Chain-of-Though produced by the model.

We want to convert this trace to a state $S=M\|F$ such that F is the CNF formula in the DIAMCS encoding in the prompt input and M is the "assignment trace" at the last attempt (i.e., after the last <code>[BT]</code> token.). As such, M correspond to the <code>D 2 D -1 -4</code> portion of the trace and thus $M=2^d \, \bar{1}^d \, \bar{4}$ as described in Appendix C.5.1. We formalize this process as follows:

Definition C.18 (Translating CoT to Abstract DPLL State). For any number of variables $p \in \mathbb{N}^+$, let \mathcal{V} be the set of tokens:

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V = \{-i, i \mid i \in [p]\} \cup \{D, [SEP], [BOS], [BT], 0, SAT, UNSAT\}.
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Define a mapping $f_S: \mathcal{V}^* \to \mathcal{S} \cup \{\text{error}\}$ that converts a sequence of tokens $R \in \mathcal{V}^*$ into an abstract DPLL state as follows:

- 1. If R ends with SAT or UNSAT, then set $M_{\mathcal{S}}(R)$ to SAT or UNSAT accordingly.
- 2. Else if R contains exactly one [SEP] token, split R at [SEP] into R_{DIMACS} and R_{Trace} .
- 3. Parse R_{DIMACS} into a CNF formula F, assuming it starts with [BOS] and ends with 0. If parsing fails, set $M_S(R) = \text{fail}$.
- 4. Initialize an empty sequence M to represent variable assignments and set a flag $isDecision \leftarrow \texttt{False}$.
- 5. Process each token t in R_{Trace} sequentially:
 - If t = D, set is $Decision \leftarrow True$.
 - Else if t = [BT], remove literals from M up to and including the last decision literal (i.e., perform backtracking).
 - Else if t = i or -i for some $i \in [n]$:
 - Let l be the literal corresponding to $x_i = T$ if t = i, or $x_i = F$ if t = -i.
 - If l is already assigned in M with a conflicting value, set $M_{\mathcal{S}}(R) = \text{fail}$.
 - Else, append l to M, annotated as a decision literal if isDecision = True, or as a
 unit propagation otherwise.
 - Reset is Decision ← False.
 - Else, set $M_{\mathcal{S}}(R) = \text{error.}$
- 6. **Return** the state $M \parallel F$.
- 7. If any of the above steps fail, set $M_{\mathcal{S}}(R) = \text{fail}$.

We now present the inductive lemma:

Lemma C.19 (Inductive Lemma). For any $p, c \in \mathbb{N}^+$, for any input $F_{DIMACS} \in \text{DIMACS}(p, c)$ of length n, let F be the boolean formula in CNF form encoded in F_{DIMACS} . Let A be the model described in section C.4 with parameters p, c. Let $(\mathbf{s}_{1:n}, \mathbf{s}_{1:n+1}, \dots)$ be the trace of \mathbf{s} when running the Greedy Decoding Algorithm 1 with model A and input prompt $\mathbf{s}_{1:n} = F_{DIMACS}$. For every $i \in \mathbb{N}^+$, if $f_{\mathcal{S}}(\mathbf{s}_{1:n+i}) = S$ and $S \notin \{\text{SAT}, \text{UNSAT}, \text{error}\}$, then there exist $j \in \mathbb{N}^+$ and $S' \in \mathbb{S}$ such that $S \Longrightarrow S'$ and $f_{\mathcal{S}}(\mathbf{s}_{1:n+i+j}) = S'$.

We now show trace equivalence between the model A and some instantiating of the abstract DPLL with a specific heuristic:

Definition C.20. For any heuristic $h: \mathbb{S} \to \mathcal{L}$ where \mathcal{L} is the set of literals, let DPLL_h denote an instantiation of the abstract DPLL algorithm that selects h(S) as the decision literal when performing Decide and only performs the BackTrack operation for Backjump. h(S) is a valid heuristic if DPLL_h always abides by the Decide transition.

Lemma C.21. (Trace Simulation) There exists a valid heuristic $h : \mathbb{S} \to \mathcal{L}$ for which the Transformer model A is trace equivalent to DPLL_h on all inputs in DIMACS(p,c)

Proof. We aim to show that there exists a valid heuristic $h: \mathcal{S} \to \mathcal{L}$ such that the Transformer model A is trace equivalent to DPLL_h on all inputs in $\mathrm{DIMACS}(p,c)$.

Define the heuristic h as follows: For any state $S \in \mathcal{S}$, let h(S) be the literal that the Transformer model A selects as its next decision literal when in state S.

Formally, given that the model A outputs tokens corresponding to decisions, unit propagations, backtracks, etc., and that these tokens can be mapped to transitions in the abstract DPLL system via the mapping M_S (as per the *Translating CoT to Abstract DPLL State* definition), we set:

$$h(S) = \begin{cases} \text{the decision literal chosen by } A \text{ in state } S, & \text{if } A \text{ performs a } \text{Decide transition,} \\ \text{undefined,} & \text{otherwise.} \end{cases}$$

This heuristic is valid because A always abides by the Decide transition rules, ensuring h(S) selects a literal that occurs in F and is undefined in M, satisfying the conditions of a valid heuristic.

Define a mapping $\phi: \Sigma_A \to \Sigma_B$, where Σ_A is the set of possible states of model A, and Σ_B is the set of possible states of DPLL_h, such that for any state S in the execution trace of A, $\phi(S) = S$. That is, we identify the states of A with the corresponding states in DPLL_h by mapping the sequence of assignments and the formula F directly.

Proof of Trace Equivalence:

- We proceed by induction on the number of steps in the execution trace.
- 1367 1368 Base Case (i = 0):

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At the beginning, both algorithms start from the initial state with no assignments:

For
$$A: S_0^A = \emptyset \parallel F$$
, and For $DPLL_h: S_0^B = \emptyset \parallel F$.

- 1371 Clearly, $\phi(S_0^A) = S_0^B$.
- 1373 Inductive Step:
- Assume that after k steps, the states correspond via ϕ :

$$\phi(S_k^A) = S_k^B.$$

- We need to show that after the next transition, the states still correspond, i.e., $\phi(S_{k+1}^A) = S_{k+1}^B$.
- Suppose the model A applies a UnitPropagate operation, transitioning from state S_k^A to S_{k+1}^A by adding a literal l deduced via unit propagation.
- Since unit propagation is deterministic and depends solely on the current assignment M and formula F, DPLL_h will also apply the same $\mathrm{UnitPropagate}$ operation, transitioning from S_k^B to S_{k+1}^B by adding the same literal l.
- Thus, $\phi(S_{k+1}^A) = S_{k+1}^B$.
- Suppose the model A applies a Decide operation, transitioning from S_k^A to S_{k+1}^A by adding a decision literal $l = h(S_k^A)$.
- By the definition of the heuristic h, DPLL_h also selects l as the decision literal in state S_k^B . Both algorithms make the same decision and transition to the same next state.
- 1391 Therefore, $\phi(S_{k+1}^A) = S_{k+1}^B$.
- Suppose the model A applies a Backjump operation, backtracking to a previous state and assigning a new literal.
- Since $DPLL_h$ performs only the BackTrack operation for Backjump (as per the definition), and A simulates this operation, both algorithms backtrack in the same manner and update their assignments accordingly.
- 1397 1398 Thus, $\phi(S_{k+1}^A) = S_{k+1}^B$.
- If the model A reaches a terminal state indicating SAT or UNSAT, then so does DPLL_h, since their sequences of transitions have been identical up to this point.
- In all cases, the next state of model A corresponds to the next state of DPLL_h under the mapping ϕ . Therefore, by induction, the execution traces of A and DPLL_h are such that for all i,

$$\phi(S_i^A) = S_i^B.$$

Since the heuristic h selects the same decision literals as the model A, and A always abides by the Decide transition (as per its design), h is a valid heuristic according to the definition provided.

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D CODE FOR THEORETICAL CONSTUCTION

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1413 2
1414<sub>3</sub>
        def nearest_token(tok_emb: OneHotTokEmb, vocab: List[str],
                              targets: List[str], v: SOp | List[SOp],
14154
                              indices: PosEncSOp = indices):
1416 <sup>5</sup>
             if not isinstance(v, list):
1417_{7}^{6}
                 \nabla = [\nabla]
1418 8
1419 9
             target_tok_ids = [vocab.index(target) for target in targets]
             target_tokens = Concat([tok_emb[:, target_tok_id]
142010
                  for target_tok_id in target_tok_ids])
142111
142212
             in_targets = Linear(target_tokens, np.ones((1, len(targets))))
             filtered_index = (indices * in_targets)
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1423<sub>14</sub>
1424<sub>15</sub>
             new_v = []
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             for v_i in v:
1426<sup>17</sup>
                 if isinstance(v_i, SOp):
1427<sup>18</sup>
                      new_v.append(v_i)
                  elif v_i == 'target' or v_i == 'targets':
1428<sub>20</sub>
                      new_v.append(target_tokens)
142921
                  else:
                      raise ValueError('Unsupported value type')
143022
1431<sup>23</sup>
143224
             return Mean(ones, filtered_index, new_v, bos_weight=1)
1433<sub>26</sub>
143427
        def t(encodings: SOp, num_vars,
143528
               true_vec=(1, 0),
1436<sup>29</sup>
               false\_vec=(0, 1),
1437<sup>30</sup>
31
               none_vec=(0, 0),
               ones: Ones = ones):
1438
             mat = np.zeros((2 * num_vars, 2 * num_vars))
143933
             true_vec_off = (true_vec[0] - none_vec[0], true_vec[1] - none_vec[1])
144034
             false_vec_off = (false_vec[0] - none_vec[0], false_vec[1] - none_vec[1])
             for i in range(num_vars):
144135
1442<sup>36</sup>37
                  true_id = i
                  false_id = num_vars + i
1443
                 mat[true_id, true_id] = true_vec_off[0]
144439
                 mat[true_id, false_id] = false_vec_off[0]
                 mat[false_id, true_id] = true_vec_off[1]
144540
                 mat[false_id, false_id] = false_vec_off[1]
1446<sup>41</sup>
1447 42
             bias = np.zeros(2 * num_vars)
144844
             bias[:num_vars] += none_vec[0]
1449<sub>45</sub>
             bias[num_vars:] = none_vec[1]
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1451<sup>47</sup>
             return Linear([encodings, ones],
                 np.hstack([mat.T, bias.reshape((-1, 1))]))
1452_{49}^{48}
1453<sub>50</sub>
145451
        def dpll(num_vars, num_clauses, context_len,
                   mean_exactness=20, nonsep_penalty=20,
145552
                   return_logs=False) -> Tuple[
1456<sup>53</sup>
1457<sup>54</sup>
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             SOp, List, Dict[str, SOp]]:
             vocab: List = ([str(i) for i in range(1, num_vars + 1)]
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                            + [str(-i) for i in range(1, num_vars + 1)]
1459<sup>56</sup>
                            + ['0', '[SEP]', '[BT]', '[BOS]', 'D', 'SAT', 'UNSAT'])
1460<sub>58</sub>
            idx: Dict[str, int] = {token: idx for idx, token in enumerate(vocab)}
146159
            sop_logs: Dict[str, SOp] = {}
146260
            sops.config["mean_exactness"] = mean_exactness
            # Initialize Base SOps
146361
            tok_emb = OneHotTokEmb(idx).named("tok_emb")
146462
1465<sub>64</sub>
            nearest_sep = nearest_token(tok_emb=tok_emb,
146665
                                           vocab=vocab.
                                           targets=['0', '[SEP]', '[BT]'],
146766
                                           v=[indices, 'target']).named(
146867
1469<sup>68</sup>
                "nearest_sep")
1470<sub>70</sub>
            # The nearest (including self) separator token and whether
147171
            # the previous separator token is '0', '[SEP]', '[UP]', '[BT]'
            p_i_sep_p, b_0, b_sep, b_b_sep
147272
                nearest_sep[:, 0].named("p_i_sep_p"),
147373
                nearest_sep[:, 1].named("b_0"),
1474<sup>74</sup>
1475<sub>76</sub>
                nearest_sep[:, 2].named("b_SEP"),
                nearest_sep[:, 3].named("b_BackTrack"))
147677
147778
            # The nearest 'D' token, which denotes the next token is a decision
            p_i_D = nearest_token(tok_emb=tok_emb, vocab=vocab, targets=['D'],
147879
                                    v=indices).named("p_i_D")
147980
1480 81
            prev_pos = Id([p_i_sep_p, tok_emb[:, idx['D']]])[indices - 1]
148183
            # p_i_sep: The previous (excluding self) separator token
148284
            p_i_sep = (prev_pos[:, 0] - is_bos).named("p_i_sep")
148385
            # b_decision: whether the current position is a decision literal
148486
1485<sub>88</sub>
            b_decision = prev_pos[:, 1].named("b_decision")
148689
            # The distance to the nearest separator,
            # i.e., the length of the current state
148790
148891
            d_i_sep = (indices - p_i_sep_p).named("d_i_sep")
148992
            # Attention operation for representing the current
1490
            # clause/assignment as a bitvector of dimension 2d
149195
            p_i_sep_2 = (p_i_sep * p_i_sep).named("p_i_sep_2")
149296
            e_vars = tok_emb[:, : 2 * num_vars].named("e_vars")
149397
            r_i_pre = Mean(q_sops=[p_i_sep_2, p_i_sep, ones],
                            k\_sops=[-ones, 2 * p\_i\_sep, -p\_i\_sep\_2],
1494<sup>98</sup>
                            v_sops=e_vars).named("r_i_pre")
1495
            r_i = (r_i_pre * (indices - p_i_sep)).named("r_i")
149601
149702
            # The position of the previous (excluding self) separator token
            p_i_sep_min = p_i_sep[p_i_sep_p].named("p_i_sep_min")
149803
1499^{104}
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1500
106
            # The same position in the previous state.
            # This is used for copying from the previous state
150107
            p_i_min = (p_i_sep_min + d_i_sep + num_vars * b_SEP).named("p_i_min")
150208
            # The position of the last decision in the previous state
150309
150410
            p_i_D_min = p_i_D[p_i_sep_p].named("p_i_D_min")
1505
1505
112
            # Is the next token the literal resulting from backtracking?
150613
            b_D_min = (p_i_D_min == p_i_min + 1).named("b_D_min")
150714
            # Check if the current assignment satisfies the formula
150815
            # (See Theorem Proof for justification)
1509<sup>16</sup>
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118
            sat_q = [r_i, ones]
            sat_k = [-r_i, (-nonsep_penalty) * (1 - tok_emb[:, idx['0']])]
151119
            sat_v = is_bos
```

```
1512
151320
            b_sat = (Mean(sat_q, sat_k, sat_v,
1513
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122
                 bos_weight=nonsep_penalty - 0.5) > 0).named("b_sat")
1515_{23}
            # Check if the current assignment contracdicts the formula
            # (See Theorem Proof for justification)
151 £24
            unsat_q = [t(r_i, num_vars, true_vec=(1, 0),
151725
1518 126
                false\_vec=(0, 1), none\_vec=(1, 1)), ones]
   127
            unsat_k = sat_k
1519
128
            unsat_v = 1 - is_bos
152Q<sub>29</sub>
            b_cont = (Mean(unsat_q, unsat_k, unsat_v,
152130
                bos_weight=nonsep_penalty - 0.5) > 0).named("b_cont")
152431
            b_{copy_p} = (p_i_min < (p_i_sep_p - 1)).named("b_copy_p")
1523 32
   133
1524
134
1525<sub>35</sub>
            # Unit Propagation
            up_q = unsat_q
152£36
            up_k = unsat_k
152†37
1528<sup>138</sup>
            up_v = num_clauses * r_i
1528
1529
140
            o_up = Mean(up_q, up_k, up_v, bos_weight=nonsep_penalty - 1.5)
153Q_{41}
153142
            e_up = (
                     GLUMLP (act_sops=(o_up - t(r_i, num_vars,
153443
153344
                                                    true_vec=(1, 1),
                                                    false_vec=(1, 1),
   145
153<u>4</u>
146
                                                   none_vec=(0, 0))))
153547
                     - GLUMLP(act_sops=(o_up - 1))
153£48
            ).named("e_up_new")
153749
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             # Heuristic for decision literal selection:
             # Find the most common literal in remaining clauses
1540_{53}
            heuristic_q = [t(r_i, num_vars, true_vec=(-10, 1),
                               false_vec=(1, -10), none_vec=(0, 0)), ones]
154154
            heuristic_k = [r_i, (-nonsep_penalty) * (1 - tok_emb[:, idx['0']])]
154255
1543 156
            heuristic_v = r_i
   157
            heuristic_o = SelfAttention(heuristic_q, heuristic_k, heuristic_v)
1544
154\bar{\phi}_{59}
             # Whether the current assignment contains no decision literal
154£60
            b_no_decision = (p_i_D <= p_i_sep).named("b_no_decision")</pre>
154761
            # Whether Backtracking is finished
154862
            b_BT_finish = ((p_i_D_min <= p_i_min) & b_BackTrack)</pre>
1549
164
155Q<sub>65</sub>
            # The negation of the last decision literal in the previous state
155166
            e_BT = t(e_vars[p_i_D_min + 1], num_vars=num_vars,
                       true_vec=(0, 1), false_vec=(1, 0), none_vec=(0, 0))
155267
1553 168
            # The next index in the previous state for copying
1554<sub>170</sub>
            p_i_min_index = (p_i_min + 1).named("p_i_min_index")
155$71
155672
            # The next token in the previous state for copying
155†73
            e_copy = tok_emb[p_i_min_index].named("e_copy")
1558<sup>174</sup>
            # Whether we've decided that the formula is UNSAT
   175
1559
176
            b_unsat = (b_no_decision & b_cont).named("b_unsat")
156Q<sub>77</sub>
156178
             # Whether we're negativing the last decision literal for backtracking
            b_backtrack = (b_D_min & b_BackTrack).named("b_backtrack")
156279
1563 180
             # Whether we're copying tokens from the previous state
   181
156\overset{\overset{\centerdot}{4}}{}_{182}
            b_copy = (b_copy_p & (1 - b_BT_finish)).named("b_copy")
156583
```

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1566
            b_BT_token = (b_cont & (1 - tok_emb[:, idx['[BT]']]))
156784
1567
1568
186
            b_not_D = (1 - tok_emb[:, idx['D']]).named("b_not_D")
            e_unassigned = t(r_i, num_vars, true_vec=(0, 0),
1569_{87}
                 false_vec=(0, 0), none_vec=(1, 1)).named("e_unassigned")
157088
            out = CPOutput(len(vocab),
157189
                 [(b_sat, idx['SAT'], 16),
1572 190
                 (b_unsat, idx['UNSAT'], 15),
191
1573
192
                 (b_BT_token, idx['[BT]'], 14),
157493
                 (b_backtrack, Pad(e_BT, len(vocab), idx['1']), 12),
157594
                 (b_copy, e_copy, 6),
                 (None, Pad(e_up, len(vocab), idx['1']), 4),
157<sup>6</sup>95
                 (b_not_D, idx['D'], 3),
157796
1577
1578
198
                 (None, Pad(e_unassigned + heuristic_o,
                             out_dim=len(vocab), start_dim=idx['1']), 1)])
157999
158200
                 return out
1581
```