Asynchrony Invariance Loss Functions for Graph Neural Networks

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Abstract

A ubiquitous class of graph neural networks (GNNs) operates according to the messagepassing paradigm, such that nodes systematically broadcast and listen to their neighbourhood. Yet, these synchronous computations have been deemed potentially sub-optimal as they could result in irrelevant information sent across the graph, thus interfering with efficient representation learning. In this work, we devise self-supervised loss functions biasing learning of synchronous GNNbased neural algorithmic reasoners towards representations that are invariant to asynchronous execution. Asynchrony invariance could successfully be learned, as revealed by analyses exploring the evolution of the self-supervised losses as well as their effect on the learned latent embeddings. Our approach to enforce asynchrony invariance constitutes a novel, potentially valuable tool for graph representation learning, which is increasingly prevalent in multiple real-world contexts.

1. Introduction

Graph neural networks (GNNs) constitute deep learning models for representation learning from graph-structured data. Because graph-based abstractions apply to a wide range of real-world domains, GNNs have proven powerful in multiple scientific and industrial settings [\(Lam et al.,](#page-7-0) [2023;](#page-8-1) [Wang et al.,](#page-8-0) 2023; Veličković, 2023; Veličković et al., [2020\)](#page-8-2). A large class of GNNs operates according to the message-passing framework [\(Gilmer et al.,](#page-7-1) [2017\)](#page-7-1), where the representation vector of a node is updated based on recursive aggregation and transformation of the neighbouring node vectors.

Traditionally, these GNN operations are performed in a synchronous fashion, that is, nodes necessarily broadcast and listen to their neighbours at every layer, which has been deemed sub-optimal [\(Faber & Wattenhofer,](#page-7-2) [2022;](#page-7-2) [Dudzik](#page-7-3) [et al.,](#page-7-3) [2023\)](#page-7-3). Indeed, systematic neighbourhood aggregation can induce over-smoothing of the neighborhood features (i.e., progressively more indistinguishable node representations) as the number of GNN layers increases [\(Li et al.,](#page-7-4) [2018\)](#page-7-4). Moreover, for ubiquitous tree-based graphs, listening to the complete neighbourhood can also imply that the node receptive field grows exponentially with the number of layers. Yet information becomes compressed into a fixed-size vector, incidentally resulting in information loss referred to as over-squashing [\(Di Giovanni et al.,](#page-7-5) [2024;](#page-7-5) [Alon & Yahav,](#page-6-0) [2020\)](#page-6-0). Finally, the representational capacity of GNNs is upper bounded by the Weisfeiler-Lehman (WL; [Weisfeiler](#page-8-3) [& Leman](#page-8-3) [1968\)](#page-8-3) graph isomorphism test [\(Xu et al.,](#page-8-4) [2018;](#page-8-4) [Morris et al.,](#page-7-6) [2019\)](#page-7-6), which has also been attributed to the uniform treatment of nodes inherent to standard messagepassing [\(Faber & Wattenhofer,](#page-7-2) [2022\)](#page-7-2). Importantly, these GNN-associated limitations were mitigated by facilitating asynchronous communication between nodes [\(Faber & Wat](#page-7-2)[tenhofer,](#page-7-2) [2022;](#page-7-2) [Finkelshtein et al.,](#page-7-7) [2023\)](#page-7-7).

In addition to potential expressivity gains, promoting asynchrony in GNNs could be beneficial from an algorithmic alignment perspective. [Xu et al.](#page-8-5) [\(2019\)](#page-8-5) proposed that neural network architectures whose sub-parts, or modules, align with the sub-routines of the correct reasoning process for a given task will generalise better. Specifically, these authors established a parallel between the computational structure of GNNs and that of the Bellman-Ford algorithm, which could account for better in- and out-of-distribution (OOD) generalisation performance on shortest path-based tasks for GNNs (Dudzik & Veličković, [2022;](#page-7-8) [Xu et al.,](#page-8-5) [2019\)](#page-8-5). Yet, an important caveat remains. For algorithmic tasks such as Bellman-Ford, only a handful of updates would need to be sent at each step. Dynamic programming-aligned reasoning might thus still be hindered by the synchronous nature of GNN computations.

This possibility was explored by [Dudzik et al.](#page-7-3) [\(2023\)](#page-7-3), who devised synchronous GNNs whose layers were provably invariant to asynchrony (i.e., asynchronous execution would

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have produced analogous latent node embeddings). The authors distinguished between increasingly strict levels of asynchrony, which progressively improved OOD performance on the CLRS-30 Algorithmic Reasoning Benchmark (Veličković et al., [2022a\)](#page-8-6).

However, [Dudzik et al.](#page-7-3) [\(2023\)](#page-7-3) implemented asynchrony invariance axiomatically, deriving conditions that could be met through specific GNN architectural choices. The present study aims at leveraging the asynchrony invarianceassociated benefits in [Dudzik et al.](#page-7-3) [\(2023\)](#page-7-3), while permitting to increase the design space, and hence the expressivity of possible GNN architectures. To this end, we evaluate whether asynchrony invariance can be enforced through self-supervised loss functions.

2. Neural Algorithmic Reasoning

This work focuses on asynchrony invariance-related benefits in a neural algorithmic reasoning (NAR) context (though a broader range of applications is possible). NAR aims at neuralising classical algorithms. Because deep learning networks are more malleable, novel, potentially optimised versions of the initial algorithms can be learned (Veličković [& Blundell,](#page-8-7) [2021;](#page-8-7) [Xu et al.,](#page-8-5) [2019\)](#page-8-5). NAR can also be viewed as being concerned with designing deep learning architectures capable of solving classical algorithmic tasks, thereby enhancing the generalisability and tractability of neural networks.

Veličković & Blundell [\(2021\)](#page-8-7) proposed a blueprint for NAR based on the encode-process-decode paradigm [\(Hamrick](#page-7-9) [et al.,](#page-7-9) [2018\)](#page-7-9). An encoder function f maps inputs to highdimensional latent embeddings. These are then read out by a processor neural network P , which learns to execute the algorithmic task, typically by relying on supervised signals promoting reproduction of the algorithmic output and potentially of the intermediate algorithmic trajectory. P generates output embeddings that are subsequently decoded using a function g. Importantly, while the inputs would typically be artificial during learning, f and g could later be altered (while freezing parameters in P), such that the model would be capable of coping with natural inputs (Veličković [et al.,](#page-8-8) [2022b\)](#page-8-8).

Notably, NAR can be probed and compared across distinct GNN-based models in a systematic fashion using the CLRS-30 Algorithmic Reasoning Benchmark introduced by Veličković et al. [\(2022a\)](#page-8-6). Using the encode-process-decode approach, CLRS-30 implements a collection of algorithmic tasks of differing nature (incl. dynamic programming, sorting, greedy, searching). This benchmark was leveraged here, using settings matching [Dudzik et al.](#page-7-3) [\(2023\)](#page-7-3) (see Section [4](#page-4-0) for architectural details).

3. Self-supervised Asynchrony Invariance

[Dudzik et al.](#page-7-3) [\(2023\)](#page-7-3) define different forms of asynchrony invariance and identify increasingly tight sufficient conditions for the message passing functions to enforce them. However, the authors acknowledge that these are not necessary conditions. Therefore, the potential richness of the space of functions implementing asynchrony invariance is not fully exploited, which might lead to sub-optimal performance. Moreover, although the node latent representations lie in a high-dimensional space, it is possible that they are distributed in the vicinity of a lower-dimensional manifold [\(Fef](#page-7-10)[ferman et al.,](#page-7-10) [2016\)](#page-7-10). Consequently, enforcing asynchrony invariance in the entire high-dimensional space might not be required, as long as the message passing functions satisfy asynchrony invariance in the subspace where the data lives.

In this work, we study the possibility of weakly enforcing the different forms of asynchrony invariance. To this end, a self-supervised loss is added to the optimisation objective to guide the training procedure towards networks exhibiting asynchrony invariance. Our approach is similar to [\(Cohen-](#page-7-11)[Karlik et al.,](#page-7-11) [2020\)](#page-7-11), who include a loss term penalising violations of subset-permutation invariance in a recurrent network model, which does not exhibit this property by design. Similarly, Ong & Veličković [\(2022\)](#page-7-12) introduce a regularisation term to enforce commutativity for a learnable aggregator.

To define our augmented optimisation objective, we rely on the notation and the mathematical foundations in [Dudzik](#page-7-3) [et al.](#page-7-3) [\(2023\)](#page-7-3). Their theory builds on the notion of *monoid*, which corresponds to a set equipped with a non-invertible operation. This mathematical structure allows to formally describe how the messages *act* on the node states. Additionally, the authors rely on 1-cocycles to formalise enforcement of invariance for argument generation. These can be understood intuitively as mappings satisfying a condition analogous to the Leibniz derivative rule, i.e. $D(fq) = D(f) \cdot q + f \circ D(q)$, where \cdot and \circ define a right and a left action, respectively, over a given monoid. For conciseness purposes, we omit the formal presentation of these notions, and refer the reader to [Dudzik et al.](#page-7-3) [\(2023,](#page-7-3) Appendix A).

Equation [1](#page-1-0) defines the transformation process of the node embeddings $\{x_u\}_{u\in\mathcal{V}}\subseteq S$ at each message passing step

$$
\mathbf{x}'_u = \phi\left(\mathbf{x}_u, \bigoplus_{v \in N(u)} \psi(\mathbf{x}_u, \mathbf{x}_v)\right) \tag{1}
$$

The function $\phi : S \times M \longrightarrow S$ represents the node update, and takes as input the node embeddings at a given point of execution, as well as the aggregated messages from the neighbours of the node. If the set of messages has a monoidal structure $(M, \oplus, 1)$, the node update ϕ can be

described in terms of a left action • : $M \times S \longrightarrow S$, so $\phi(s, m) := m \cdot s$. With this notation, two types of asynchrony invariance arise:

- 1. Message aggregation asynchrony. If M is a commutative monoid, the aggregated message does not depend on the order in which the messages are received, as illustrated in Figure [1.](#page-2-0) In [Dudzik et al.](#page-7-3) [\(2023\)](#page-7-3), this level of asynchrony is enforced axiomatically, relying on an aggregator such as *sum*, *max* or *mean* (though a learnable commutative aggregator could be used; see Ong & Veličković [2022\)](#page-7-12).
- 2. Node update asynchrony. If ϕ is a left action, it verifies the associativity axiom i.e. $\phi(s, n \oplus m)$ = $\phi(\phi(s,m), n)$. Therefore, upon message reception, the node can be updated asynchronously without requiring to aggregate all of the messages beforehand (Figure [2\)](#page-2-1).

Figure 1. (a) Messages from nodes $\{a, b, c\}$ to node b are aggregated synchronously to obtain the message m_b that updates the embeddings of node b. (b) For a commutative aggregator \oplus , the resulting message is the same as when the messages from nodes ${a, b}$ are first aggregated to obtain $m_{b{a,b}}$, and then aggregated with m_{bc} .

In order to enforce node update asynchrony invariance, one possibility is to set $\phi = \oplus$, such that associativity follows from that of \oplus [\(Dudzik et al.,](#page-7-3) [2023\)](#page-7-3). However, this choice can potentially limit the expressivity of the GNN. It is therefore desirable to guide the learnable node update towards asynchrony invariance during training rather than enforcing associativity axiomatically. Specifically, the violation of the associativity constraint can be measured in terms of the l^2 distance between the node embeddings resulting from updating after having processed all messages, and those obtained

Figure 2. (a) Node b embeddings are updated upon reception of the message obtained after synchronously aggregating the messages from its neighbours. (b) If ϕ is a left-action, this transformation yields the same embeddings as updating b with the message m_{ba} and then with m_{hh} .

after successive updates with the individual messages

$$
R_{\text{L2}} = \mathbb{E}\left[\left\| |\phi(s, n \oplus m) - \phi(\phi(s, m), n)| \right\|_2 \right] \quad (2)
$$

where the expectation is taken with respect to the messages $m, n \in M$ and the node embeddings $s \in S$. Therefore, it can be estimated from samples during training in a Monte-Carlo fashion. The pseudocode enforcing R_{L2} can be found in Appendix [D](#page-10-0) (computations for the upcoming, additional penalty terms are analogous).

If the node update implements a left action, R_{L2} trivially vanishes. Yet, the converse only holds in the support of the message and node embeddings distributions, which is a strength of this method. Indeed, enforcing associativity for all elements of the sets S and M is not strictly required, as it is only necessary for this property to hold in the subset in which the data lives.

Thus far, the discussion on how the messages are generated has been intentionally omitted. From Equation [1,](#page-1-0) the arguments to the message function are the node embeddings themselves, obtained after processing all of the messages from the previous message-passing step. Still, for promoting a higher level of asynchrony, each node should be able to prepare its arguments for the message function without requiring to process all of its incoming messages beforehand. In this regard, [Dudzik et al.](#page-7-3) [\(2023\)](#page-7-3) define an argument generation function $\delta : M \times S \longrightarrow A$, where $(A, +, 0)$ denotes the argument monoid, thus explicitly distinguishing the argument generation from the node update. With this notation, a further level of asynchrony can be defined:

3. Argument generation asynchrony. If δ verifies that $\delta_1(s) = 0$ and $\delta_{n \oplus m}(s) = \delta_n(m \cdot s) + \delta_m(s)$, then δ supports argument generation asynchrony, as it is illustrated in Figure [3.](#page-3-0) This is equivalent to enforcing the 1-cocycle condition for the function $D : m \in M \longrightarrow$ $D(m) := \delta_m(\cdot) \in [S, A]$, as proved by [Dudzik et al.](#page-7-3) [\(2023\)](#page-7-3).

Figure 3. (a) Node update and argument generation at node b upon reception of the message obtained after synchronously aggregating the messages from its neighbours. (b) The 1-cocycle condition for δ ensures that the aggregated partial arguments, generated after processing m_{ba} and then m_{bb} , are identical to those generated by the invocation of the argument generation function with the aggregated messages m_b .

Assuming $S = A$ and $\delta = \phi$, if the 1-cocycle condition is satisfied, A is an idempotent monoid [\(Dudzik et al.,](#page-7-3) [2023\)](#page-7-3), which limits the choices for the argument aggregation. Alternatively, the constraint $\delta = \phi$ can be lifted and the cocycle condition weakly enforced by including an additional regularisation term in the loss, as defined by Equation [3](#page-3-0)

$$
R_{\text{L3}}^{\text{CO}} = \mathbb{E}\left[\left\| \delta_{n \oplus m}(s) - \left(\delta_n(\phi(s, m)) + A \delta_m(s) \right) \right\|_2 \right] \tag{3}
$$

A final, related, level of asynchrony posits that the message resulting from successive processing of partial arguments is equivalent to that resulting from aggregated arguments:

4. Message generation asynchrony. If $\psi : A \times A \longrightarrow$ M is a monoid multimorphism, i.e. a monoid homormorphism in each individual component, the message function can be invoked even when its arguments are not fully ready, as shown in Figure [4.](#page-3-1) Therefore, $\psi(a_{u_1} + a_{u_2}, a_v) = \psi(a_{u_1}, a_v) \oplus \psi(a_{u_2}, a_v)$, and $\psi(a_u,a_{v_1}+a_{v_2})=\psi(a_u,a_{v_1})\oplus\psi(a_u,a_{v_2})$ need to be satisfied, so that ψ is bi-linear.

Figure 4. Comparison of the message generation procedure when the arguments are aggregated before the invocation of the message function (a), with the generation of the message from the partial arguments $a'_{b{a,b}}$ and $a'_{b{a}}$ (b). For a monoid multimorphism ψ , both operations yield the same message m'_{bc} .

Message generation asynchrony can be enforced axiomatically, for instance by implementing ψ as a log-semiring bilinear layer [\(Dudzik et al.,](#page-7-3) [2023\)](#page-7-3). Alternatively, the expectation shown in Equation [4](#page-3-2) can be used as an additional regularisation term to ensure that ψ is linear in both variables

$$
R_{\text{L3}}^{\text{MULT}} = \mathbb{E}\left[\left\|\psi(\delta_{n_u}(\phi(s_u, m_u)), s_v) \oplus \psi(\delta_{m_u}(s_u), s_v) - \psi(\delta_{n_u}(\phi(s_u, m_u)) + A \delta_{m_u}(s_u), s_v)\right\|_2\right] + \mathbb{E}\left[\left\|\psi(s_u, \delta_{n_v}(\phi(s_v, m_v))) \oplus \psi(s_u, \delta_{m_v}(s_v)) - \psi(s_u, \delta_{n_v}(\phi(s_v, m_v)) + A \delta_{m_v}(s_v))\right\|_2\right]
$$
\n(4)

The regularisation terms in Equations [2,](#page-2-2) [3,](#page-3-0) and [4](#page-3-2) can be used to augment the optimisation objective, as shown in Equation [5](#page-3-3)

$$
\mathcal{L}_{\text{AUG}}(w) = \mathcal{L}(w) + \lambda_{\text{L2}} R_{\text{L2}} + \lambda_{\text{L3}} \underbrace{(R_{\text{L3}}^{\text{CO}} + R_{\text{L3}}^{\text{MULT}})}_{R_{\text{L3}}} \tag{5}
$$

where $\mathcal{L}(w)$ represents the quality loss, and λ_{L2} , λ_{L3} are hyperparameters that define the weight given to each regularisation component in the augmented loss.

4. Architectures

Based on the definitions in Section [3,](#page-1-1) [Dudzik et al.](#page-7-3) [\(2023\)](#page-7-3) define three levels of asynchrony invariance for a GNN:

- Level 1: The GNN satisfies message aggregation asynchrony. As in [Dudzik et al.](#page-7-3) [\(2023\)](#page-7-3), this level of asynchrony is enforced axiomatically by relying on a commutative aggregator such as *max* or *sum*.
- Level 2: The GNN satisfies Level 1, as well as node update asynchrony. [Dudzik et al.](#page-7-3) [\(2023\)](#page-7-3) enforce this level by setting $\phi = \oplus = \max$.
- Level 3: The GNN satisfies Level 2, along with argument and message generation asynchrony. To enforce this level axiomatically, Dudzik & Veličković [\(2022\)](#page-7-8) set $\phi = \oplus = \text{max}$ and use a log-semiring bilinear layer for the message function ψ .

These increasingly strict levels of asynchrony invariance can be weakly enforced using the augmented loss in Equation [5.](#page-3-3) The present work examines whether a GNN can robustly learn to implement asynchrony invariance through self-supervision.

Moreover, we build upon [Dudzik et al.](#page-7-3) [\(2023\)](#page-7-3), explicitly distinguishing the argument generation function δ and the node update ϕ , thereby promoting better exploration of the model design space. Additionally, we allow the argument monoid operation $+$ to be different from that of the message monoid ⊕. Specific architectures evaluated in the following sections are summarised in Table [1](#page-4-1) (see Table [B.1](#page-4-1) for complete version).

Table 1. GNN architectures. The prefix in the architecture name indicates the level of asynchrony invariance that the network implements by design. In the main text, max-based architectures (e.g., L1-max) are analogous except for $oplus$ and, if applicable, $+$.

	$L1-SUM$	L2	L1- δ -SUM	L3
⊕	SUM	MAX	SUM	MAX
Φ	$LINEAR + RELU$	MAX	LINEAR + RELU	MAX
δ	Φ	Φ	LINEAR + RELU	Φ
	٠		SUM	MAX
ψ	LINEAR	LINEAR	LINEAR	LOG-SEMIRING BILINEAR

5. Results

5.1. Quantitative Analyses

Table [2](#page-4-2) displays test performance for GNN architectures described in Section [4](#page-4-0) (see Table [C.3](#page-10-1) for full range of architectures). In CLRS-30, training and validation performance, which assess generalisation inside the support of the training data, are distinguished from test performance, which evaluates OOD extrapolation by varying the problem size. That

is, input graphs of 16 nodes are presented during training and validation, while 64 nodes are used at test time. All models generally exhibited perfect or near-perfect validation performance (see Table [C.2\)](#page-4-2). Yet, this was not the case for OOD generalisation.

The L1-sum GNN was largely outperformed by the L2 model (Table [2\)](#page-4-2), thereby replicating [Dudzik et al.](#page-7-3) [\(2023\)](#page-7-3). In these cases, high validation performance for L1-sum thus appears to have partly been due to overfitting to the statistics of the training graphs, as opposed to truly having learned the algorithm. Notably, this large performance difference was reduced when using the L1-max architecture (Table [2\)](#page-4-2), which complements previous findings that the max aggregator improves OOD generalisation relative to summation (Veličković et al., [2020;](#page-8-2) Veličković et al., [2022a;](#page-8-6) [Xu et al.,](#page-8-9) [2020\)](#page-8-9). This result can be understood in terms of algorithmic alignment, since max-aggregation is better aligned to the discrete decisions over neighbourhoods inherent to various algorithms such as Bellman-Ford (Veličković et al., [2020\)](#page-8-2). Further focusing on Bellman-Ford, it was noted that, while the ReLU MLP modules of a GNN can demonstrate learning of nonlinear functions when interpolating [\(Cybenko,](#page-7-13) [1989\)](#page-7-13), their predictions become linear outside the support of the training data [\(Xu et al.,](#page-8-9) [2020\)](#page-8-9). This does not fit well with sum-aggregation, where a nonlinear function must be learned to simulate the algorithm. Conversely, max-aggregation restricts learning to a linear function [\(Xu](#page-8-9) [et al.,](#page-8-9) [2020\)](#page-8-9), thus promoting better generalisation.

While performance was largely determined by structural architectural properties (e.g., form of \oplus), in some cases (e.g., Dijkstra, activity selector, MST Prim), enforcing L2 asynchrony through self-supervision (i.e., $\lambda_{L2} > 0$) improved OOD performance for the L1-sum architecture (Table [2\)](#page-4-2). Additional analyses enforcing L3 asynchrony (i.e., L1-based architecture with $\lambda_{L{2,3}} > 0$; see Table [C.3\)](#page-10-1) also revealed improvements.

Table 2. Test (OOD) results for CLRS-30 algorithmic tasks across different GNN architectures. Mean scores across 3 random seeds are displayed, with standard deviations indicated. $\lambda_{L2} > 0$ denotes regularised architecture $(= 0$ otherwise). For regularised models, the hyperparameter settings yielding the highest performance are displayed.

Importantly, even in situations where the effects of asynchrony invariance were difficult to infer from OOD performance, this does not imply that it was not learned. This was verified by the qualitative analyses in Sections [5.2](#page-5-0) and [5.3.](#page-5-1)

5.2. Loss Analyses

To gain further insight into the results in Section [5.1,](#page-4-3) we examined whether R_{L2} and R_{L3} effectively decreased when $\lambda_{L{2,3}} > 0$, which would be indicative of learned asynchrony invariance. These analyses focused on the Bellman-Ford [\(Bellman,](#page-7-14) [1958\)](#page-7-14) algorithm, which computes shortest paths from a source node by relaxation. That is, at each step the distances from each node to the source are updated with better approximations until convergence. Focusing on Bellman-Ford was motivated by its alignment with the computation structure of GNNs (see [Xu et al.](#page-8-5) [2019\)](#page-8-5).

The evolution of the quality loss (i.e., $\mathcal{L}(w)$) as well as of the regularised losses are displayed in Figures [5](#page-5-2) (for $R_{1,2}$), and [6](#page-6-1) (for $R_{1,3}$). For illustration purposes, the results shown are restricted to comparing non-regularised with regularised versions of the $L1-\delta$ -sum architecture, since this model incorporated both R_{L2} and R_{L3} . Yet, analogous loss patterns were observed for the broader range of GNNs described in this work.

Notably, without self-supervision, R_{L2} and R_{L3} do not decrease throughout training. This supports the absence of an inductive bias for asynchrony invariance in the GNN architecture, thereby suggesting that potential asynchrony-related effects would arise from the self-supervised procedure. This is reinforced by the decrease in R_{L2} and R_{L3} when enforcing asynchrony invariance. Crucially, the network appeared to leverage asynchrony invariant transformations in both in- and out-of-distribution settings. Indeed, $R_{1,2}$ and $R_{1,3}$ were also associated with low (i.e., $< 10^{-1}$) values at test time. Therefore, overall, these findings indicate that Level 2 (Figure [5\)](#page-5-2) and Level 3 (Figure [6\)](#page-6-1) asynchrony invariance [\(Dudzik et al.,](#page-7-3) [2023\)](#page-7-3) can be learned in a self-supervised fashion.

5.3. Latent Space Representations

To further probe the putative influence of learned asynchrony invariance, we examined the latent space representations of the neural processor at test time. Specifically, we leveraged the fact that the three regularisation terms in Section [3](#page-1-1) are expressed in terms of the distance between embedding pairs. For instance, the regularisation term R_{L2} in Equation [2](#page-2-2) involves the difference between the node embeddings after the update with the aggregated messages $\phi(s, n \oplus m)$ and the embeddings after subsequent updates $\phi(\phi(s, m), n)$. Therefore, we retrieved the trajectories of these embedding pairs across message passing steps, and jointly represented their evolution by applying PCA and keeping the two most dominant directions at each computation step. We filtered the graphs terminating at the same step T , and aggregated

Figure 5. Loss evolution for $\mathcal{L}(w)$ (training) as well as for R_{L2} at training, validation and test time for $L1-\delta$ -sum without (upper) and with (lower) self-supervised asynchrony invariance enforcement ($\lambda_{L2} = \lambda_{L3} = 0.5$). For illustration purposes, training losses elements have been grouped in 50-iteration bins (i.e., means are displayed, with standard deviations indicated by the fill). Similarly, 2-iteration bins are displayed for the validation loss. The mean loss is shown for test.

the trajectories across the node dimensions using max (see Mirjanić et al. [2023](#page-7-15) for a similar approach). This yielded embeddings of shape $N \times D \times T$, where N is the number of sampled execution graphs and D is the dimensionality of the latent representations (set to $D = 128$).

For the L1- δ -sum architecture, if $\lambda_{L2} = 0$, the embeddings for each loss component grouped in clearly distinct clusters, as shown in Figure [7](#page-6-2) (upper). This demonstrates that the node embeddings under asynchronous execution will necessarily diverge from those obtained by synchronously performing the message passing steps, thus possibly leading to a misalignment with the underlying algorithm. This was not the case with regularisation ($\lambda_{L2} > 0$), as evidenced by the node embeddings for synchronous and asynchronous execution clustering together (Figure [7;](#page-6-2) lower). Analogous results were obtained for the Level 3 asynchrony invariance regularisation terms, as shown in Figures [8](#page-7-16) and [9.](#page-7-17) Indeed, for the regularised model ($\lambda_{L3} > 0$), the gap between syn-

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Figure 6. Same as Figure [5](#page-5-2) but for R_{L3} .

chronous and asynchronous trajectories was reduced relative to the non-regularised model.

Analogous analyses for the $L1-\delta$ -max architecture can be found in Appendix [A.](#page-9-0) In that case, the two clusters grouped together almost perfectly at every step, as shown in Figures [A.1,](#page-2-0) [A.2](#page-2-1) and [A.3.](#page-3-0)

Regularisation towards asynchrony invariance therefore appeared to alter the structure of the processor computations, a phenomenon also observed through tracking the global execution trajectory of the node embeddings (see Figure [E.4\)](#page-3-1).

6. Conclusion & Future Work

This work has designed self-supervised, asynchrony invariance-enforcing loss functions for GNNs. Notably, asynchrony-related benefits had already been demonstrated. Indeed, altering the standard message-passing scheme to facilitate asynchronous communication enhanced GNN expressivity and mitigated GNN-associated limitations [\(Finkelshtein et al.,](#page-7-7) [2023;](#page-7-7) [Faber & Wattenhofer,](#page-7-2) [2022\)](#page-7-2). Yet, asynchronous execution implied reduced scalability relative to synchronous alternatives [\(Dudzik et al.,](#page-7-3) [2023\)](#page-7-3).

Figure 7. Step-wise PCA visualisations of the node embeddings $\phi(s, n \oplus m)$ (red) and $\phi(\phi(s, m), n)$ (blue) used to compute the associativity regularisation term R_{L2} (Equation [2\)](#page-2-2) during Bellman-Ford execution at test time. The architecture $L1-\delta$ -sum was used, as defined in Table [B.1](#page-4-1) with no regularisation (upper) and λ_{L2} = $\lambda_{L3} = 0.5$ (lower).

This was addressed in [Dudzik et al.](#page-7-3) [\(2023\)](#page-7-3), who built synchronous GNN architectures provably asynchrony invariant rather than asynchronous. Yet, while these authors implemented axiomatically-defined asynchrony invariance using specific architectural settings, GNNs under our proposed self-supervised approach span a broader design space.

It should still be noted that learning asynchrony invariant embeddings did not appear to systematically improve OOD performance on all of the algorithmic tasks evaluated here. In the future, greater OOD difficulty (e.g., problem size > 64) could be explored. This could increase the window for potential asynchrony-related performance improvements. Beyond problem size (Veličković et al., [2020\)](#page-8-2), extrapolation could be investigated on graphs with unseen structure, edge weights, as well as node features [\(Xu et al.,](#page-8-9) [2020\)](#page-8-9), as different tasks could benefit from asynchrony to differing extents.

Still, overall, the present work yields novel insights regarding the ability of GNNs to converge towards asynchrony invariant embeddings. To our knowledge, this had not yet been achieved in a self-supervised fashion.

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Figure 8. Same as Figure [7](#page-6-2) but for the argument embeddings $\delta_{n\oplus m}(s)$ (red) and $\delta_n(m \cdot s) + \delta_m(s)$ (blue) used to compute the 1-coycle regularisation term $R_{\rm L3}^{\rm CO}$ (Equation [3\)](#page-3-0).

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Figure 9. Same as Figure [7](#page-6-2) but for the message embeddings $\psi(a_{u_1} + a_{u_2}, a_v)$ (red) and $\psi(a_{u_1}, a_v) \oplus \psi(a_{u_2}, a_v)$ (blue) used to compute the multimorphism regularisation term $R_{\text{L3}}^{\text{MULT}}$ (Equation [4\)](#page-3-2).

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A. Latent Space Representations (L1-δ-max)

Figure A.1. Step-wise PCA visualisations of the node embeddings $\phi(s, n \oplus m)$ (red) and $\phi(\phi(s, m), n)$ (blue) used to compute the associativity regularisation term R_{L2} (Equation [2\)](#page-2-2) during Bellman-Ford execution at test time. The architecture L1- δ -max was used, as defined in Table [B.1](#page-4-1) with no regularisation (left) and $\lambda_{L2} = \lambda_{L3} = 0.5$ (right).

Figure A.2. Same as Figure [A.1](#page-2-0) but for the argument embeddings $\delta_{n\oplus m}(s)$ (red) and $\delta_n(m \cdot s) + \delta_m(s)$ (blue) used to compute the 1-coycle regularisation term R_{L3}^{CO} (Equation [3\)](#page-3-0).

Figure A.3. Same as Figure [A.1](#page-2-0) but for the message embeddings $\psi(a_{u_1} + a_{u_2}, a_v)$ (red) and $\psi(a_{u_1}, a_v) \oplus \psi(a_{u_2}, a_v)$ (blue) used to compute the multimorphism regularisation term $R_{\text{L3}}^{\text{MULT}}$ (Equation [4\)](#page-3-2).

B. Architectures

Table B.1. GNN architectures (incl. those in Table [1\)](#page-4-1). The prefix in the architecture name indicates the level of asynchrony invariance that the network implements by design [\(Dudzik et al.,](#page-7-3) [2023\)](#page-7-3).

C. Quantitative Results

C.1. Validation

Table C.2. Validation results for CLRS-30 algorithmic tasks across different GNN architectures. Mean scores across 3 random seeds are displayed, with standard deviations indicated. $\lambda_{L{2,3}} > 0$ denotes regularised architectures (= 0 otherwise). For regularised models, the λ_{L2} and/or λ_{L3} hyperparameter settings yielding the highest test performance are displayed.

C.2. Test

Table C.3. Test (OOD) results for CLRS-30 algorithmic tasks across different GNN architectures (incl. results from Table [2\)](#page-4-2). Mean scores across 3 random seeds are displayed, with standard deviations indicated. $\lambda_{L_{12,3}} > 0$ denotes regularised architectures (= 0 otherwise). For regularised models, the λ_{L2} and/or λ_{L3} hyperparameter settings yielding the highest test performance are displayed (for L1-sum and L1-max: $\lambda_{L2} \in \{0.05, 0.1, 0.5, 1\}$ was implemented, for L2- δ -sum and L2- δ -max: $\lambda_{L3} \in \{0.05, 0.1, 0.5, 1\}$, and for L1- δ -sum and L1-δ-max: $\lambda_{L2} \in \{0.05, 0.1, 0.5\}$ and $\lambda_{L3} \in \{0.05, 0.1, 0.5\}$)

D. Pseudocode for R_{L2} penalty computation

The regularisation terms are expressed in terms of expectations, and thus they can approximated by uniformly sampling a subset of node embeddings and messages at each training step. Note that if the sampling was not uniform, weaker types of asynchrony invariance might be induced during training, e.g. the node update only behaving as a monoid action at a subset of steps in the algorithm execution.

Note that computational cost of estimating the regularisation is proportional to the number of messages, nodes and arguments sampled at each training step. For instance, here, asynchrony invariance was learned by sampling a small fraction of nodes (≈ 0.2) and messages (≈ 0.1) at each training step, though alternative settings might prove more optimal for other problem types.

Algorithm 1 R_{L2} penalty computation

Require: Number of steps in algorithm execution T .

Require: Graph $G = (V, E)$ with nodes V and edges E.

Require: Node features $\mathbf{x}_u^{(t)}$ and messages $\mathbf{m}_{uv}^{(t)}$ at each point of execution.

1: $R_{L2} = 0$

- 2: for $t = 1$ to T do
- 3: Sample a set of nodes $S \subseteq V$ uniformly from V
- 4: for each node $u \in S$ do
- 5: Sample a set of neighbours $N'(u) \subset N(u)$
- 6: Aggregate messages from subset of neighbours:

$$
\mathbf{m}_u^{(t)} = \mathop{\oplus}_{v \in N'(u)} \mathbf{m}_{uv}^{(t)}
$$

7: Update node state with aggregated messages:

$$
\mathbf{x}_{u_1}^{(t)} = \phi\left(\mathbf{x}_u^{(t)}, \mathbf{m}_u^{(t)}\right)
$$

8: Set $\mathbf{x}_{u_2}^{(t)} = \mathbf{x}_u^{(t)}$

- 9: **for** each neighbour $v \in N'(u)$ **do**
- 10: Update partial node state with individual messages:

$$
\mathbf{x}_{u_2}^{(t)} = \phi\left(\mathbf{x}_{u_2}, \mathbf{m}_{uv}^{(t)}\right)
$$

11: end for

12: Update R_{L2} regularisation penalty:

$$
R_{L2} = R_{L2} + \left|\left|\mathbf{x}_{u_1}^{(t)} - \mathbf{x}_{u_2}^{(t)}\right|\right|_2
$$

13: end for

14: end for

15: return R_{L2}

E. Step-wise PCA

We replicated the step-wise PCA visualisations in Mirjanić et al. [\(2023\)](#page-7-15) to investigate how regularisation towards asynchrony invariance modifies the global execution trajectory of the node embeddings. Again, this is shown for non-regularised and regularised versions of L1-δ-sum (Figure [E.4\)](#page-3-1). In both cases, clearly defined computation steps can be observed. Yet, without regularisation, the distances travelled by the node embeddings at each step tend to decrease, exhibiting convergence towards a particular region of the space, in line with [Mirjanic et al.](#page-7-15) [\(2023\)](#page-7-15). On the other hand, with regularisation, the decrease in the distances travelled at each computation step is not as dramatic. Additionally, the clusters at each computation are more compact. Therefore, regularisation towards invariance appears to significantly change the structure of the processor's computations.

Figure E.4. Global step-wise PCA visualisations of the node embeddings for the architecture L1-δ-sum, as defined in Table [B.1](#page-4-1) with no regularisation (left) and $\lambda_{L2} = \lambda_{L3} = 0.5$ (right).