

# CoSMoEs: Compact Sparse Mixture of Experts

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## Abstract

Sparse Mixture of Expert (MoE) models are widely used foundation architectures at large scale, yet remain under-explored at smaller sizes. In this work, we introduce Compact Sparse Mixture of Experts (CoSMoEs) for on-device inference, addressing three key challenges: Quality, Memory, and Latency. On the quality front, we conduct a fair evaluation (removing confounding factors) and show that MoE architectures outperform dense models at on-device scale. We further propose weight-decomposed experts, which improve MoE performance beyond the standard formulation. On the memory and latency front, we address the large parameter count of MoE models by improving expert offloading efficiency through a novel training-time loss, reducing inference latency for on-device deployment.

## 1 Introduction

Mixture of Experts (MoEs) have become a popular extension of the transformer architecture (Vaswani et al., 2023). The core idea is that each token in the input sequence is routed through a set of sub-networks, or “experts”, whose outputs are combined via a gating mechanism that determines each expert’s contribution.

While all experts are activated in the most general MoE formulation (Jacobs et al., 1991; Jordan and Jacobs, 1993), *sparse* Mixture of Expert models select only a subset of experts per token (Cai et al., 2024), as used in Qwen (Bai et al., 2023; Yang et al., 2024), OLMoE (Muennighoff et al., 2024), Mixtral (Jiang et al., 2024), and DeepSeek (DeepSeek-AI, 2024). These large-scale MoE models are optimized for highly parallelized server-side inference.

In contrast, this work focuses on small-scale MoEs for edge devices, which face a distinct set of challenges around Quality, Memory, and Latency.

**Quality.** Unlike prior work (e.g., Jiang et al. (2024)), we establish a fair comparison between MoEs and dense models by aligning on active parameters (FLOP-aligned, *FA*) and total parameters (parameter-aligned, *PA*). We further reduce confounding factors by normalizing training data, recipes, and architectures wherever possible. Our evaluation shows that MoE architectures improve average language modeling performance by over 2.3% across model sizes. Building on these results, we propose a novel MoE extension following the intuition of “expert specialization,” yielding up to an additional 1.1% improvement.

**Memory and Latency.** As shown in Figure 1, models trained in server environments face additional constraints when deployed on edge devices. Despite these restrictions being largely architecture-independent, the high total parameter count of modern MoE models severely affect their ability to be deployed on the edge. While the sparsity property of MoE architectures can partially offset the high parameter count through expert offloading, this incurs a large (4-20 $\times$ ) inference latency increase (Xue et al., 2024). To relax this trade-off, we propose a “block-wise expert selection” loss that reduces expert offloads by 6 $\times$  and improves inference latency by 50%.

## 2 CoSMoEs Models

### 2.1 Sparse Mixture-of-Experts

At the core of this work is the sparse Mixture-of-Expert (MoE) architecture<sup>1</sup>, popularized by GShard (Lepikhin et al., 2020) and Switch Transformers (Fedus et al., 2022). While MoEs can be applied to different parts of the architecture, the most common approach replaces the dense feed-forward layer with a router component and multiple experts. By selecting a discrete subset of experts

<sup>1</sup>When referring to MoE throughout this paper, we assume sparsity.

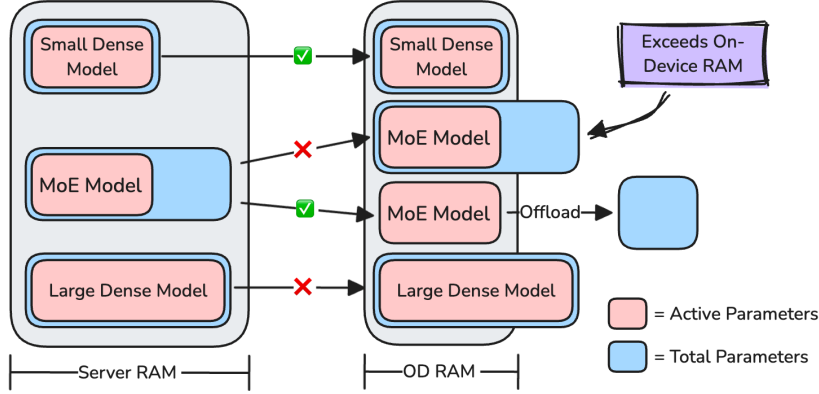


Figure 1: Server-side training environment (left) compared to the memory-constrained inference environment (right), showing deployment restrictions for parameter-heavy MoEs and large dense models on edge devices.

at each step, sparse MoE models are characterized by two quantities: their *active parameters* (FLOPs) and their *total parameters* (model size in memory). For expert selection, different routing paradigms have been proposed, either selecting experts per token (token choice) (Shazeer et al., 2017) or per expert (expert choice) (Zhou et al., 2022). We use token-choice routing, following the findings for text-only models in Muennighoff et al. (2024).

## 2.2 Weight-Decomposed Experts

Standard MoE models have a naturally large total parameter count because each expert maintains a full copy of the feed-forward layer weights. To reduce this overhead, we propose a lightweight expert formulation using matrix weight decomposition (“WD”), inspired by Low-Rank (“LoRA”) adapters (Hu et al., 2021). The key intuition is that each expert is intended to “specialize” on a subset of input tokens, so each expert need not have full representational capacity. We therefore replace the full expert matrices of shape  $n \times m$  with their decomposition into two smaller matrices of shape  $n \times r$  and  $r \times m$ :

$$M_{n \times m} \approx L_{n \times r} \times R_{r \times m} \quad (1)$$

with  $r \ll n$  and  $r \ll m$  (see also Figure 2). In preliminary experiments, we find that setting  $r$  to half the hidden dimension yields the best trade-off between parameter reduction and model performance. To ensure a parameter-aligned comparison, we adjust the number of heads and layers for weight-decomposed models (prefixed with *WD*), as detailed in Section 3.1.

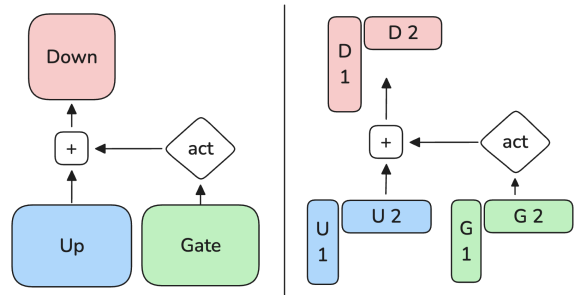


Figure 2: Feed Forward Layer: Standard (left) and Weight-Decomposed (right).

## 2.3 Block-wise Expert Selection

A central challenge for on-device MoE inference is expert offloading: when only active experts are kept in accelerator memory, each change in expert assignments requires transferring parameters between storage and compute, incurring substantial latency. Multiple lines of research have explored inference-time solutions such as predictive offloading and bitwidth adaptations (Yi et al., 2023; Eliseev and Mazur, 2023; Aminabadi et al., 2022). We approach this problem from a different angle: rather than optimizing the offloading mechanism, we reduce the number of offloading events by encouraging *temporally coherent* expert assignments during training.

To this end, we propose a “Block-wise Expert Selection” (BIES) loss term that penalizes frequent expert switches between consecutive tokens, closely related to the expert load balancing loss in Fedus et al. (2022):

Let  $R$  be a router logits tensor with shape  $(B, T, E)$ , where  $B$  is the batch dimension,  $T$  is the sequence length, and  $E$  is the expert dimension. We compute routing weights  $W$  by applying

the softmax function to  $R$ , scaled by a temperature parameter  $\tau$ :

$$W = \text{softmax}(\tau R) \quad (2)$$

In a non-differentiable step, we select the top- $k$  experts  $K$  for each token based on  $W$ . Let  $S$  be the selected experts tensor with shape  $(B, T, K)$ :

$$S = \text{top}_k(W, K) \quad (3)$$

We then count the hard expert replacements  $H$  by comparing consecutive tokens’ expert assignments:

$$H_e = \sum_{b=1}^B \sum_{t=1}^{T-1} |(S_{[b,t+1]} = e) - (S_{[b,t]} = e)| \quad (4)$$

$$H = \sum_{e=1}^E H_e$$

where  $e$  is the expert index and  $S_{[b,t]} = e$  equals 1 if expert  $e$  is among the top- $k$  candidates for token  $t$ . We normalize  $H$  by the batch size, top- $k$ , and number of tokens:

$$H_{norm} = \frac{\lfloor \frac{H}{2} \rfloor}{B \cdot K \cdot (T - 1)} \quad (5)$$

Since  $H$  is non-differentiable, we introduce a continuous surrogate  $L$  that sums the per-expert probability differences between consecutive tokens:

$$L = \sum_{b=1}^B \sum_{t=1}^{T-1} \sum_{e=1}^E |W_{b,t+1,e} - W_{b,t,e}| \quad (6)$$

$$L_{norm} = \frac{L}{B \cdot T}$$

The final loss is the product of the hard and soft expert selection terms:

$$\text{loss} = H_{norm} \cdot L_{norm} \quad (7)$$

To match the sequence-level computation of the BIES loss, we replace the standard (model-level) load balancing loss (Fedus et al., 2022) with **layer-wise load balancing** (following Lin et al. (2024)).

**Layer-wise load balancing.** An important design choice is to compute the load balancing loss *per layer* rather than aggregating across layers. When the loss is computed at the model level, it can be trivially minimized by consistently selecting a single expert per layer—for example, with 3

experts and 3 layers, always selecting expert 0 in layer 0, expert 1 in layer 1 and expert 2 in layer 2 achieves a perfect balance while also minimizing the BIES term. Figure 3 illustrates this failure mode with 3 layers and 3 experts.

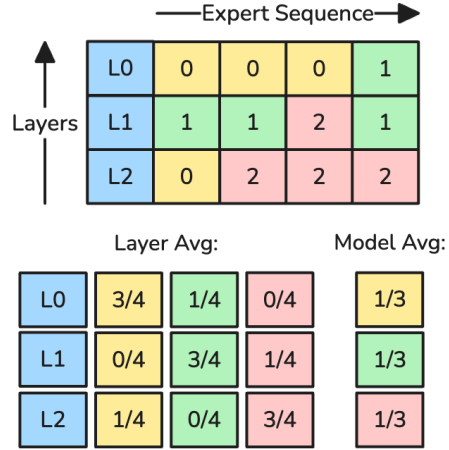


Figure 3: Example expert selection (for simplicity,  $k=1$ ) for individual layers and the complete model.

## 3 Experimental Setup

### 3.1 Model Configuration

We compare two on-device size categories: “Phone-sized” (1–3B parameters) and “Wearable-sized” (100–300M parameters), across three architectures: Dense, MoE, and WD MoE, as described in Table 1<sup>2</sup>. All models are based on the Llama3 architecture, with an MoE component consisting of eight total experts, two of which are active per token. We follow the standard expert implementation from the Hugging Face codebase (Wolf et al., 2020). Model hyperparameters are kept consistent while aligning on active and total parameters. When trade-offs are necessary, we favor depth over breadth, following Liu et al. (2024).

### 3.2 Training Details

We pre-train all models on the FineWeb Education dataset (FW-edu, Penedo et al. (2024)), a 1.4 trillion token text corpus provided by Hugging Face (Wolf et al., 2020). FW-edu represents a high-quality, general-purpose language dataset.

### 3.3 Evaluation Metrics

We evaluate along three axes. For **language modeling performance**, we use the EleutherAI

<sup>2</sup>The BIES extension uses the MoE architecture and is therefore not listed separately.

Model	Params	L	H	Hid	Seq	Steps	Bsz
<b>Phone-sized models, 1B-3B Parameters</b>							
Dense	1.50B	16	32	2048	2048	310k	2048
MoE	1.37B (3.75B)	24	18	1440	2048	310k	2048
+ WD	1.42B (3.65B)	26	20	1600	2048	310k	2048
Dense	3.61B	28	24	3072	2048	310k	2048
<b>Wearable-sized models, 100-300M Parameters</b>							
Dense	189M	19	8	512	2048	310k	2048
MoE	188M (377M)	19	8	432	2048	310k	2048
+ WD	188M (377M)	32	10	400	2048	310k	2048
Dense	380M	29	12	768	2048	310k	2048

Table 1: On-device model candidates. Params = #Active (#Total) Parameters, L = Layers, H = Self-Attention Heads, Hid = Hidden size, Seq = Sequence length, Bsz = effective batch size.

LM eval harness with nine benchmarks (Gao et al., 2024), following the evaluation protocols of Llama3 (Grattafiori et al., 2024) and MobileLLM (Liu et al., 2024). For **offloading efficiency**, we report the Expert Replacement Ratio (ExRep), which measures the percentage of realized expert replacements, and the optimal expert balance, which computes the average per-layer deviation from a uniform distribution. For **memory and latency**, we report per-token generation speed and peak memory usage.

## 4 Results

### 4.1 Language Modeling Performance

Table 2 presents our language modeling results. A random baseline is shown at the top, followed by our MoE results at phone and wearable scale, with public baselines at the bottom for context.

**Phone-sized models.** All MoE candidates outperform the random baseline by a large margin and consistently improve over the FA dense model by at least 2%. For MMLU and AGI-English, all models show only minor gains, indicating room for further improvement. On the remaining benchmarks, clear improvements are observed. Among MoE variants, the weight-decomposed model performs best overall. We observe a minor performance regression when using the BIES loss. Compared to the PA dense model, MoE candidates perform better on 3 out of 10 metrics, falling only half a percentage point short on average. In the context of previously published models, our MoE candidates outperform the FA Llama 3.2 1B and OLMoE models but do not reach the PA Llama 3.2 3B performance.

**Wearable-sized models.** The wearable-sized evaluation shows similar trends. The weight-decomposed model again achieves the best MoE

performance, here even surpassing the PA dense model. At wearable scale, at least one MoE model outperforms the PA dense model on 6 of 10 tasks. Compared to the published MobileLLM models, we observe improvements at both the 125M and 350M parameter scales. The BIES model again shows a slight performance drop relative to the standard MoE, consistent with the phone-scale findings.

### 4.2 Offload Efficiency

As illustrated in Figure 1, running MoE models on-device often requires offloading experts to stay within memory constraints, at the cost of significant latency increases. Since expert offloading frequency is data-dependent, we use a 100-sample subset of the C4 dataset (Raffel et al., 2020) as a proxy for general text data. Table 3 presents results along three dimensions: the expert replacement percentage (ExRep), the realized inference speed<sup>3</sup>, and expert balancing quality (deviation from uniform expert balance,  $\Delta$ Uniform). The BIES extension achieves over  $6\times$  fewer expert switches than the standard MoE model. This directly translates to a  $1.5\times$  improvement in generation speed, with only a minor regression of less than 1% relative in expert balancing<sup>4</sup>.

**Qualitative analysis.** Figure 4 visualizes expert assignments across 35 tokens for a single layer, comparing the BIES model (top) and the standard MoE model (bottom). The BIES model reduces expert replacements from 21 to 11 while preserving expert diversity—both models actively use 6 out of 8 experts. This illustrates how the BIES loss encourages temporally coherent expert assignments without collapsing to a single expert.

**Layer-wise expert balance.** To understand the per-layer impact of the BIES loss, Figure 5 plots the layer-wise expert balance for both models. With BIES, greater expert divergence is observed in lower layers, whereas the standard MoE model exhibits higher divergence in upper layers. Higher expert diversity in later layers appears preferable, given the general intuition that lower layers encode more local, syntactic information while upper layers capture more global, semantic structures.

<sup>3</sup>Full on-device benchmarks are presented in Section 4.3.

<sup>4</sup>Inference latency improvements are batch-size dependent.

Model	Params	MMLU	AGI-E	Arc-C	Arc-E	BoolQ	PIQA	SIQA	HellaS	OBQA	WinoG	Avg
<b>Random Baseline</b>												
Random	–	24.53	16.07	21.08	25.25	51.07	51.74	33.11	26.31	29.40	50.83	32.94
<b>Phone-sized models, ~1B-3B Parameters</b>												
Dense	1.50B	24.78	17.99	36.95	74.03	59.08	74.54	41.76	59.88	41.20	57.54	48.78
MoE	1.37B (3.75B)	<u>25.96</u>	17.65	42.58	76.77	60.89	75.52	42.12	65.07	42.40	<u>62.35</u>	51.13
+ BIES	1.37B (3.75B)	25.40	17.50	41.55	<u>77.02</u>	62.81	76.06	41.91	63.14	42.60	59.04	50.70
+ WD	1.42B (3.65B)	23.90	<b>18.20</b>	43.69	76.81	<b>66.76</b>	76.39	<b>45.14</b>	66.51	42.80	62.04	52.22
Dense	3.61B	<b>26.41</b>	16.82	<b>44.54</b>	<b>77.9</b>	65.87	<b>77.48</b>	43.3	<b>67.18</b>	<b>45.00</b>	<b>63.46</b>	<b>52.80</b>
<b>Wearable-sized models, ~100-200M Parameters</b>												
Dense	189M	22.9	16.82	23.29	56.82	57.09	64.15	37.82	36.36	32.8	50.99	39.90
MoE	188M (377M)	<b>25.27</b>	17.37	27.9	<u>63.09</u>	<u>58.39</u>	69.04	39.61	44.09	<b>34.4</b>	53.03	43.22
+ BIES	188M (377M)	24.27	17.58	24.83	58.84	<b>59.82</b>	66.49	38.64	39.70	33.40	49.96	41.35
+ WD	188M (377M)	23.64	17.16	<u>28.58</u>	62.58	57.13	<b>69.31</b>	<b>40.28</b>	<u>46.15</u>	33.20	<b>54.38</b>	<b>43.24</b>
Dense	380M	24.79	<b>17.86</b>	<b>28.92</b>	<b>64.35</b>	52.02	69.21	39.97	<b>46.53</b>	33.80	51.62	42.91
<b>Public Baselines across Model Sizes</b>												
MobLLM (2024)	135M	23.02	17.45	19.97	46.38	60.34	64.96	38.08	38.17	28.40	52.57	38.93
MobLLM (2024)	350M	26.33	17.47	23.89	56.4	61.96	68.88	39.87	49.57	31.00	57.38	43.28
Llama3.2 (2024)	1.4B	36.92	18.80	31.31	65.40	63.61	74.54	42.84	47.74	26.20	60.06	46.70
Llama3.2 (2024)	3.6B	54.01	22.53	42.32	74.41	72.81	76.71	47.13	55.32	31.20	69.30	54.50
OLMoE (2024)	1.68B (6.92B)	25.74	17.19	40.87	74.20	60.52	74.70	44.37	60.38	38.40	58.72	49.50

Table 2: Model comparison on zero-shot LM evaluations. Params = #Active (#Total) Parameters, BIES = Block-wise Expert Selection, WD = Weight-Decomposed, MobLLM = MobileLLM. Public baselines are evaluated using the EleutherAI LM eval harness (2024).

Model	ExRep (↓)	Tok/s Gen (↑)	ΔUni (↓)
MoE	43.82	15.02	<b>9.60</b>
+ BIES	<b>6.55</b>	<b>23.10</b>	9.67

Table 3: Impact of the BIES loss on expert replacement ratio (in percent), generation speed (tokens/second), and deviation from the uniform expert distribution (in percent). ↓ = lower is better, ↑ = higher is better.

### 4.3 On-Device Benchmarks

Since on-device models often execute in CPU-based environments or on proprietary accelerators, we compare model latency on both CPU and GPU<sup>5</sup>. As this paper targets training-time improvements, we use standard inference code from the Hugging Face Transformers library (Wolf et al., 2020) and the gpt-fast codebase (PyTorch Labs, 2023) without inference-specific optimizations such as EdgeMoE (Yi et al., 2023). Table 4 presents results along four dimensions: (1) language modeling performance (from Table 2), (2) inference speed across 128 tokens on CPU and GPU, (3) peak memory after 128 token generations, and (4) suitability for on-device deployment. In addition to the previously described model candidates, we evaluate standard MoE offloading as “Offl,” where only active experts are kept on the accelerator.

**Latency.** On CPU, the FA dense model achieves the highest generation rate; MoE candidates are slightly slower, and the PA dense model is 2× slower. On GPU, MoE models generally produce

fewer tokens per second than dense models, primarily due to their deeper architecture (Table 1). We also observe the 1.5× speed-up between standard offloaded MoE models and BIES offloaded models, consistent with Table 3. For context, inference-based offloading strategies such as those in Eliseev and Mazur (2023) and Aminabadi et al. (2022) achieve 2–3× and 5.5× latency reductions, respectively, at comparable model sizes. However, unlike our training-time approach, inference-time offloading methods often require additional modeling components to predict future expert usage, which can be impractical for on-device scenarios.

**Peak Memory.** Without expert offloading, the peak generation memory of MoE candidates is comparable to the PA dense model. With offloading, peak memory drops to the level of the FA dense model, since only active parameters are kept in memory. This makes offloaded MoE models viable on-device candidates (see ✓).

### 4.4 Ablation Studies

Going beyond the standard MoE setup with two active and eight total experts, we ablate these dimensions and explore their impact on quality, latency, and memory. We evaluate eight model configurations spanning a range of active and total expert counts. Figure 6 summarizes findings along the active expert (left) and total expert (right) dimensions. For active expert ablations, we fix the total number of experts to 8; for total expert ablations, we fix the number of active experts to 2.

<sup>5</sup>Evaluations are performed in a server environment; actual on-device accelerator numbers may vary.

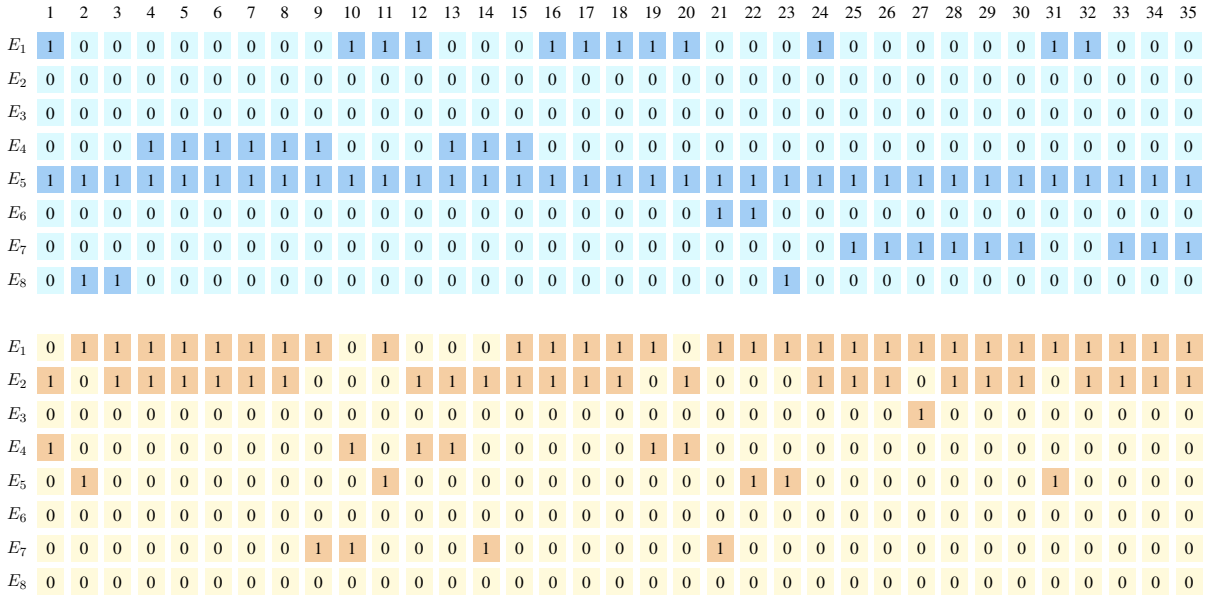


Figure 4: Expert assignments across 35 tokens for a single layer. 1 = Active, 0 = Inactive. Top: BIES (11 replacements), Bottom: Standard MoE (21 replacements).

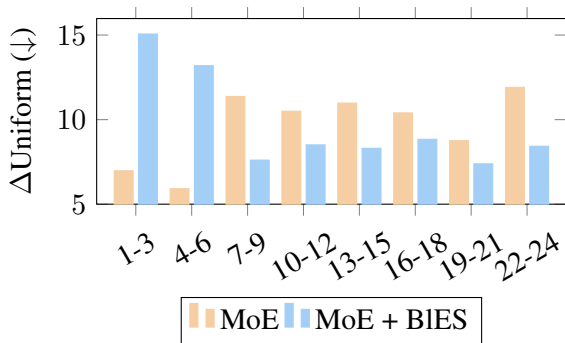


Figure 5: Per-layer divergence of expert routing from the uniform distribution. Large values indicate expert collapse toward a pseudo-dense layer.

**Active Expert Ablation.** Increasing the number of active experts improves model quality, though returns diminish as the count approaches 8. Generation speed decreases linearly with the number of active experts, while peak memory remains constant<sup>6</sup>.

**Total Expert Ablation.** Model quality increases roughly linearly with the number of total experts, though the improvement is less pronounced than for active experts. Generation speed is unaffected since FLOPs are held constant. However, total expert count significantly impacts peak memory<sup>7</sup>.

In summary, increasing either active or total ex-

<sup>6</sup>Peak memory would increase if experts were actively offloaded.

<sup>7</sup>Peak memory would remain constant with active offloading, at the cost of reduced generation speed.

Model	LM Eval	Latency		Mem	
Setup	Avg	Gen (tok/sec)		Gen	
Metric	%	CPU	GPU	GB	☑ / ☒
Dense	48.78	4.47	73.10	5.8	☑
MoE	51.13	4.30	40.60	14.7	☒
+ WD	52.22	3.85	33.50	14.2	☒
+ Offl	51.13	4.30	15.02	5.4	☑
+ BIES	50.70	4.30	23.10	5.4	☑
Dense	52.80	1.77	42.60	14.0	☒

Table 4: On-device benchmarks. Gen = Generation of 128 tokens (1 token prefill), Offl = Offloaded, BIES = Block-wise Expert Selection. Mem = Peak GPU memory. ☑ = Phone-sized, assuming <6GB of RAM use (e.g., iPhone 12 Pro).

perts improves model quality but requires a trade-off in either latency or memory.

#### 4.5 Training Efficiency

Figure 7 compares the training dynamics of MoE and dense model candidates, aligned by dataset, training steps, and hyperparameters. We track average language modeling performance at checkpoints from 10k to the full 310k steps.

Comparing the FA and PA dense models with our best-performing MoE model, we corroborate the findings of Lin et al. (2024), observing a 5–10× training efficiency gain for MoE models over their FA dense counterparts. Our MoE candidate reaches the best performance of the 1.4B dense model at around 35k steps, while the larger 3.6B dense model achieves generally higher scores throughout training.

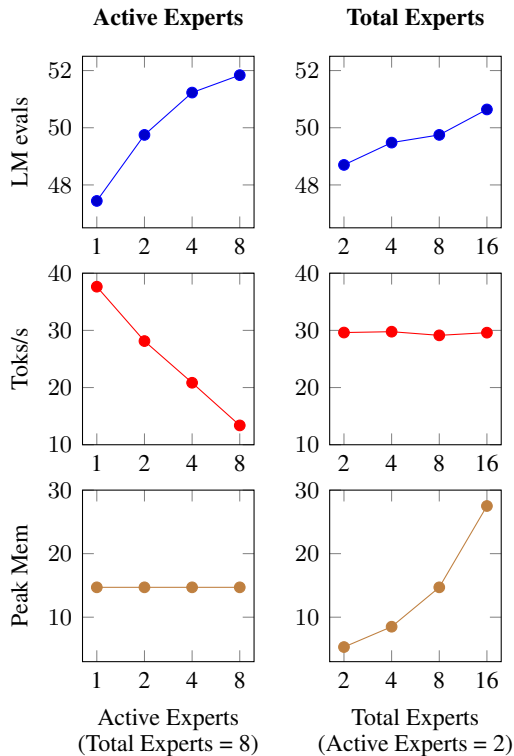


Figure 6: Active (left) and total (right) expert ablations of the 1.4B MoE model at 50k steps (~210B tokens).

## 5 Related Work

**Small-Scale Language Models.** As foundation models become increasingly expensive to train and deploy, two research directions have emerged. The first focuses on improving small-scale architectures, including MobileLLM (Liu et al., 2024), MobiLlama (Thawakar et al., 2024), and the BabyLlama series (Timiryasov and Tastet, 2023; Tastet and Timiryasov, 2024). The second targets training data quality through cleaner, more curated pipelines, as demonstrated by the Microsoft Phi series (Abdin et al., 2024) and Hugging Face efforts (Ben Allal et al., 2024; Lozhkov et al., 2024). For a comprehensive survey, see Nguyen et al. (2024).

**Sparse Mixture of Experts.** Sparse MoEs span a wide range of model sizes: Qwen (2023; 2024) and OLMoE (2024) at 1–3B active parameters, Mixtral (2024) and DeepSeek (2024) at around 7B, and DBRX (Databricks, 2023) and Grok-1 (x.ai, 2023) at 36B and 86B active parameters, respectively. The OLMoE paper (Muennighoff et al., 2024) is particularly relevant, presenting training insights and design decisions for MoE models at smaller scales; we follow many of its findings. Comparing the inner workings of large MoE models, Lo

et al. (2024) analyze Mixtral, Grok, and DeepSeek, finding initial similarities despite different training paradigms. We pursue similar comparisons but prioritize fairness by controlling confounding factors. For a detailed MoE survey, see Cai et al. (2024).

### Weight Decomposition for Mixture of Experts.

Along similar lines to our weight-decomposed experts, Dou et al. (2024) proposed a LoRA-style extension that converts dense networks into MoE models during supervised fine-tuning (SFT). By freezing the dense backbone and introducing a router at the SFT stage, their approach aims to reduce catastrophic forgetting of pre-training knowledge. In contrast, we apply weight decomposition directly during pre-training, training more parameter-efficient experts from the start.

**Inference Efficiency.** Our BIES loss is complementary to prior work on inference-time offloading optimization. Xue et al. (2024) improve expert pre-fetching and caching to reduce parameter transfers. EdgeMoE (Yi et al., 2023) enhances offloading through predictive strategies and bitwidth adaptations. Other frameworks include Mixtral Fast Inference (Eliseev and Mazur, 2023) and DeepSpeed Efficient Inference (Aminabadi et al., 2022). Unlike these approaches, our method reduces the number of offloading events during training rather than optimizing the offloading mechanism at inference time, making the two approaches orthogonal and potentially complementary.

## 6 Conclusion

We demonstrate how to enable sparse MoE architectures for on-device inference along three key dimensions: Quality, Memory, and Latency. On the quality front, a fair comparison shows that MoE models outperform their dense counterparts on language modeling tasks by over 2.35%. Our weight-decomposed experts yield further gains of up to 1.1% over standard MoE models. To make MoE models practical for on-device deployment, we address the offloading bottleneck by reducing expert switches during training. Our block-wise expert selection loss improves offloading efficiency by 6× and increases generation speed by 50% compared to standard offloaded MoE models. These results pave the way for deploying MoE architectures in on-device scenarios, supporting high-quality, privacy-preserving foundation models for edge devices.

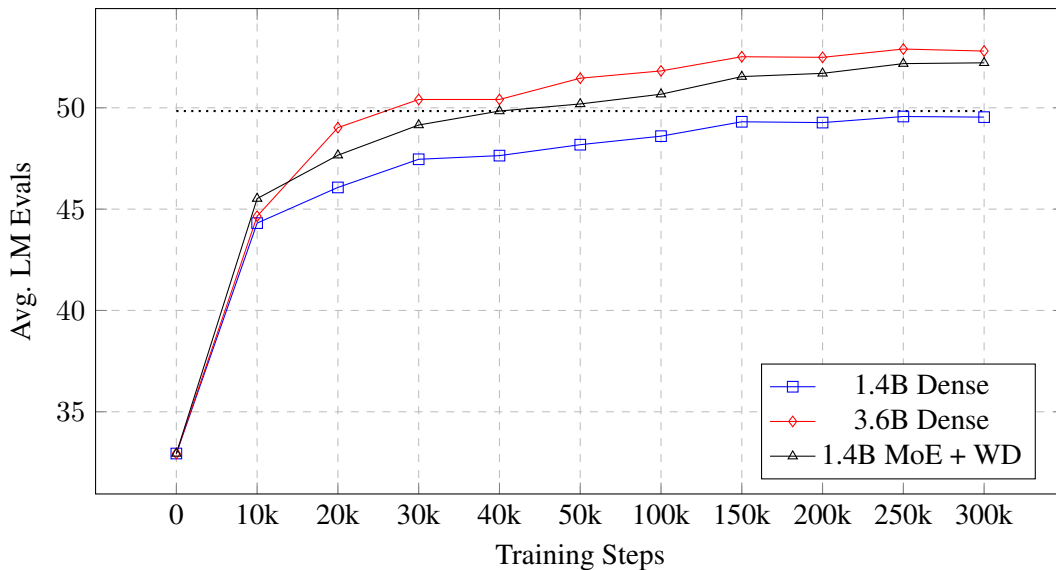


Figure 7: Training dynamics across model candidates. The dotted line marks the best 1.4B Dense checkpoint performance, reached by the MoE + WD model at  $\sim 35$ k steps.

## Limitations

We note two limitations of this work.

First, while we aim for a fair comparison between MoE and dense models, a perfectly balanced comparison remains elusive. Aligning active and total parameters necessarily introduces variation in model depth and width, which affects certain metrics (e.g., latency depends heavily on layer count). Additionally, keeping hyperparameters aligned may inadvertently favor one architecture. Thorough hyperparameter sweeps would help address this but would introduce their own confounding factors.

Second, our evaluations and benchmarks are performed on server hardware, which introduces a level of abstraction from actual edge devices. Although we aim to present a complete picture of MoE models for on-device use, this gap between server and edge environments is a limitation of our current evaluation.

## References

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