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System Demo for Transfer Learning from Vision to Language using Domain Specific CNN Accelerator for On-Device NLP Applications

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Abstract

Power-efficient CNN Domain Specific Accelerator (CNN-DSA) chips are currently available for wide use in mobile devices. These chips are mainly used in computer vision applications. 015 However, the recent work of Super Characters method for text classification and sentiment anal-018 ysis tasks using two-dimensional CNN models has also achieved state-of-the-art results through 020 the method of transfer learning from vision to text. In this paper, we implemented the text classification and sentiment analysis applications on mobile devices using CNN-DSA chips. Compact network representations using one-bit and 025 three-bits precision for coefficients and five-bits for activations are used in the CNN-DSA chip with power consumption less than 300mW. For edge devices under memory and compute constraints, the network is further compressed by approximating the external Fully Connected (FC) 030 layers within the CNN-DSA chip. At the workshop, we have two system demonstrations for NLP tasks. The first demo classifies the input English Wikipedia sentence into one of the 14 on-034 035 tologies. The second demo classifies the Chinese online-shopping review into positive or negative.

1. Introduction

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Power-efficient CNN Domain Specific Accelerator (CNN-DSA) chips are currently available for wide use. Sun et al. (Sun et al., 2018c;a) designed a two-dimensional CNN-DSA accelerator which achieved a power consumption of less than 300mW and an ultra power-efficiency of 9.3TOPS/Watt. All the processing is in internal memory instead of external DRAM. Demos on mobile and embedded systems show its applications in real-world implementation.



Figure 1. Efficient On-Device Natural Language Processing system demonstration. The CNN-DSA chip is connected to Raspberry Pi through the USB interface. Keyboard sends the typing text input to Raspberry Pi through USB. A monitor is connected to Raspberry Pi through HDMI for display. On the monitor, it shows the introduction for the demo (zoom in to see details). There are two demos. The first demo classifies the input English Wikipedia sentence into one of the 14 ontologies. The second demo classifies the Chinese online-shopping review into positive or negative.

tations. The 28nm CNN-DSA accelerator attains a 140fps for 224x224 RGB image inputs at an accuracy comparable to that of the VGG (Simonyan & Zisserman, 2014).

For Natural Language Processing tasks, RNN and LSTM models (Tang et al., 2015; Lai et al., 2015) are widely used, which are different network architectures from the twodimensional CNN. However, the recent work of Super Characters method (Sun et al., 2018b; 2019b) using twodimensional word embedding achieved state-of-the-art result in text classification and sentiment analysis tasks, showcasing the promise of this new approach. The Super Characters method is a two-step method. In the first step, the characters of the input text are drawn onto a blank image, so that an image of the text is generated with each of its characters embedded by the pixel values in the two-dimensional space. The resulting image is called the Super Characters image. In the second step, the generated Super Characters image is fed into a twodimensional CNN models for classification. The two-

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Figure 2. Data flow for the system demonstration of efficient on-device NLP using Super Characters method and CNN-DSA chip.

dimensional CNN models are trained for the text classification task through the method of Transfer Learning, which
finetunes the pretrained models on large image dataset, e.g.
ImageNet (Deng et al., 2009), with the labeled Super Characters images for the text classification task.

In this paper, we implemented NLP applications on mobile
devices using the Super Characters method on a CNN-DSA
chip as shown in Figure 1. It takes arbitrary text input from
keyboard connecting to a mobile device (e.g. Raspberry Pi).
And then the text is pre-processed into a Super Characters
image and sent to the CNN-DSA chip to classify. After
post-processing at the mobile device, the final result will
be displayed on the monitor.

085 2. System Design and Data Flow

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As shown in Figure 2, the keyboard text input is pre-processed by the Raspberry Pi (or other mobile/embedded devices) to convert into a Super Characters image. This pre-processing is only a memory-write operation, which requires negligible computation and memory resources.

092 The Super Characters method works well for Asian lan-093 guages which has characters in squared shapes, such as 094 Chinese, Japanese, and Korean. These glyphs are easier for 095 CNN models to recognize than Latin languages such as En-096 glish, which is alphabets-based in a rectangular shape and 097 may have to break the words at line-changing. To improve 098 the performance for English, a method of Squared English 099 Word (SEW) is proposed to cast English word in a squared 100 shape as a glyph (Sun et al., 2019a). Figure 3 shows an example of this method. Basically, each word takes the same size of a square space lxl. Words with longer alphabets will have smaller space for each alphabet. Within the lxl104 space, the word with N alphabets will have each of its al-105 pha in the square area of $\{l/ceil[sqrt(N)]\}^2$, where sqrt(.)106 stands for square root, and *ceil*[.] is rounding to the top.

The CNN-DSA chip receives the Super Characters im-

Th	tig er	i s	t h e	lar ges	spe cl e s
amo ng	t h e	Fel i da e	an	cl as si fi ed	i n
t h e	gen ds	Pan the ra.	Ιt	i s	mp st
reco gniz abie	f o r	it s	da r k	ver tic al	str ipe s
on	redd sh- ge	f u r	Ψĥ	а	hi g
unde rsid e.					

Figure 3. An example for Squared English Word (SEW) method. The two-dimensional embedding in this image corresponds to the text of "The tiger is the largest species among the Felidae and classified in the genus Panthera. It is most recognizable for its dark vertical stripes on reddish-orange fur with a lighter underside."

age through the USB connection to the mobile device. It outputs the classification scores for the 14 classes in the Wikipedia text classification demo. The classification scores mean the probabilities for classification but before softmax. The mobile device only calculate the argmax to display final classification result on the monitor, which is also negligible computations. The CNN-DSA chip completes the complex CNN computations with low power less than 300mw.

3. Compact Network Representations for Efficient Inference

3.1. Approximating FC layers for On-Device Applications under Memory and Computation Constraints

The CNN-DSA chip is a fast and low-power coprocessor. However, it does not directly support inner-product operations of the FC layers. It only supports 3x3 convolution, Relu, and max pooling. If the FC layers are executed on the mobile device, there will be increased requirements for
memory, computation, and storage for the FC coefficients.
And it will also spend more interface time with the CNNDSA chip for transmitting the activation map from the chip,
and also cost relative high power consumption for the mobile device to execute the inner-product operations.

In order to address this problem, we proposed the GnetFC model, which approximates the FC layers using multiple layers of 3x3 convolutions. This is done by adding a sixth major layer with three sub-layers as shown in Figure 4. The

224x224 input						
224x224 input	Conv 1-1 (64ch, 3-bits, padding zeros) Conv 1-2 (64ch, 3-bits, padding zeros) . 1 st Major Layer					
	✓ 2x2 max pooling					
112x112 input	Conv 2-1 (128ch, 3-bits, padding zeros)					
i i i i i i i i i i i i i i i i i i i	Conv 2-2 (128ch, 3-bits, padding zeros) 2 nd Major Layer					
	✓ 2x2 max pooling					
56x56 input	Conv 3-1 (256ch, 1-bit, padding zeros)					
	Conv 3-2 (256ch, 1-bit, padding zeros)					
1	Conv 3-3 (256ch, 1-bit, padding zeros)					
v 2x2 max pooling						
28x28 input	Conv 4-1 (512ch, 1-bit, padding zeros)					
LoxLo mpat	Conv 4-2 (512ch, 1-bit, padding zeros) 4 th Major Layer					
	Conv 4-3 (512ch, 1-bit, padding zeros)					
	✓ 2x2 max pooling					
14x14 input	Conv 5-1 (256ch, 1-bit, padding zeros)					
14X14 mput	Conv 5-2 (256ch, 1-bit, padding zeros) 5 th Major Layer					
	Conv 5-3 (256ch, 1-bit, padding zeros)					
v 2x2 max pooling						
7x7 input	Conv 6-1 (14ch, 1-bit, No padding)					
5x5 input	Conv 6-2 (14ch, 1-bit, No padding) 6 th Major Layer					
3x3 input	Conv 6-3 (14ch, 1-bit, No padding)					
1x1 scalar	14x1x1 output					

Figure 4. Model architecture. The input is of size 224x224 with multiple channels, and the output is of size 14x1x1. The architecture within blue dashed square is the model loaded into the CNN-DSA chip.

model is similar to VGG architecture except that it has six major layers, and the channels in the fifth major layer is reduced to 256 from the original 512 in order to save memory for the sixth layer due to the limitation of the on-chip memory. The sub-layers in each major layer has the same color. Each sub-layer name is followed by the detailed information in brackets, indicating the number of channels, bits-precision, and padding. The first five major layers has zero paddings at the image edge by one-pixel. But the sixth major layer has no padding for the three sublayers, which reduces the activation map from 7x7 through 5x5 and 3x3 and finally to 1x1. The output is of size 14x1x1, which is equal to an array of 14 scalars. The final classification result can be simply obtained by an argmax operation on the 14 scalars. This reduces the system memory footprint on the mobile device and accelerate the inference speed. 164

3.2. Low-precision Inference in the Chip

The memory of the CNN-DSA chip is built within the accelerator, so it is very power-efficient without wasting the energy for moving the bits from external DDR into internal SRAM. Thus the on-chip memory is very limited, which supports maximum 9MB for coefficients and activation map. As shown in Figure 4, the first two major layers uses 3-bits precision and the other four major layers uses 1-bit precision. All activations are presented by 5-bits in order to save on-chip data memory. The representation mechanism inside the accelerator supports up to four times compression with the 1-bit precision, and two times compression with the 3-bits precision. Due to the high compression rate, the convolutional layers in VGG16 with 58.9MB coefficients in floating precision could be compressed into only about 5.5MB within the chip. This is a more than 10x compression of the convolution layers. This compact representation has been proved to be successful on ImageNet (Deng et al., 2009) standard training and testing data and achieved the same level of accuracy as floating point models with 71% Top1 accuracy. The compact CNN representation without accuracy loss is because of the redundancy in the original network.

To efficiently use the on-chip memory, the model coefficients from the third major layers are only using 1-bit precision. For the first two major layers, 3-bits model coefficients are used as fine-grained filters from the original input image. And the cost on memory is only a quarter for the first major layer and a half for the second major layer if using the same 3-bits precision.

The total model size is 2.8MB, which is more than 200x compression from the original VGG model with FC layers. It completes all the convolution and FC processing within the CNN-DSA chip for the classification task with little accuracy drop. The GnetFC model on the CNN-DSA chip on the Wikipedia demo obtains an accuracy of 97.4%, while the number for the original VGG model is 97.6%. The accuracy drop is mainly brought by the approximation in GnetFC model, and also partially because of the bit-precision compression. The accuracy drop is very little, but the savings on power consumption and increasing on the inference speed is significant. It consumes less than 300mw on the CNN-DSA chip, and the power for pre/postprocessing is negligible. The CNN-DSA chip processing time is 15ms, and the pre-processing time on mobile device is about 6ms. The time for post-processing is negligible, so the total text classification time is 21ms. It can process nearly 50 sentences in one second, which satisfies more than real-time requirement for NLP applications.

4. Conclusion

166 We implemented efficient on-device NLP applications 167 on a 300mw CNN-DSA chip by employing the two-168 dimensional embedding used in the Super Characters 169 method. The two-dimensional embedding converts text 170 into images, which is ready to be fed into CNN-DSA chip 171 for two-dimensional CNN computation. The demonstra-172 tion system minimizes the power consumption of deep neu-173 ral networks for text classification, with less than 0.2% ac-174 curacy drop from the original VGG model. The potential 175 use cases for this demo system could be the intension recog-176 nition in a local-processing smart speaker or Chatbot.

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